EXPERIMENT 7

Call, Jumps and Loop Instructions

Objective

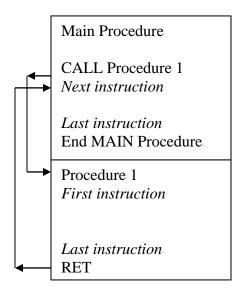
- Understand working of Procedure (CALL instruction) and Stack
- Understand difference between Conditional and Unconditional jump
- Implement LOOP instruction

Theory

The call and jump instructions transfer the flow of the program. The main difference between call and jump instruction is the jump instruction jumps to target and proceed further instructions, whereas call instruction jump to target (called subroutine or procedure) execute the lines until reach to the RET instruction, then return back to the next line from where its called.

CALL and **RET** instruction

The Subroutines (procedures) in 8086/88 assembly language are design by using CALL and RET instruction. The CALL instruction use to call the procedure and RET instruction jump back the flow to the next line from where it is call.



• When procedure is call the processor first store (PUSH) the address of next line on the stack and jump to the procedure. The procedure execute until RET (return) instruction execute; then processor get back (POP) the address from stack (the address of next line which is store when procedure is call) and jump to the next line of the CALL instruction.



Example: In the following program a procedure of display string function is made by the name of DISPLAY.

MOV AX, @DATA

MOV DS, AX

LEA DX, MESSAGE1

CALL DISPLAY

LEA DX, MESSAGE2

CALL DISPLAY

LEA DX, MESSAGE3

CALL DISPLAY

JMP END PROG

DISPLAY: MOV AH, 09H ; Display function subroutine

INT 21H

RET

END PROG:

Jump instruction

Unlikely the CALL instruction jump instruction has same working except that the jump instruction not return. The Jumps instruction can distinguish into two types:

- 1) Unconditional Jump
- 2) Conditional Jump

1) Unconditional Jump

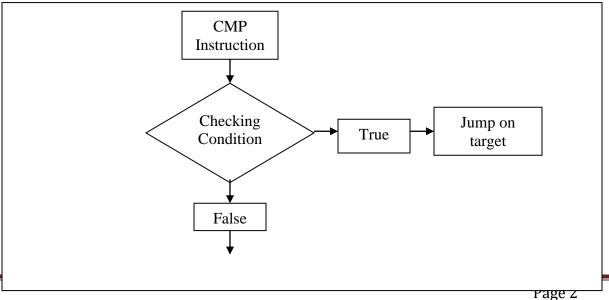
The unconditional jump transfers the control of program without checking any condition. The unconditional jump can go any where in the program and has no limitation.

Unconditional Jump Format

JMP Target ; The target is the label

2) Conditional Jump

The conditional jump transfers the controls of program by first watching the condition. The conditional jump depend on the value of status flag. If condition is true then it jump on target else it goes on the next line. Also all unconditional jumps are short jump means the target must present with in range of -128 byte to +127 byte.





Next Line

The working of conditional jump

Conditional jump can be categorized into three types:

1) **The Signed Jump**

2) <u>(a=<2)</u>		
JG/JNLE	Target; Jump if Greater /	(ZF = 0 & SF = OF)
	; Jump if Not Less or Equal	
JGE/JNL	Target; Jump if Greater or Equal /	(SF = OF)
	; Jump if Not Less	
JL/JNGE	Target; Jump if Less /	(SF <> OF)
	; Jump if Not Greater or Equal	
JLE/JNG	Target; Jump if Less or Equal /	(ZF = 0 & SF <> OF)

; Jump if Not Greater

2) The Unsigned Jump

c chingined bu	<u>mp</u>	
JA / JNBE	Target; Jump if Above /	(CF = 0 & ZF = 0)
	; Jump if Not Below or Equal	
JAE/JNB	<i>Target</i> ; Jump if Above or Equal /	(CF = 0)
	; Jump if Not Below	
JB/JNAE	Target; Jump if Below /	(CF = 1)
	; Jump if Not Above or Equal	
JBE/JNA	Target; Jump if Below or Equal /	(CF = 1 or ZF = 1)
	: Jump if Not Above	

3) The Single Flag Jump

JZ/JE	Target; Jump if Zero/ Jump if Equal	$(\mathbf{ZF} = 1)$
JNZ/JNE	Target; Jump if Not Zero/ Jump if Not Equal	(ZF = 0)
JC	Target; Jump if Carry	(CF = 1)
JNC	Target; Jump if No Carry	(CF = 0)
JP/JPE	Target; Jump if Parity/ Jump if Parity Even	(PF = 1)
JNP /JPO	Target; Jump if No Parity/ Jump if Parity Odd	(PF = 0)
JO	Target; Jump if Overflow	(OF = 1)
JNO	Target; Jump if No Overflow	(OF = 0)
JS	Target; Jump if negativeSign	(SF = 1)
JNS	Target; Jump if No negativeSign	(SF = 0)

The difference between Signed jump and Unsigned jump is only status of Sign Flag (SF). The Signed jump watch the status of Signed flag with other flag whereas the Unsigned jump does not watch signed flag. The Single Flag jump only watch the status of the desire flag only.



The Signed Jump example:

CMP designation, source

JG Target ; Jump if greater

The Unsigned Jump example:

CMP designation, source

JA Target ; Jump if above

The above instructions having same working that is jump if destination is greater than source but the Signed jump see SF(signed flag), CF(carry flag) and ZF(

flag) where as Unsigned jump see only CF(carry flag) and ZF(zero flag).

The Single flag jump

CMP designation, source

JZ Target ; Jump if designation = source

and

JC Target ; Jump if carry JP Target ; Jump if parity

Example:

1) The following program takes decimal number from keyboard and if user presses button other then any decimal number then it take again.

AGAIN: MOV AH, 01H ; Take input function

INT 21H CMP AL, '0'

JB AGAIN ; Jump if below than 0

CMP AL, '9'

JA AGAIN ; Jump if greater than 9

2) The following program takes only + sign and - sign from keyboard and jump on that function, and if user presses any other button then it take again.

AGAIN: MOV AH, 01H ; Take input function

INT 21H

CMP AL, '+'; Compare with + sign

JZ ADDITION; If + sign enter then jump to ADDITION

CMP AL, '-'; Compare with - sign

JE SUBTRACTION; If - sign enter then jump to SUBTRACTION

JMP AGAIN ; Jump to again if all condition were false

The CMP Instruction

In the above examples the unconditional jump depend on the values present in the Status Flag, but how the flag is affect to meet condition. This is done by CMP instruction, the CMP instruction compare the destination with source by subtracting source value from destination value, the result is not store in operand but according to result the bits of status flag register affected. The CMP



Instruction is used to compare Byte or Word, to check status of Bit(s) see TEST instruction. (Note that the CMP is instruction is same as SUB instruction except the answer is not store in the designation in CMP instruction).

The TEST Instruction

The TEST instruction perform logical AND operation to affect the flags but result does not store. The TEST instruction is used to check specific bit(s) are set or clear.

Example:

The only difference between upper case character ASCII and lower case character ASCII is of bit 5 which is '1' in lower case character ASCII and '0' in upper case character ASCII. The following program check the ASCII present in AL register, if it is upper case the program display 'U' character on screen and if it is lower case then display 'L'.

TEST AL,00100000B
JE LOWER
MOV DL,'U'
MOV AH,02H
INT 21H
JMP END_PROG
LOWER:
MOV DL,'L'
MOV AH,02H
INT 21H
END_PROG:

The LOOP Instruction

The LOOP instruction repeats the sequence of instructions. The LOOP instruction jumps on target to the number of times the value present in CX register.

The LOOP instruction execute in following sequence.

- 1. Decrease CX register.(5) -4,3,2,1
- 2. Compare CX with 0.
- 3. If CX = 0 then goto next line else jump on Target.

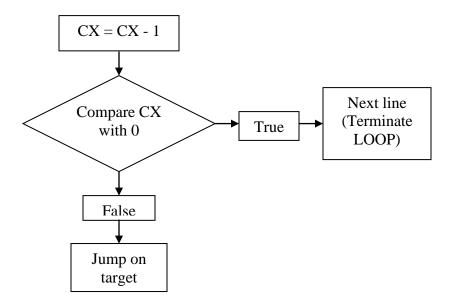
LOOP Instruction format:

LOOP Target; The target should be written before the LOOP instruction

Example:

MOV CX, 05H MOV AL, 00H MOV BL, 09H HERE: INC AL DEC BL LOOP HERE

After completion of the program values present in register are Al = 05H, = BL = 04H and CX = 00H



The LOOP instruction flow

Exercise

1) Write a password program which take three character and display '*' on screen. Then compare the input character with the store character and display "Correct Password" if password correctly enter else print "Incorrect password" on screen.



```
92 ; You may customize this and other start-up templates; 93 ; The location of this template is c:\emu8086\inc\0_com_template.txt
04
05 org 100h
06
07 ; add your code here
08
     .data
09
10 A db 'password is correct $'
11 B db 'password is not correct $'
12 C db 'Enter your Password : $'
13 MESSAGE db Oah , Odh , 'your input were : $'
15
     .code
16 main :
mov ax, edata
mov ds, ax
19
mov dx,offset C
21 mov ah,09h
22 int 21h
23
24 mov ah,01h
    mov ah,01h int 21h int 21h int 21h
25
26
27
28
29 mov dx,'ab'
30 mov bx,'c'
31 lea dx,bx
32
     mov cx,dx
    cmp al,cl
35
36
    je L1
38
39 mov dl,10
40 mov ah, 2
41 int 21h
42
43
    mov dl,13
44 mov ah, 2
45 int 21h
46
47 mov dx, offset B
48 jmp L
49
50
51
52
53
54 L1:
55 mov dl,16
56 mov ah,2
57 int 21h
    mov dl,10
57
58
     mov dl,13
60 mov ah, 2
61 int 21h
62
63 mov dx,offset A
64 jmp L
```



```
67
  68 L:
  69
     mov ah,09h
  70
71
      int 21h
      mov dx,offset MESSAGE
  73
74
      mov ah,9
int 21h
  75
  76
77
      mov dl,al
      add d1,6
      mov ah,02h
int 21h
  78
  80
      mov dl,al
mov ah,02h
int 21h
  81
  82
  83
  84
      mov dl,al
mov ah,02h
int 21h
  85
  86
  87
  88
  89
     ret
                                             Ů
                                Ê
                                                           4Ι
                                                                                    mov
     ah,2
                                            reload
                                                       step back
                                                                    single step
                                                                                    run
                                                                                              step delay ma
                               Load
int 21h
mov dx, offset A
                              registers
                                                      F400:0154
                                                                                        F400:0154
                                    Н
                                        L
jmp L
                                  02
                                       2A
                                               F4150: FF 255 RES
                                                                                 BIOS DI
                              \Delta X
                                                                             •
                                               F4151: FF 255 RES
                                                                                 INT 020h
                                   00
                                       63
                                               F4152: CD 205
                               BX
L:
                                                                                 ADD LBX +
                                                F4153: 20 032 SPA
                                                                                              SI], AL
mov ah,09h
                                                                                 ADD [BX + SI], AL
ADD [BX + SI], AL
                                               F4154: CF 207
F4155: 00 000
                                   00
                                       63
                               \propto
int 21h
                                                                NULL
                                   |01 ||2A
                               \mathsf{DX}
                                                F4156: 00 000 NULL
                                                                                  ADD [BX + SI], AL
mov dx.offset MESSA(
                                                                                  ADD [BX + SI], AL
                                                F4157:
                                                        00
                                                           000
                                                                NULL
mov ah,9
int 21h
                               CS
                                    F400
                                               F4158:
                                                                                  ADD BH, BH
                                                                NULL
                                                        000 000
                                                F4159:
                                                        000 000
                                                                NULL
                                                                                  DEC BP
                               IΡ
                                    0154
                                                F415A:
                                                        00
                                                           000
                                                                NULL
                                                                                  SBB CL.
                                                                                           BH
mov dl.al
                                                F415B:
                                                                                      [BX + SI], AL
                                                                NULL
                                                        000 000
                                                                                  ADD
                               SS
add d1,6
                                    0700
                                                F415C:
                                                        000 000
                                                                NULL
                                                                                  ADD
                                                                                      [BX + SI], AL
mov ah, 02h
                                                                                 ADD [BX + SI], AL
ADD [BX + SI], AL
                                                F415D:
                                                        00
                                                           000
                                                                NULL
                               SP
                                    FFFA
int 21h
                                                F415E:
                                                        000 000
                                                                NULL
                                                F415F:
                                                        000 000
                                                                NULL
                                                                                  ADD
                                                                                      [BX + SI], AL
                               ΒP
                                    0000
mov dl,al
mov ah,02h
int 21h
                                                F4160:
                                                        FF
                                                            255
                                                                 RES
                                                                                  ADD BH, BH
                                                F4161: FF
                                                           255
                                                                                 DEC BP
                                                                RES
                               SI
                                    0000
                                                                                 ADD BH, CL
ADD [BX + SI], AL
                                                F4162: CD 205
                               DΙ
                                    0000
                                                F4163:
                                                        1A 026
mov dl,al
mov ah,02h
int 21h
                                               F4164: CF 207
                                    0700
                               DS
                                    0700
                               ES
                                                screen
                                                        source
                                                                 reset
                                                                          aux
                                                                                 vars
                                                                                         debug
                                                                                                 stack
                                                                                                     508 emulator screen (80x25 chars)
Enter your Password: weg
password is not correct
your input were: ***
```



2) Write a program which ask user to enter a digit (from 1 to 9 only) and then print the table of that number.

The program is look like this

Please enter a digit = 3

(User press 3 and then press Enter key)

- $3 \times 01 = 03$
- $3 \times 02 = 06$
- $3 \times 03 = 09$
- $3 \times 04 = 12$
- $3 \times 05 = 15$
- $3 \times 06 = 18$
- $3 \times 07 = 21$
- $3 \times 08 = 24$
- $3 \times 09 = 27$
- $3 \times 10 = 30$



```
001 .model small
002 .stack 100h
       .data
A DB 'enter a number : $'
003
004
005
       .code
006
      main proc
007
               mov ax,@data
mov ds,ax
lea dx,A
mov ah,9
int 21h
008
009
010
011
013
015
               mov dx,10
mov ah,2
int 21h
017
               mov dx,13
mov ah,2
int 21h
019
020
021
022
               mov ah,01h
int 21h
023
024
025
026
027
028
             mov ch, OAH
             mov cl,00h
029
             cmp al,3ah
sub al,30h
mov bh,al
mov bl,01h
030
031
032
033
034
035
036
037
038
               L1:
               mov dl, Odh
mov ah, O2h
int 21h
039
040
               mov dl,0ah
mov ah,02h
int 21h
041
042
043
044
               mov dl,'0'
mov ah,02h
int 21h
045
046
047
               mov dl.bh
add dl.30h
mov ah.02h
int 21h
048
049
050
051
052
               mov dl,'*'
mov ah,02h
int 21h
054
056
               mov al,bl
mul bh
                MAA
061
```

.....



