

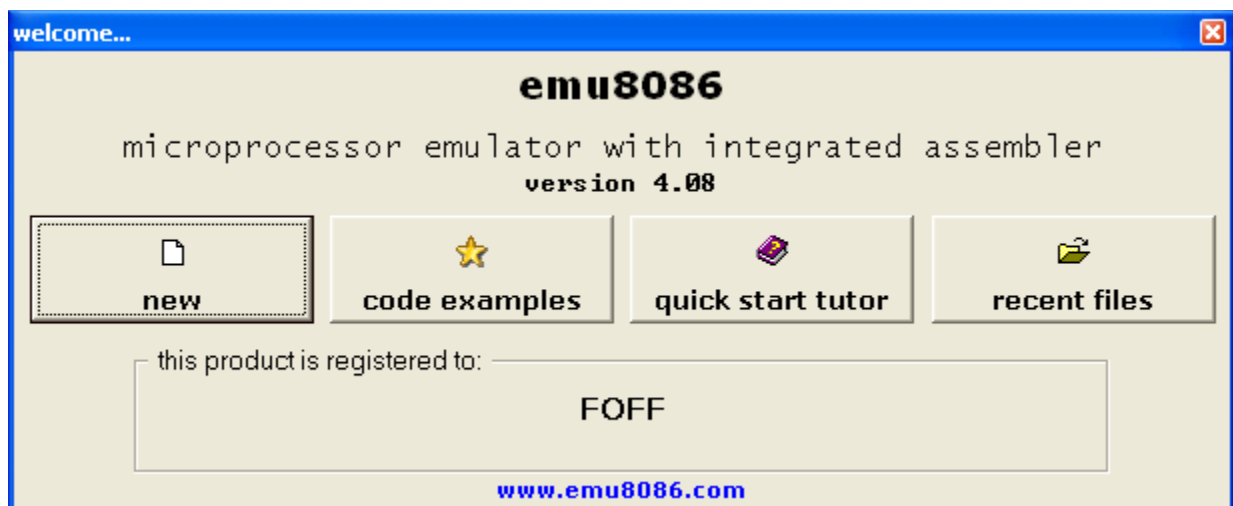
LAB#2

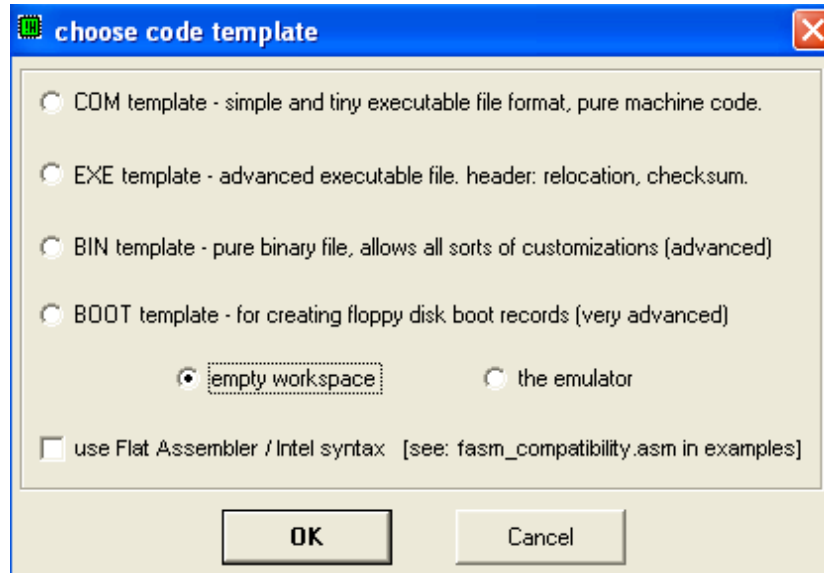
Objective

To understand and familiarize with the 8086 emulator environment.

8086 Microprocessor Emulator, also known as EMU8086, is an emulator of the program 8086 microprocessor. It is developed with a built-in 8086 assembler. This application is able to run programs on both PC desktops and laptops. This tool is primarily designed to copy or emulate hardware. These include the memory of a program, CPU, RAM, input and output devices, and even the display screen.

There are instructions to follow when using this emulator. It can be executed into one of the two ways: backward or forward. There are also examples of assembly source code included. With this, it allows the programming of assembly language, reverse engineering, hardware architecture, and creating miniature operating system (OS).





the directive `.model small` tells the assembler that you intend to use the small memory model - one code segment.

It simply tells the structure of the memory

The stack often holds temporary and local variables.

emu8086 - assembler and microprocessor emulator 4.08

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```

01 .model small
02 .stack 200
03 .data
04     msg db 'Indus University$'
05 .code
06 .startup
07
08     mov ah,06h
09     mov dx,'A'
10     int 21h
11
12 .exit
13 end
14 |

```

line: 14 col: 1

drag a file here to open

The screenshot shows the 'emulor: noname.exe_' window. The top menu bar includes 'file', 'math', 'debug', 'view', 'external', 'virtual devices', 'virtual drive', and 'help'. Below the menu is a toolbar with buttons for 'Load', 'reload', 'step back', 'single step' (highlighted), and 'run'. A 'step delay ms: 0' field is also present.

On the left, the 'registers' section displays the state of various registers (AX, BX, CX, DX, CS, IP, SS, SP, BP, SI, DI, DS, ES) with their high (H) and low (L) bytes. For example, AX is 00 00, BX is 00 00, CX is 01 01, and DS is 07 00.

The main area shows assembly code at memory address 071F:0000. The code is displayed in two columns. The left column shows the current instruction being executed, and the right column shows the next instruction. The current instruction is 'MOV DX, 0071Dh' at address 071F0:BA 186. The next instruction is 'MOV DS, DX' at address 071F1:1D 029.

On the right, a window titled 'original source code' displays the source code for the assembly. The code is as follows:

```

01 .model small
02 .stack 200
03 .data
04 msg db 'Indus University'
05 .code
06 .startup
07
08 mov ah,06h
09 mov dx,'A'
10 int 21h
11
12 .exit
13 end
14
15
16

```

The background of the window shows a landscape with a blue sky and green hills.

Taking a single input and displaying it through carriage return

```
.MODEL SMALL
.STACK 100H

.CODE
MAIN:
    MOV AH, 1                ; read a character
    INT 21H

    MOV BL, AL               ; save input character into BL

    MOV AH, 2                ; carriage return
    MOV DL, 0DH
    INT 21H

    MOV DL, 0AH              ; line feed
    INT 21H

    MOV AH, 2                ; display the character stored
    MOV DL, BL
    INT 21H

    MOV AH, 4CH              ; return control to DOS
    INT 21H
END MAIN
```

Use single step option for line by line execution

Lab Objective

Task#1: Write a program that takes a single character input and displays it in a new line and observe the contents of registers by using single stepping and record them

```

01 .model small
02 .stack 100h
03 .code
04 main proc
05     mov ah,1
06     int 21h
07     mov bl,al
08     mov ah,2
09     mov dl,0dh
10     int 21h
11
12     mov dl,0ah
13     int 21h
14
15     mov ah,2
16     mov dl,bl
17     int 21h
18
19     mov ah,4ch
20     int 21h
21     ret
22
23
24
25
26

```

emulator: noname.exe

file math debug view external virtual devices virtual drive help

Load reload step back single step run step delay ms: 0

registers	H	L
AX	00	00
BX	00	00
CX	01	1B
DX	00	00
CS	0720	
IP	0000	
SS	0710	
SP	0100	
BP	0000	
SI	0000	
DI	0000	
DS	0700	
ES	0700	

0720:0000

07200: B4 180	↓
07201: 01 001	⊖
07202: CD 205	=
07203: 21 033	!
07204: 8A 138	è
07205: D8 216	↑
07206: B4 180	↓
07207: 02 002	⊖
07208: B2 178	⌘
07209: 0D 013	CRET
0720A: CD 205	=
0720B: 21 033	!
0720C: B2 178	⌘
0720D: 0A 010	NEWL
0720E: CD 205	=
0720F: 21 033	!
07210: B4 180	↓
07211: 02 002	⊖
07212: 8A 138	è
07213: D3 211	u
07214: CD 205	=
07215: 21 033	!

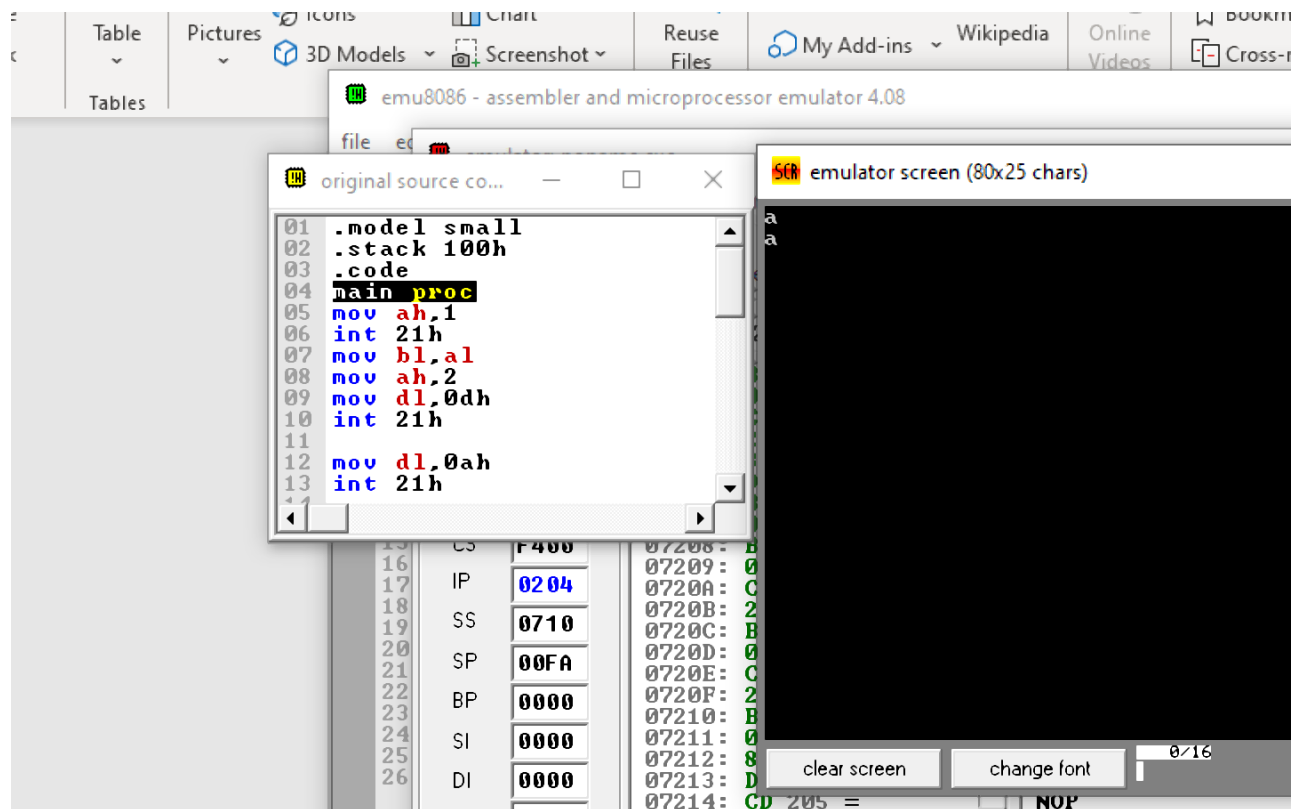
0720:0000

```

MOV AH, 01h
INT 021h
MOV BL, AL
MOV AH, 02h
MOV DL, 0Dh
INT 021h
MOV DL, 0Ah
INT 021h
MOV AH, 02h
MOV DL, BL
INT 021h
MOV AH, 04Ch
INT 021h
RET
NOP
NOP
NOP
NOP
NOP
NOP
NOP
...

```

screen source reset aux vars debug stack flags



Task#2: Write a program to display a string in Assembly Language.

Assembly Code:

```

03 ; the location of this template is c:\emu8086\inc\0_com_template
04
05 name "hi"
06
07 org 100h
08
09 jmp start
10
11 msg: db "hey there!", 0Dh,0Ah,24h
12
13 start: mov dx,msg
14        mov ah,09h
15        int 21h
16
17        mov ah,0
18        int 16h
19
20 ret
21
22
23
24
25

```

Emulator Registers:

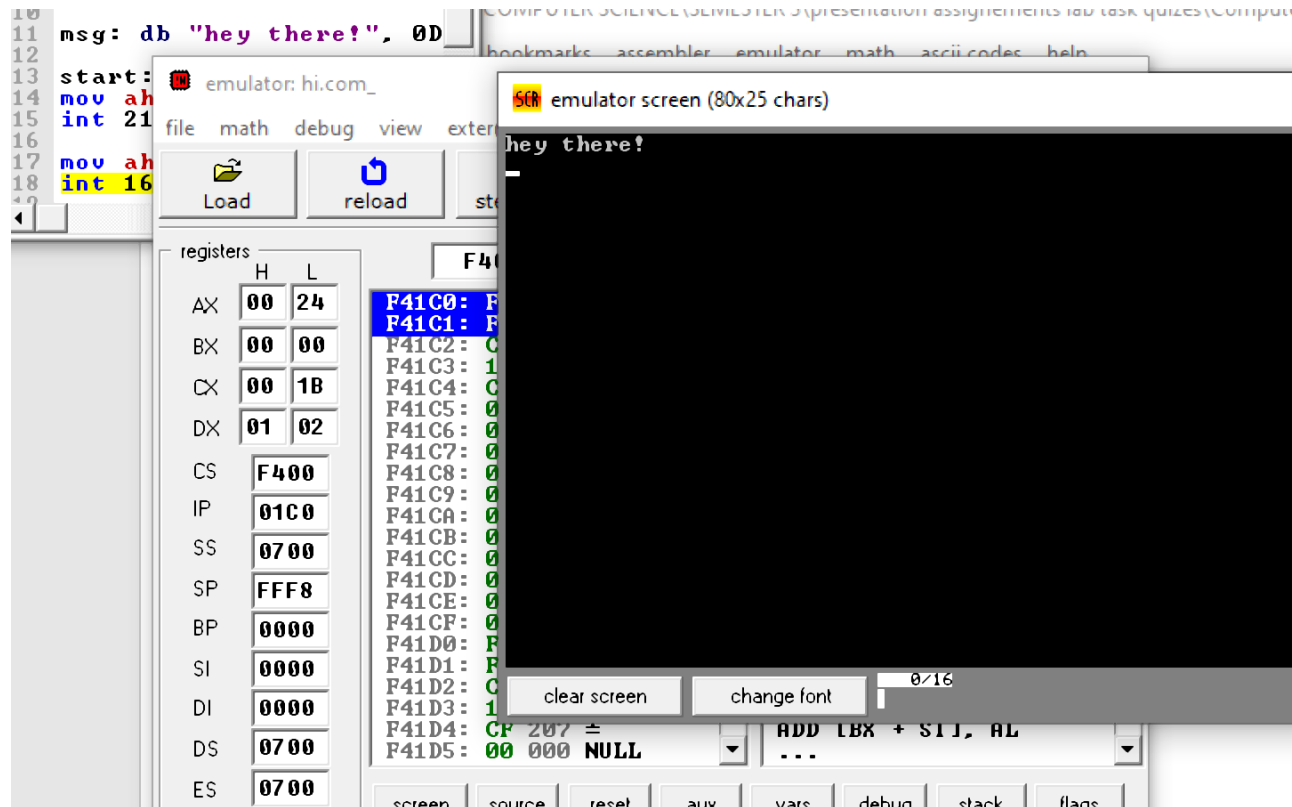
Register	H	L
AX	00	00
BX	00	00
CX	00	1B
DX	00	00
CS	0700	
IP	0100	
SS	0700	
SP	FFFE	
BP	0000	
SI	0000	
DI	0000	
DS	0700	
ES	0700	

Memory Dump (0700:0100):

Address	Hex	ASCII	Comment
07100:	EB 235 6		
07101:	0D 013 CRET		
07102:	68 104 h		
07103:	65 101 e		
07104:	79 121 y		
07105:	20 032 SPA		
07106:	74 116 t		
07107:	68 104 h		
07108:	65 101 e		
07109:	72 114 r		
0710A:	65 101 e		
0710B:	21 033 !		
0710C:	0D 013 CRET		
0710D:	0A 010 NEWL		
0710E:	24 036 \$		
0710F:	BA 186		
07110:	02 002 @		
07111:	01 001 @		
07112:	B4 180		
07113:	09 009 TAB		
07114:	CD 205 =		
07115:	21 033 !		

Instruction Stream:

Address	Instruction
07100:	JMP 010Fh
07101:	PUSH 07965h
07102:	AND [SI] + 068h, DH
07103:	DB 65h
07104:	JB 0170h
07105:	AND [DI], CX
07106:	OR AH, [SI]
07107:	MOV DX, 00102h
07108:	MOV AH, 09h
07109:	INT 021h
0710A:	MOV AH, 00h
0710B:	INT 016h
0710C:	RET
0710D:	NOP
0710E:	NOP
0710F:	NOP
07110:	NOP
07111:	NOP
07112:	NOP
07113:	NOP
07114:	NOP
07115:	...



Task#2:

Observe the contents of registers by using single stepping and record them.

.MODEL SMALL

.STACK 100H

.DATA

MESSAGE1 DB 0AH, 0DH, "INDUS UNIVERSITY\$"

.CODE

MAIN:

MOV AX, @DATA

MOV DS, AX

MOV DX, OFFSET MESSAGE1

MOV AH, 09H

INT 21H

MOV AH, 4CH

INT 21H

END MAIN

Registers	After 1 st Instruction	After 2nd Instruction	After 3rd Instruction	After 4th Instruction	After 5th Instruction
AX	07 20	07 20	07 20	09 20	09 20
BX	00 00	00 00	00 00	00 00	00 00
CX	01 30	01 30	01 30	01 30	01 30
DX	00 00	00 00	00 00	00 00	00 00

Registers	After 6th Instruction	After 7th Instruction	After 8th Instruction	After 9th Instruction	After 10th Instruction
AX	09 24	09 24	4C 24	4C 24	4C 24
BX	00 00	00 00	00 00	00 00	00 00
CX	01 30	01 30	01 30	00 00	00 00
DX	00 00	00 00	00 00	00 00	00 00