

Assignment - 1

PAGE :

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1) What do you mean by microprocessor?
List out its application areas?

⇒ microprocessor is a multipurpose programmable, clock driven, register based electronic device that reads binary instructions from a storage device called memory, accepts binary data as input processes data according to those instructions and provide results as output. The microprocessor operates in binary 0 and 1 known as bits that are represented in terms of electrical voltages in the machine that means 0 represents low voltage level and 1 represents high voltage level. Each microprocessor recognizes and processes a group of bits called the word and microprocessors are classified according to their word length such as 8 bit microprocessor with 8 bit word and 32 bit microprocessor with 32 bit word etc.

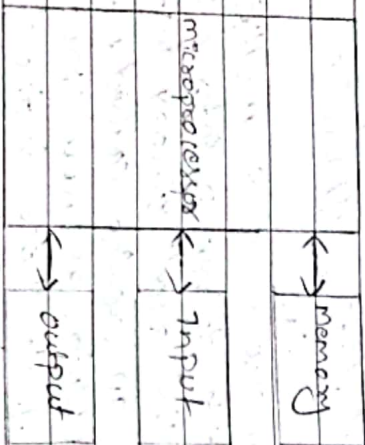


Fig. microprocessor

Applications of microprocessor

- * microprocessor is the CPU of the microcomputer
- * microprocessor is used in data monitoring system, automatic weighing balancing system
- * It is used in security system like CCTV surveillance, smart locks etc..
- * It is also used in communication system

2) Explain stored program concept and von Neumann Architecture?

=> Stored-program concept: Storage of instructions in computer memory to enable it to perform a variety of tasks in sequence or intermittently. Program electronically stored in binary numbers format in a memory device. So that instructions could be modified by the computer as determined by intermediate computational results which enabled digital computers to become much more flexible and powerful.

Storing the data and instructions in a same memory is called stored program concept which is first adopted by John von Neumann and such architecture is named as von-Neumann architecture. It is the ability to store instructions in the memory along with the data on which the instruction operate. The von Neumann architecture consists of three distinct components a central processing unit, memory unit and input/output (I/O) interfaces.

CPU consist of control unit and arithmetic and logic unit (ALU). ALU is responsible for carrying out all arithmetic and logical operations on data where as control unit determines the order of flow of instructions that need to be executed in programs by issuing control signals to the hardware.

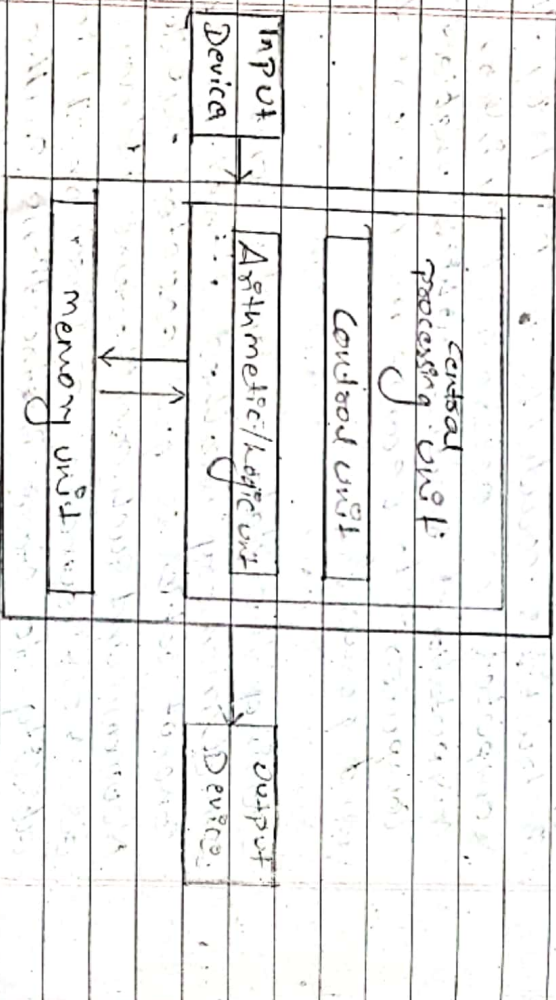


Fig. Block diagram of Von Neumann Architecture

3) Draw the block diagram of base microprocessor and explain its operation.

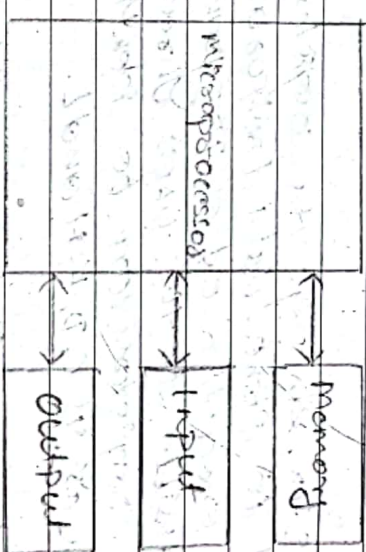


Fig. Block diagram of base microprocessor.

Microprocessor
It is clock driven semiconductor device consisting of electronic logic circuits manufactured by using either a large scale integration (LSI) or very large scale integration (VLSI) technique. It is capable of performing various computing functions and making decisions to change the sequence of program execution.

It can be divided into three segments.

A. Arithmetic/Logic Unit: It performs arithmetic operations as addition and subtraction and logic operations are AND, OR, XOR.

B. Registers Array: The registers are primarily used to store data temporarily during the execution of a program and data accessible to the user through instruction. The registers can be subdivided by letters such as R, C, D, E, H and L.

C. Control Unit: It provides the necessary timing and control signals to all the operations in the microcomputer. It controls the flow of data between the microprocessor and memory & peripherals.

Memory

Memory stores binary information such as instructions and data and provides that information to the up whenever necessary. Memory has two sections.

A. Read Only Memory (ROM): Used to store programs that do not need alteration and can only read.

B. Read/Write Memory (RAM): Also known as user memory. Volatile is used to store user program and data. The information stored in this memory can be easily read and altered.

Input/Output

* It communicates with the outside world using two devices input and output which are also known as peripherals.

* Two input device such as keyboard, switches, and analog to digital converter transfer binary information from outside world to the microprocessor.

* The output device transfers data from the microprocessor to the outside world. They include the devices such as LED, CRT, digital to analog converter, printer, etc.

4) What is system bus? Explain its types.

→ A system bus is a single computer bus that connects the major components of a computer system, combining the functions of a data bus to carry information, an address bus to determine where it should be sent, and a control bus to determine its operation. The technique was developed to reduce costs and improve modularity, and although popular in the 1970s and 1980s, more modern computers use a variety of separate buses adapted to more specific needs. It is varies from system to system.

Types of system bus

Address Bus

It is a group conducting wires which carries address only. Address bus is unidirectional because data flows in one direction. from microprocessors to memory & from microprocessors to I/O devices (i.e out of microprocessor).

Length of address bus of 8085 microprocessor is 16 bit (i.e four hexadecimal digits) and determine the amount of memory a system can address.

Data Bus

It is a group of conducting wires which carries data only. Data bus is bidirectional because data flow in both directions, from microprocessor to memory or I/O devices and from memory or I/O devices to microprocessor. Length of Data bus of 8085 microprocessor is 8 bit ranging from 00H to FFH (i.e two hexadecimal digits) where H denotes Hexadecimal. The length of the data bus is directly related to the largest number that the bus can carry, such as an 8-bit bus can represent all unique values i.e 0 to 255.

Control Bus

It is a group of conducting wires, which is used to generate timing and

control signal to control all the associated peripherals. Microprocessor uses control bus to process data, that is to do with selected memory location. Some signals are:

- * Memory read
- * Memory write
- * I/O Read
- * I/O write
- * Opcode fetch

5) Differentiate between von Neuman and Harvard architecture.

=>	Von Neuman architecture	Harvard Architecture
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i) It is a theoretical design. It is a modern architecture based on stored program theory based on the Harvard computer concept and multi computer model.

ii) It uses same physical memory address for instructions and data. iii) It uses separate memory address for instructions and data.

iii) Processor needs two clock cycles to execute an instruction. iv) Processor need one cycle to complete an instruction.

iv) Similar control unit design buses is more complicated and development cost is cheaper and faster development cost.

v) Data transfer and instruction fetches cannot be performed simultaneously. vi) Data transfer and instruction fetches can be performed at the same time.

vii) Used in personal computers and signal processing. viii) Used in microcontrollers, laptops, and workstations.