



# AI IC Design Course Training

*This Certifies That*

ANSHU MOHANAN

has completed the requirements of

**IC Design LAB, In-Memory Computing Circuit Design, FPGA LAB**

From July 1<sup>st</sup> to September 30<sup>th</sup> and is awarded this certificate.

Given at **Chang Gung University Electrical Engineering Department VLSI/CAD LAB**

This 30<sup>th</sup> Day of September Year of 2024.

Associate Prof. J- Chyng

