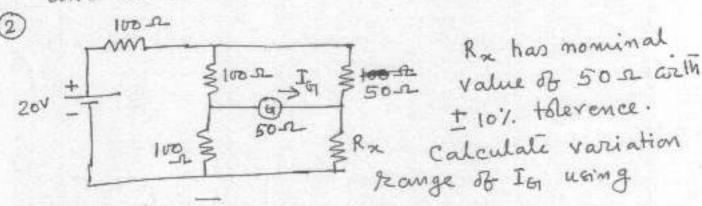


Assume both the ckts to be in steady state and diode D to be ideal.



(3) use compensation Theorem to calculate i(t) in The following network.

(2) CKH. was in stead

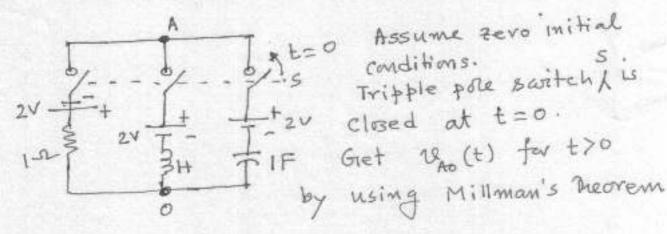
20V + 1 342 SX t=0
20V - 1 342 SX t=0
31H SAGE

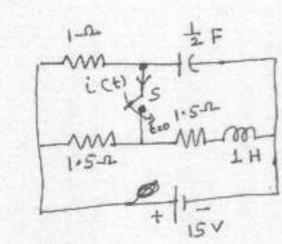
state before saritching.

Verify your result

by solving it using

Therenin Theorem.



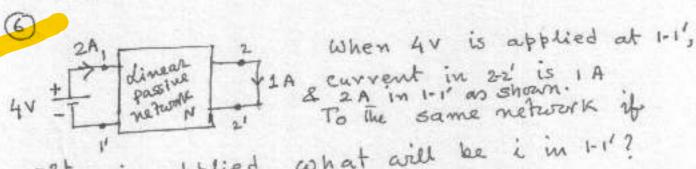


(5)

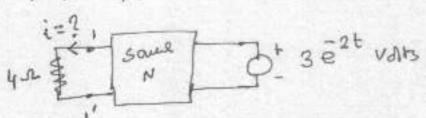
After sis has been reached, s is closed at t=0. Calculate i(t) for t>0 by using

(a) Thevenin Theorem.

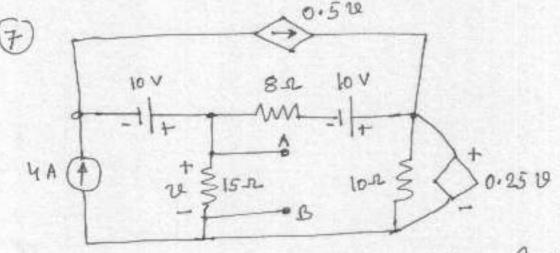
(b) Mesh Analysis.



what will be i in 1-1'?



Apply suitable network theorems to get the result. Make necessary assumptions



- (a) use superposition Theorem to get 12.
- (b) Gret Therenin Equivalent out across AB.