

ANSUMAN DAS

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EDUCATION

VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY <i>BACHELORS OF TECHNOLOGY IN ELECTRONICS AND TELECOMMUNICATION</i>	SAMBALPUR, ODISHA 2020 – 2024
KENDRIYA VIDYALAYA NO.1, BALASORE <i>INTERMEDIATE AND MATRICULATION</i>	BALASORE, ODISHA 2006 – 2019

EXPERIENCE

INTERNSHIP <i>INTEGRATED TEST RANGE(ITR), DRDO</i> <ul style="list-style-type: none">Worked on Digital Modulation in Simulink And Designing Of Digital FiltersExplored about the "Telemetry System" used for Defence purpose	MAY 2022 – JUNE 2022 CHANDIPUR, ODISHA
CORE MEMBER <i>E-CELL, VSSUT</i> <ul style="list-style-type: none">I am a Core Team member and also the Event Management Lead of the E-Cell VSSUT, or Techno- Management Club, at our institution.I actively participate in the planning of events like "TEDX-VSSUT" and our techno-fest "SAMAVESH."	MARCH 2021 – PRESENT SAMBALPUR, ODISHA

PROJECTS

VOICE ASSISTANT <i>Python</i> <ul style="list-style-type: none">A Voice assistant is a user interface that takes the voice commands from the user and does the corresponding task.	MARCH 2023
VOTING MACHINE <i>VLSI, System Verilog, XILINX</i> <ul style="list-style-type: none">Designed an EVM(Electronic Voting Machine) by the help of System Verilog.	FEBRUARY 2023
DIGITAL FILTERS <i>MATLAB, Simulink</i> <ul style="list-style-type: none">Worked on Digital Modulation in Simulink And Designing Of Digital Filters	AUGUST 2022
ALU OF 8085 MICROPROCESSOR <i>VLSI, System Verilog, XILINX</i> <ul style="list-style-type: none">Developed the Arithmetic Logical Unit(ALU) unit of the 8085 microprocessor	JUNE 2022
PROFILE WEBSITE <i>HTML, CSS, Bootstrap, Git</i> <ul style="list-style-type: none">Developed profile based website using Bootstrap	MAY 2021

TECHNICAL SKILLS

Languages: Python, C/C++, HTML/CSS, System Verilog, MATLAB
Frameworks: WordPress
Developer Tools: Git, VS Code, Visual Studio, PyCharm