

ANSUMAN DAS

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EDUCATION

BACHELORS OF TECHNOLOGY IN ELECTRONICS AND TELECOMMUNICATION

VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY • SAMBALPUR, ODISHA • 2024

INTERMEDIATE AND MATRICULATION

KENDRIYA VIDYALAYA NO.1, BALASORE • BALASORE, ODISHA • 2019

EXPERIENCE

CORE MEMBER

E-CELL, VSSUT

March 2021 - Present, SAMBALPUR, ODISHA

- Core Team Member and Event Management Lead, E-Cell VSSUT (Techno-Management Club), VSSUT, with 2 years of experience in event management and team work. Contributed to the planning and execution of events such as "TEDX-VSSUT" and "SAMAVESH" techno-fest.
- Administered events with 500+ participants, increasing engagement and attendance by 50% and also raised fundings.

INTERNSHIP

Proof & Experimental Establishment (PXE), DRDO

May 2023 - June 2023, CHANDIPUR, ODISHA

- Streamlined network infrastructure utilizing OSI model, TCP/IP protocols, and client-server model, leading to a 40% improvement in data transfer rate within the network.
- Networking: Built IP networks that can handle up to 100 concurrent connections, and that works proficient in network parameters and protocols.

INTERNSHIP

Integrated Test Range (ITR), DRDO

May 2022 - June 2022, CHANDIPUR, ODISHA

- Initiated Simulink, a leading simulation and modeling tool, to design and implement four digital modulation schemes (BPSK, QPSK, 16-QAM, and 64-QAM) and two digital filters (low-pass and high-pass) with different specifications and parameters.
- Reduced implementation time for the project by 30% and improved the performance of digital communication systems by 15%.

PROJECTS

VOTING MACHINE

November 2022 - November 2022

- The proposed digital EVM designed on Xilinx ISE using Verilog HDL and can also be implemented on FPGA board for real time purpose with 90% accuracy .
- Effected method can be used from university to national level, as it can be reprogrammed for different tasks and save costs upto 60%.

TRAFFIC LIGHT CONTROLLER

June 2022 - June 2022

- Verilog Hardware Description Language (HDL) programming used to construct the hardware with 80% accuracy .
- Strengthened the process using Verilog and we can mount it on a circuit or just upload it to the circuit accordingly so that particular circuit functions with a success rate of nearly 70% according to the code we rendered.

DIGITAL FILTERS

Integrated Test Range (ITR), DRDO • May 2022 - June 2022

- Proficiently developed and implemented Digital Modulation techniques using Simulink, enabling accurate signal transmission and reception in Digital Filters.
- Optimized Digital Filters for all type of possible applications, resulting in improved system performance.

SKILLS

C/C++, HTML/CSS; Proficient in MATLAB, Python, Excel, Networking.

CERTIFICATIONS

CCNA: Introduction to Networks

Cisco • 2023

- Earner has knowledge of networking including IP addressing, how physical, data link protocols support Ethernet, can configure connectivity between switches, routers and end devices to provide access to local and remote resources.

PCAP: Programming Essentials in Python

OpenEDG Python Institute • 2023

- Knows the universal concepts of computer programming, including variables, data structures, algorithms, control flow, functions, and exceptions.

Excel Skills for Business: Essentials

Coursera • 2022

- The Excel user interface, how to operate essential navigational controls in Excel and how to perform basic data entry with Excel spreadsheets.
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