ANSUMAN DAS

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EDUCATION

Bachelors Of Technology In Electronics And Telecommunication , 2020 - 2024
Veer Surendra Sai University Of Technology, Sambalpur, Odisha.

Intermediate And Matriculation, 2006 - 2019
Kendriya Vidyalaya No.1 Balasore, Odisha.

SKILLS

Technical Skills	Python, C/C++, MATLAB, VLSI, IoT
Database	MySQL
Others	Git, MS-Office, Windows
Soft Skills	Leadership, Writing, Time Management, Communication

EXPERIENCE

E-CELL, VSSUT May 2020 - Present
Event Management Lead *Burla, Odisha*

- Contributed to the planning and execution of events such as "TEDX-VSSUT" and "SAMAVESH" techno-fest.

Proof And Experimental Establishment (PXE), DRDO May 2023 - June 2023
Summer Internship *Chandipur, Odisha*

- Streamlined network infrastructure utilizing OSI model, TCP/IP protocols, and CS model.
- Led to a 40% improvement in seamless data transmission within the network.
- Networking: Built IP networks that can handle up to 100 concurrent connections, and that works proficient in network parameters and protocols.

Integrated Test Range (ITR), DRDO May 2022 - June 2022
Summer Internship *Chandipur, Odisha*

- Initiated Simulink, a simulation and modeling tool, to design and implement four digital modulation schemes.
- Reduced implementation time for the project by 30% and improved the performance of digital communication systems by 15%.

PROJECTS

HOME AUTOMATION USING ARDUINO. Designed a Home Automation Controller using Arduino, and the software based on IoT, BLYNK.Effected method can be used to measure the consumption of energy, as it can be reprogrammed for different tasks and save costs upto 60%.

TRAFFIC LIGHT CONTROLLER. Verilog Hardware Description Language (HDL) programming used to construct the hardware with 80% accuracy.Strengthened the process using Verilog and mount it on a circuit or just upload it to the circuit accordingly so that particular circuit functions with a success rate of nearly 70% according to the code we rendered.

VOTING MACHINE. The proposed digital EVM designed on Xilinx ISE using Verilog HDL and can also be implemented on FPGA board for real time purpose with 90% accuracy.Effected method can be used from university to national level, as it can be reprogrammed for different tasks and save costs upto 60%.

CERTIFICATIONS

- **CCNA: Introduction to Networks**
- **PCAP: Programming Essentials in Python**
- **Excel Skills for Business: Essentials**