

ANSUMAN DAS

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EDUCATION

BACHELORS OF TECHNOLOGY IN ELECTRONICS AND TELECOMMUNICATION

VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY • SAMBALPUR, ODISHA • 2024

INTERMEDIATE AND MATRICULATION

KENDRIYA VIDYALAYA NO.1, BALASORE • BALASORE, ODISHA • 2019

EXPERIENCE

INTERNSHIP

Proof & Experimental Establishment (PXE), DRDO

May 2023 – June 2023, CHANDIPUR, ODISHA

- Designed an IP network for efficient data transmission within a given range, and for small office setups, using the OSI model and TCP/IP protocols, and the client – server model.
- Networking: Designed and implemented IP networks, proficient in network parameters and protocols.

INTERNSHIP

Integrated Test Range (ITR), DRDO

May 2022 – June 2022, CHANDIPUR, ODISHA

- Successfully executed a comprehensive project on "Digital Modulation in Simulink and Designing of Digital Filters".
- Utilized Simulink, a leading simulation and modeling tool, to design and implement digital modulation schemes and digital filters.
- Reduced implementation time for the project by 30% and improved the performance of digital communication systems by 15%.

CORE MEMBER

E-CELL, VSSUT

March 2021 – Present, SAMBALPUR, ODISHA

- Core Team Member and Event Management Lead, E-Cell VSSUT (Techno-Management Club), VSSUT, with 2 years of experience in event management and team work.
- Fully contributed to the planning and execution of events such as "TEDX-VSSUT" and "SAMAVESH" techno-fest.
- Successfully organized events with 500+ participants, increasing engagement and attendance by 50% and also raised fundings.

PROJECTS

IP NETWORK

Proof & Experimental Establishment (PXE), DRDO • June 2023 – June 2023

- Designed an IP network For Transmission of Data for a small office.
- Utilized the OSI model, TCP/IP protocols and the client-server model to connect different parts of the network for efficient data transmission in the given range. Improved data transfer rate by 40% within the network .

TRAFFIC LIGHT CONTROLLER

June 2022 – June 2022

- The hardware design developed using Verilog Hardware Description Language (HDL) programming.
- We can code the process using Verilog and we can mount it on a circuit or just upload it to the circuit accordingly so that particular circuit functions with a success rate of nearly 70% according to the code we implemented.

DIGITAL FILTERS

Integrated Test Range (ITR), DRDO • May 2022 – June 2022

- Proficiently developed and implemented Digital Modulation techniques using Simulink, enabling accurate signal transmission and reception in Digital Filters.
- Designed and optimized Digital Filters for all type of possible applications, resulting in improved system performance .

SKILLS

C/C++, HTML/CSS; Proficient in MATLAB, Python, Excel, Networking.

CERTIFICATIONS

CCNA: Introduction to Networks

Cisco • 2023

- Earner has knowledge of networking including IP addressing, how physical, data link protocols support Ethernet, can configure connectivity between switches, routers and end devices to provide access to local and remote resources.

PCAP: Programming Essentials in Python

OpenEDG Python Institute • 2023

- Knows the universal concepts of computer programming, including variables, data structures, algorithms, control flow, functions, and exceptions.

Excel Skills for Business: Essentials

Coursera • 2022

- The Excel user interface, how to operate essential navigational controls in Excel and how to perform basic data entry with Excel spreadsheets.