

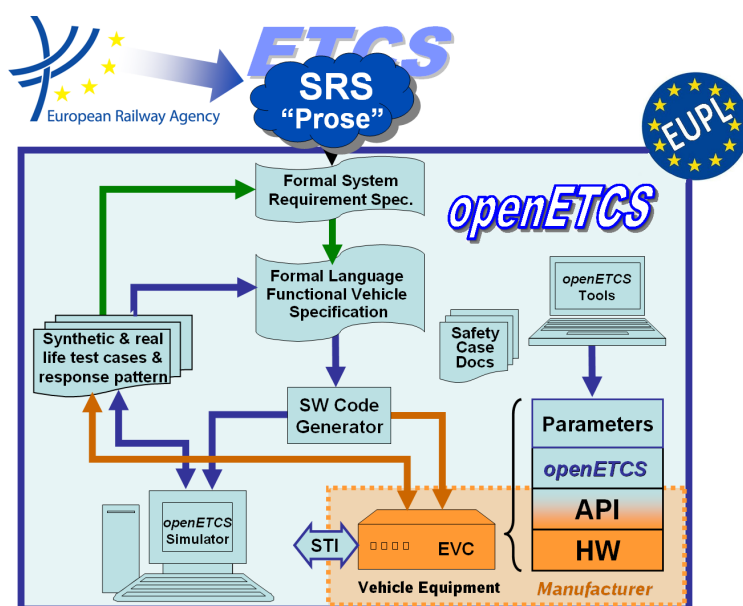
openETCS@ITEA Work Package 3: “Modelling”

# openETCS API description

## Generic interface between openETCS application and overall system

Nicolas Boverie, Pierre-Yves Le Morvan, David Mentré  
and Nicolas Van Landeghem

December 2014



**Funded by:**



Federal Ministry  
of Education  
and Research

Région de  
Bruxelles-  
CapitaleGOBIERNO  
DE ESPAÑA

MINISTERIO  
DE INDUSTRIA, ENERGÍA  
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openETCS@ITEA Work Package 3: “Modelling”

OETCS/WP3/D??  
December 2014

# openETCS API description

**Generic interface between openETCS application and overall system**

## Document approbation

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Prepared for openETCS@ITEA2 Project

**Abstract:** FIXME

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## Modification History

| Version | Section | Modification / Description | Author |
|---------|---------|----------------------------|--------|
|         |         |                            |        |

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# 1 Introduction and context

## 1.1 Introduction

This document describes the “openETCS API”. The openETCS API is an open, standardized, interface between a vendor specific platform and the openETCS application software. More details on the software architecture are given in chapter 3.

### Main objectives

The main objectives of this document are to describe an API:

- Suitable for every partner of openETCS project;
- Compatible with Vendor specific API through an “Adaptation Layer”;
- Making explicit all assumptions, including non-functional ones;
- Language agnostic, allowing interfacing with software written in C, Ada or other programming languages;
- Simple;
- Fulfilling safety objectives;
- Offering reasonable performance.

## 1.2 References

The requirements for this document are defined in (Baro and Welte, 2013, §7.1) and Welvaarts and Jacob (2014) documents.

Source of information and constraints for this document are following documents:

- Alstom API proposal defined in Boverie (2014), Alstom Transport API application layer (2014) and Alstom Transport API Appendix Functional Data Dictionary (2014);
- Comments on Alstom API proposal in Alstom Transport API comments (2014);
- The top level interfaces of openETCS SysML model (2014);
- SCADE openETCS model requirements on run-time defined in §2.2 and §3 of Steinke (2014);
- ERSA Simulator APIERSA;
- ETCS on-board interfaces as pointed out in Figure 1 of §2.5.3 in SUBSET-026 (2012).

### 1.3 Identified issues

This documents aims at reaching a consensus, satisfying relevant point of view of all openETCS project's partners.

As of now, we have identified following issues on which no consensus has been found (yet!):

1. Timing requirements are currently incompatible between SCADE model and Alstom's API definition
  - (a) Sub-issue regarding cycle time definition
  - (b) Sub-issue regarding ordering of events
2. The data flow definition in Alstom's API proposal is sometimes too far from SRS
3. No agreement on the overall behaviour of Basic software and Application software: when data flows are exchanged, how they are stored, constraints on them, ...
  - (a) No agreement on general software architecture
  - (b) What is in application Sw and generic software?
4. Define what is in or out of application (odometry, crc, ...)? Allow differentiation points between suppliers
5. No agreement on the way the various components are calling each other: order sequence, constraints, ...
6. How to manage physical units in unambiguous way?
7. The API description should distinguish abstract and concrete parts:
  - (a) Abstract parts: Dataflows, Datatypes, Input/Output, ...
  - (b) Concrete parts: Given by value or reference, C or Ada language, where is made allocation and (possible) de-allocation, ...
8. For WP5 needs, it would be preferable to have two levels within the API:
  - a low level where only "conduits" are opened between hardware architecture units. Opaque messages would be exchanged over such conduits;
  - a higher level where the specific kind of information exchanged of the conduits is detailed (e.g. precise kind of DMI/EVC messages).

For the lower level, it seems interesting to define non-functional properties (latency bounds, message size bounds, ...).

### 1.4 Abbreviations

Abbreviations not described in this section are described in SUBSET-023 (2012).

**API** Application Programming Interface. An interface between two parts of a system, describing aspects that the two parts shall be compliant with and keeping other points undetermined

**JRU** Juridical Recording Unit

## 2 Reference abstract hardware architecture

### 2.1 Why a reference abstract hardware architecture?

For proper understanding of openETCS API and of constraints imposed on both sides of the API, we need to define a *reference abstract hardware architecture*. This hardware architecture is “abstract” in the sense that the actual vendor specific hardware architecture might be totally different of the abstract architecture described in this chapter. For example, several units might be grouped together on the same processor.

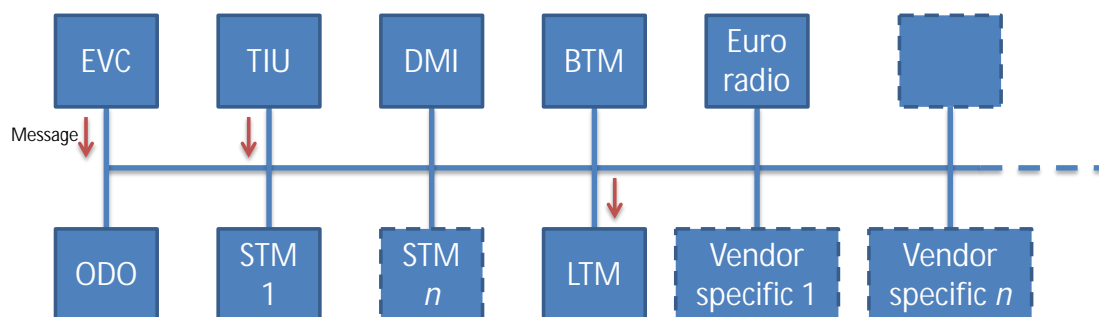
However the actual vendor specific architecture shall fulfill all the requirements and constraints of this reference abstract hardware architecture and shall not request additional constraints.

### 2.2 Definition of the reference abstract hardware architecture

The reference abstract hardware architecture is shown in figure 1.

The reference abstract hardware architecture is made of a bus on which are connected *units*:

- EVC (European Vital Computer);
- TIU (Train Interface Unit);
- ODO (Odometry);
- DMI (Driver Machine Interface);
- STM (Specific Transmission Module, up to 8 units);
- BTM (Balise Transmission Module);
- LTM (Loop Transmission Module);
- EURORADIO;
- JRU (Juridical Recording Unit);
- Zero or more Vendor specific unit.



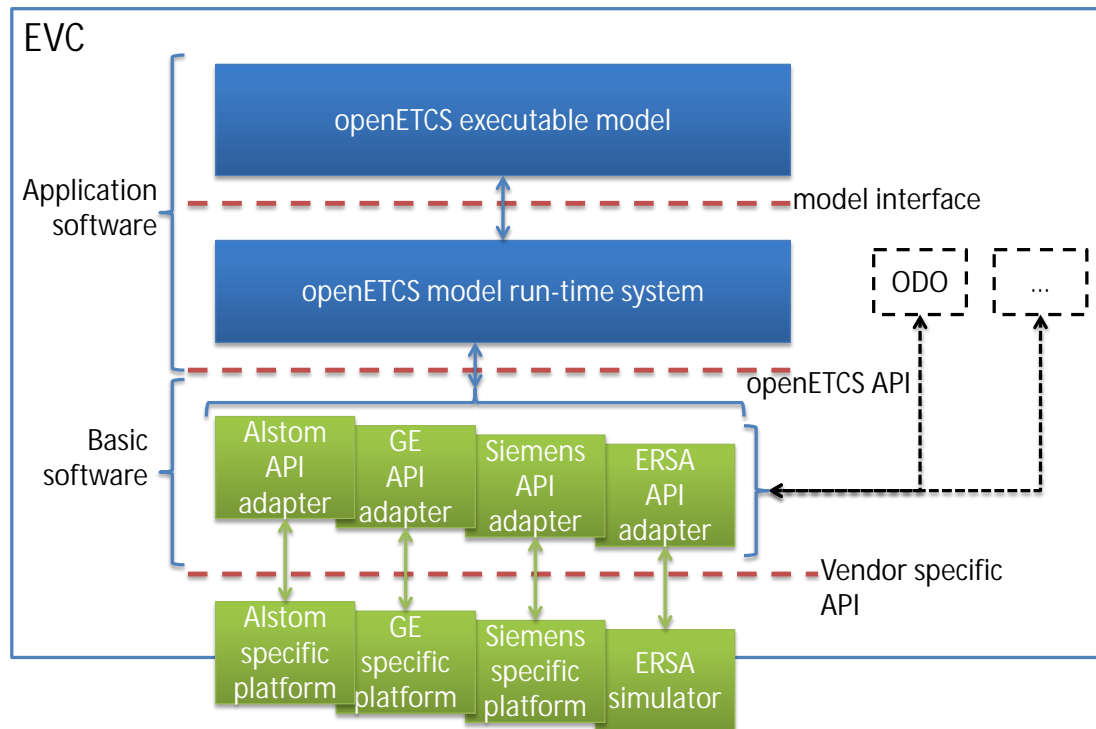
**Figure 1. Reference abstract hardware architecture**

A given instance of openETCS might not have all of above units. FIXME: Define a set of mandatory units?

Those units shall working concurrently. They shall exchange information with other units through asynchronous message passing.

### 3 Reference abstract software architecture

#### 3.1 Overall architecture



**Figure 2. Reference abstract software architecture**

The *reference abstract software architecture* is shown in figure 2. This architecture is made of following elements:

- *openETCS executable model* produced by the openETCS SCADE model (2014). It shall contain the program implementing core ETCS functions;
- *openETCS model run-time system* shall help the execution of the openETCS executable model by providing additional functions like encode/decode messages, proper execution of the model through appropriate scheduling, re-order or prioritize messages, etc. This block shall be described in another openETCS document. FIXME: ref?
- *Vendor specific API adapter* shall make the link between the Vendor specific platform and the openETCS model run-time system. It can buffer message parts, encode/decode messages, route messages to other EVC components, etc.
- All above three elements shall be included in the EVC;
- *Vendor specific platform* shall be all other elements of the system, bus and other units, as shown in figure 1.

FIXME

We have thus three interfaces:

- *model interface* is the interface between openETCS executable model and openETCS model run-time system. It shall be described in another openETCS document FIXME: ref?;
- *openETCS API* is the interface between openETCS model run-time system and Vendor specific API adapter. It is described in this document;
- *Vendor specific API* is the interface between Vendor specific API adapter and Vendor specific platform. This interface is not publicly described.

The two blocks openETCS executable model and openETCS model run-time system are making the *Application software* part. This Application software might be either openETCS reference software or vendor specific software.

The Vendor specific API adapter is making the *Basic software* part.

### 3.2 Information exchange between blocks

At this level of description, we do not explain how the various blocks of above architecture are calling themselves. We only assume they are exchanging *messages* in an asynchronous way. A message is a set of information corresponding to an event of a particular unit, e.g. a balise received from the BTM. The possible kind of messages are described in chapter 5.

How the exchange of messages is implemented in actual software, e.g. function call, storage of data in a shared buffer, ..., is described in chapter 6.

### 3.3 Architectural variations

Please note that the reference abstract hardware and software architectures do not forbid architectural variations. For example, the Odometry function could be put within the EVC (see ODO on figure 2) instead of a separate hardware unit (as it was shown on figure 1). Such Odometry function would be part of the Application software. But communication between this Odometry function within EVC and the openETCS model run-time system shall be done through the openETCS API and shall follow its conventions.

As another example, part of Vendor specific platform could be on EVC and thus the Vendor specific API would be within the EVC.

## 4 Real-time and ordering constraints

For proper functioning of the system, the openETCS API specifies real-time and ordering constraints that both the Application software as well as the Basic software and remaining of the system shall ensure. Those constraints are described in this chapter.

Some of constraints defined in sections 4.3 to 4.4.4 are derived from Alstom Transport proposal (Boverie (2014)).

### 4.1 System-wide constraints

Overall, the constraints imposed on openETCS API shall ensure the ETCS Requirements on performance are fulfilled (see SUBSET-041 (2012) for details and complete reference):

- Delay between receiving of a balise message and applying the emergency brake < 1 s
- Delay between receiving of a balise message and initiating a communication session establishment < 1.5 s
- Delay between receiving of a balise message and reporting the resulting change of status on-board < 1.5 s
- Delay between receiving of a MA via radio (both from RBC and from radio in-fill) and the update of EOA on-board < 1.5 s
- Delay between receiving of a MA from Euroloop and the update of EOA on-board < 1.5 s
- Delay between receiving of a MA from Euroloop and the update of EOA on-board < 3 s
- Delay between receiving of an emergency message and applying the reaction on-board < 1 sec, for brake order; < 1.5 sec, for indication to the driver
- Delay between receiving of a radio message and initiating a communication session establishment < 1 sec
- Delay between passing an EOLM and decoding of the first loop message  $\leq 4$  s
- Delay between driver action and new window displayed  $\leq 2$ s
- Desk becomes open to “enter Driver ID” is displayed  $\leq 3$  s
- Desk becomes open to SH mode is displayed  $\leq 15$  s
- Delay between passing an EOA/LOA and applying the emergency brake < 1 s
- Accuracy of distances measured on-board: for every measured distance  $s$  the accuracy shall be better or equal to  $\pm (5m + 5\% s)$
- Accuracy of speed known on-board:  $\pm 2$  km/h for speed lower than 30 km/h, then increasing linearly up to  $\pm 12$  km/h at 500 km/h
- The position of the train front indicated in a position report shall be estimated less than 1 sec before the beginning of sending of the corresponding position report
- Safe clock drift: 0.1%

## 4.2 Cyclic execution of the Basic and Application software

Both Basic and Application software shall be initialized once, in an *initialization phase*, and then shall be executed cyclically in a sequence of *cycles*.

During initialization phase, all units of the system shall be initialized and be ready to proceed at the end of initialization phase.

## 4.3 Ordering constraints on message exchange

Between units of the system (DMI, EVC, ...), following constraints shall be ensured:

- No message shall be lost FIXME: Real meaning? Distinguish SIL0 and SIL4?;
- Messages sent from one unit towards another one shall be received in emission order;
- Messages sent from two units towards a single one or received by a single units from two other units shall be received in any order.

## 4.4 Real-time constraints

### 4.4.1 Event propagation delay

The *external world* is the physical world out of the ETCS system. This external world sends and receives *events* to/from the ETCS system.

When an external world event is seen on a unit of the system (e.g. balise received in BTM), this event is processed and a message might be sent to another unit (e.g. EVC). In the reverse, a unit (e.g. EVC) might send a message to another unit (e.g. DMI) that might produce an event to the external world (e.g. display a message to the driver).

The system shall ensure following constraints:

- The minimum delay from an input event received of the external world until it is received (in a message) by the Application software shall be FIXME: XX ms;
- The maximum delay from an input event of the external world until it is received (in a message) by the Application software shall be FIXME: XX ms;
- The minimum delay from a message sent by the Application software until an event is sent to the external world shall be FIXME: XX ms;
- The maximum delay from a message sent by the Application software until an event is sent to the external world shall be FIXME: XX ms.

FIXME: We could impose different delays depending on considered unit.

### 4.4.2 Event re-ordering

As a consequence of ordering constraints on message exchange (§4.3) and event propagation delay (§4.4.1), two messages corresponding to two events (e.g. reception of a balise and a radio



message) on two different units (e.g. BTM and EURORADIO) send towards a third unit (e.g. EVC) might not be received in real time order, i.e. the message corresponding to the first real time event might arrive in second position on the reception unit.

#### 4.4.3 Event time-stamping

The system is asynchronous: an event is received on a unit and some time is spent before seeing the corresponding message in another unit. In order to do its computations, the Application software needs to know at which real time the event was received. In order to do so, a time-stamp shall be applied on all events requiring such computations. **FIXME: Make a list of such events?** **FIXME**

The time-stamp shall mark the full reception of the event at the input unit. It shall be applied as follow:

- For radio-message, the time-stamp shall correspond to date of reception of the last byte of the message;
- For balise, the time-stamp shall correspond to the passage of the train antenna over the balise center;
- For odometry, the time-stamp shall correspond to the corresponding odometry event.

This time-stamp shall fulfill following constraints:

- Time-stamp clock shall have a precision of 1 ms, with a deviation compared to real time of less than 0.1%;
- Time-stamp clock shall be the same on all units of the system.

#### 4.4.4 Execution time constraints

The following real-time constraints shall be ensured:

- The maximum execution time taken by Application software for initialization step is 100 ms;
- The maximum execution time of the Basic and Application software in a cycle shall be **FIXME: 300 + XX ms;** **FIXME**
- The maximum execution time of the Application software in a cycle shall be 100 ms.

### 4.5 Event burst

In some situation, a burst of events can occur. The following dimensioning shall be ensured:

- The maximum number of events sent by a unit shall be at most **FIXME: XX events;** **FIXME**
- The maximum number of events received by a unit shall be at most **FIXME: XX events.** **FIXME**  
**FIXME**

**FIXME: Do we need to define the maximum total number of events in fly?** **FIXME**

**FIXME: Do we need to define what to do when those maximums are reached?**

## 5 Abstract information flows

We describe in this chapter the information flow between EVC and other units using ASN.1 notation. See appendix A to know more about ASN.1.

### 5.1 Train Interface

The interface between the EVC and Train Interface Unit is defined in following Train-interface module. This module defines Message-Train-Interface-to-EVC and Message-EVC-to-Train-Interface.

Its structure follows SUBSET-094 (2014) which itself defines a concrete interface for SUBSET-034 (2014) (Train Interface FIS).

```

Train-interface DEFINITIONS AUTOMATIC TAGS ::=
BEGIN
  M-sleeping-signal-status ::=
    ENUMERATED { info-not-available , signal-active , signal-not-active }

  M-passiveshunting-signal-status ::=
    ENUMERATED { info-not-available , passive-shunting-permitted ,
      passive-shunting-not-permitted }

  M-nonleading-signal-status ::=
    ENUMERATED { info-not-available , non-leading-permitted ,
      non-leading-not-permitted }

  M-cab-signal-status ::=
    ENUMERATED { info-not-available , both-desks-are-closed ,
      desk-A-is-open , desk-B-is-open ,
      both-desks-are-open }

  M-directioncontroller-signal-status ::=
    ENUMERATED { info-not-available , direction-controller-in-neutral ,
      direction-controller-in-forward ,
      direction-controller-in-backward }

  M-trainintegrity-signal-status ::=
    ENUMERATED { info-not-available , train-is-not-integer ,
      train-is-integer }

  M-traction-signal-status ::=
    ENUMERATED { info-not-available , traction-on-traction-off }

  Mode-control-and-train-status ::= SEQUENCE {
    m-sleeping-st                M-sleeping-signal-status ,
    m-passiveshunting-st         M-passiveshunting-signal-status ,
    m-nonleading-st              M-nonleading-signal-status ,
    m-cab-st                     M-cab-signal-status ,
    m-directioncontroller-st      M-directioncontroller-signal-status ,
    m-trainintegrity-st          M-trainintegrity-signal-status ,
    m-traction-st                M-traction-signal-status
  }

  Isolation-status ::=
    ENUMERATED { info-not-available , on-board-equipment-is-isolated ,
      on-board-equipment-is-not-isolated }

```

```

M-brake-status ::=
    ENUMERATED { info-not-available , is-active , is-not-active }

Brake-status ::= SEQUENCE {
    m-regenerativebrake-st      M-brake-status ,
    m-eddycurrentbrake-st      M-brake-status ,
    m-magneticshoebrake-st     M-brake-status ,
    m-electropneumaticbrake-st M-brake-status ,
    m-additionalbrake-st       M-brake-status
}

P-brake-pressure-value ::= CHOICE {
    info-not-available      NULL,
    pressure                -- 0 to 6 bar with 0.1 increment
                           REAL (WITH COMPONENTS { mantissa      (0..60),
                                                    base          (10),
                                                    exponent      (-1) })
}

M-brake-signal-command ::=
    ENUMERATED { info-not-available , apply-brake , release-brake }

Brake-command ::= SEQUENCE {
    m-servicebrake-cm      M-brake-signal-command,
    m-emergencybrake-cm    M-brake-signal-command
}

M-brake-inhibit-command ::=
    ENUMERATED { info-not-available , inhibit-brake ,
                 do-not-inhibit-brake }

M-eddy-current-brake-inhibition ::=
    ENUMERATED { info-not-available ,
                 inhibit-for-service-brake ,
                 inhibit-for-emergency-brake ,
                 inhibit-for-both-service-emergency-brake ,
                 do-not-inhibit-for-service-brake ,
                 do-not-inhibit-for-emergency-brake ,
                 do-not-inhibit-for-both-service-emergency-brake
}

Brake-inhibition-command ::= SEQUENCE {
    m-regenerativebrake-cm M-brake-inhibit-command,
    m-eddycurrentbrake-cm  M-eddy-current-brake-inhibition ,
    m-magneticshoebrake-cm M-brake-inhibit-command
}

M-train-data-entry-type ::=
    ENUMERATED { info-not-available , fixed-entry-type ,
                 flexible-entry-type , switchable-entry-type }

M-train-data-info ::= NULL -- FIXME: to do

M-pantograph-command ::=
    ENUMERATED { info-not-available , lower-pantograph ,
                 raise-pantograph }

M-airtightness-command ::=
    ENUMERATED { info-not-available , tunnel-condition-active ,
                 tunnel-condition-not-active }

M-mainpowerswitch-command ::=

```

```

    ENUMERATED { info-not-available , open-main-power-swith ,
                  close-main-power-switch }

M-traction-cutoff-command ::=
    ENUMERATED { info-not-available , apply-traction-cutoff ,
                  release-traction-cutoff }

Type-I-train-commands ::= SEQUENCE {
    m-pantograph-cm      M-pantograph-command ,
    m-airtightness-cm     M-airtightness-command ,
    m-mainpowerswitch-cm  M-mainpowerswitch-command ,
    m-traction-cutoff-cm  M-traction-cutoff-command
}

D-test-distance ::= CHOICE {
    now          NULL ,
    distance -- 0..42949672940 mm with 10 mm increment
               REAL (WITH COMPONENTS { mantissa      (0..4294967294),
                                         base          (10),
                                         exponent      (-2) }) -- cm
}

D-test-trackcond ::= D-test-distance -- SUBSET-094 8.3.3.4

L-test-trackcond ::= D-test-distance -- SUBSET-094 8.3.3.8

M-trackcond ::=
    ENUMERATED { non-stopping-area ,
                  tunnel-stopping-area ,
                  sound-horn ,
                  powerless-section-lower-pantograph ,
                  radio-hole ,
                  air-tightness ,
                  switch-off-regenerative-brake ,
                  switch-off-eddy-current-brake-for-service-brake ,
                  switch-off-magnetic-shoe-brake ,
                  powerless-section-switch-off-main-power-switch ,
                  switch-off-eddy-current-brake-for-emergency-brake }

D-test-trackinit ::= D-test-distance -- SUBSET-094 8.3.3.5

Type-I-train-and-brake-inhibition-with-distance-commands ::=
    CHOICE { -- Q_TRACKINIT
        nothing-to-resume-profile-follow [0] SEQUENCE {
            d-test-trackcond      D-test-trackcond ,
            l-test-trackcond      L-test-trackcond ,
            m-trackcond           M-trackcond ,
            n-iter SEQUENCE (SIZE (0..31)) OF SEQUENCE {
                d-test-trackcond      D-test-trackcond ,
                l-test-trackcond      L-test-trackcond ,
                m-trackcond           M-trackcond
            }
        },
        empty-profile-initial-state-to-be-resumed [1] D-test-trackinit
    }

D-test-traction ::= D-test-distance

NID-ctraction ::= INTEGER (0..1023)

M-voltage ::= CHOICE {
    line-not-fitted-with-any-traction-system      NULL ,
    ac-25kV-50Hz                                  NID-ctraction ,

```

```

        ac-15kV-16-7Hz
        dc-3kV
        dc-1-5kV
        dc-600-750kV
    }

    M-change-traction-system ::= SEQUENCE {
        d-test-traction    D-test-traction ,
        m-voltage          M-voltage
    }

    Passenger-door-control-info ::= NULL -- FIXME: to do

    D-test-current ::= D-test-distance -- SUBSET-094 8.3.3.3

    M-current ::= CHOICE { -- SUBSET-026 7.5.1.62.1
        no-restriction      NULL,
        restriction         -- 0..10000 A with 10 A increment
                           REAL (WITH COMPONENTS { mantissa      (0..1000),
                                                    base            (10),
                                                    exponent        (1) }) -- 10 A
    }

    Change-of-allowed-current-consumption ::= SEQUENCE {
        d-test-current      D-test-current ,
        m-current           M-current
    }

    Message-Train-Interface-to-EVC ::= CHOICE {
        train-status        Mode-control-and-train-status , -- SUBSET-094 8.3.2.6
        brake-status        Brake-status , -- SUBSET-094 8.3.2.8
        brake-pressure       P-brake-pressure-value , -- SUBSET-094 8.3.2.9
        train-data-entry-type M-train-data-entry-type , -- SUBSET-094 8.3.2.12
        train-data-info      M-train-data-info , -- SUBSET-094 8.3.2.13
        type-l-train-and-brake-inhibition -- SUBSET-094 8.3.2.15
        Type-l-train-and-brake-inhibition-with-distance-commands
    }

    Message-EVC-to-Train-Interface ::= CHOICE {
        isolation-status      Isolation-status , -- SUBSET-094 8.3.2.7
        brake-command         Brake-command , -- SUBSET-094 8.3.2.10
        brake-inhibition       Brake-inhibition-command , -- SUBSET-094 8.3.2.11
        type-l-train-commands  Type-l-train-commands , -- SUBSET-094 8.3.2.14
        change-traction-system M-change-traction-system , -- SUBSET-094 8.3.2.16
        passenger-door-control-info
        Passenger-door-control-info , -- SUBSET-094 8.3.2.17
        change-of-allowed-current-consumption -- SUBSET-094 8.3.2.18
        Change-of-allowed-current-consumption
    }
END

```

## 6 Concrete interface with openETCS model

## Appendix A: ASN.1 Primer

*ASN.1* (Abstract Syntax Notation number One) is a formalism for the specification of abstract data types. Such a specification is made independently of programming language or wire transmission format.

From such an abstract specification, automated encoder/decoder in any programming languages can be generated, for various encoding formats like strings of bytes (BER, CER and DER encoding), strings of bits (PER encoding) or XML (BASIC-XER, CXER, EXTENDED-XER encoding).

ASN.1 is an international standard freely available (X.680-X.693 (2008)).

Example of encoder/decoder generator for ASN.1:

- `asn1c`: open source ASN.1 compiler <http://lionet.info/asn1c/compiler.html>
- `ASN1SCC` compiler: open source ASN.1 compiler that targets C and Ada, while placing specific emphasis on embedded systems <https://github.com/ttsiodras/asn1scc>
- Other ASN.1 tools can be found at <http://www.itu.int/ITU-T/studygroups/com17/languages/index.html>

### A.1 Overview of ASN.1 syntax

We describe in this appendix a very basic knowledge of ASN.1 aiming at understanding the ASN.1 definitions of this document.

The basic data types of ASN.1 are **INTEGER**, **REAL** (mathematical real values following formula  $m \times b^e$  with  $m$  mantissa,  $b$  2 or 10 base and  $e$  exponent), **BOOLEAN**, **NULL** (no type) and various types of strings<sup>1</sup>.  $a..b$  denotes are range of values from  $a$  to  $b$ .

The basic structures of ASN.1 are as followed and can be composed at will:

- **ENUMERATED** { $\}$ : an enumeration of fixed values;
- **SEQUENCE** { $\}$ : a record of elements;
- **SEQUENCE OF**: a list of element;
- **SEQUENCE (SIZE( $a..b$ )) OF**: an array of elements with indexes from  $a$  to  $b$ ;
- **CHOICE** { $\}$ : a choice between several elements (i.e. alternative, like a union in C or a variant record in Ada).

A data type definition has form “*name* ::= *definition*”. Names can use characters A–Z a–z 0–9 –.

<sup>1</sup>We ignore data types not used in this document like **OCTET STRING**, **BIT STRING**, etc.

ASN.1 is case sensitive. ASN.1 keywords are uppercase. Names starting with a capital letter are reserved for type definitions while names starting with a lower case later are used for field identifier or enumeration values.

Definitions are grouped in modules grouping **DEFINITIONS**:

```
The-module-name DEFINITIONS ::=
BEGIN
  -- some definitions
END
```

Comments start with -- until end of line or next --.

## A.2 ASN.1 examples

We now show some examples (all validated with asn1c tool).

A record of 2 elements, one Boolean and one integer:

```
Record-2-elements ::= SEQUENCE {
  a-boolean BOOLEAN,
  an-integer INTEGER
}
```

A choice between nothing or an array of 100 integer elements:

```
Choice-between-2-elements ::= CHOICE {
  nothing NULL,
  an-array SEQUENCE (SIZE (1..100)) OF INTEGER
}
```

A list of which elements are made of three possible values (value1, value2 or value3):

```
Possible-value ::= ENUMERATED {
  value1, value2, value3
}

List-of-possible-values ::= SEQUENCE OF Possible-value
```

A value that can be either an integer between 0 and 1023 or a real between 0 and 10 with a 0.1 increment ( $0 \times 10^{-1}$  to  $100 \times 10^{-1}$ ):

```
A-value ::= CHOICE {
  a-limited-integer INTEGER(0..1023),
  a-limited-real REAL (WITH COMPONENTS { mantissa (0..100),
                                          base (10),
                                          exponent (-1) })
}
```



## Appendix: References

- Alstom Transport API Appendix Functional Data Dictionary: 2014, 'Appendix Functional Data Dictionary'. Alstom Transport, v1.0 edition. [https://github.com/openETCS/requirements/blob/master/D2.7-Technical\\_Appendix/OETCS\\_API%20Requirements\\_appendix\\_functional\\_data\\_dictionary\\_v1.0.pdf](https://github.com/openETCS/requirements/blob/master/D2.7-Technical_Appendix/OETCS_API%20Requirements_appendix_functional_data_dictionary_v1.0.pdf).
- Alstom Transport API application layer: 2014, 'Appendix application layer v1.0'. Alstom Transport, v1.0 edition. [https://github.com/openETCS/requirements/blob/master/D2.7-Technical\\_Appendix/OETCS\\_API%20Requirements\\_appendix\\_application\\_layer\\_v1.0.pdf](https://github.com/openETCS/requirements/blob/master/D2.7-Technical_Appendix/OETCS_API%20Requirements_appendix_application_layer_v1.0.pdf).
- Alstom Transport API comments: 2014, 'Comments on Alstom Transport API proposal'. Alstom Transport. [https://github.com/openETCS/requirements/blob/master/D2.7-Technical\\_Appendix/2014-05-13-Munich-Meeting/OETCS\\_API\\_review\\_2014\\_05\\_12.xlsx](https://github.com/openETCS/requirements/blob/master/D2.7-Technical_Appendix/2014-05-13-Munich-Meeting/OETCS_API_review_2014_05_12.xlsx).
- Baro, S. and J. Welte: 2013, 'WP2/D2.6-9 Requirements for openETCS'. 2.0.0 edition. [https://github.com/openETCS/requirements/blob/master/D2.6-9/D2\\_6-9.pdf](https://github.com/openETCS/requirements/blob/master/D2.6-9/D2_6-9.pdf).
- Boverie, N.: 2014, 'API Requirements for OpenETCS'. Alstom Transport, v1.2 edition. [https://github.com/openETCS/requirements/blob/master/D2.7-Technical\\_Appendix/OETCS\\_API%20Requirements\\_v1.2.pdf](https://github.com/openETCS/requirements/blob/master/D2.7-Technical_Appendix/OETCS_API%20Requirements_v1.2.pdf).
- ERSA, 'ERSA simulator API'. ERSA. [https://github.com/openETCS/demonstrator/tree/master/Documentation/External%20Interface/Implementation/light\\_runner/include](https://github.com/openETCS/demonstrator/tree/master/Documentation/External%20Interface/Implementation/light_runner/include).
- openETCS SCADE model: 2014, 'openETCS SCADE model'. openETCS. <https://github.com/openETCS/modeling/tree/master/model/Scade/System>.
- openETCS SysML model: 2014, 'openETCS SysML model'. openETCS. [https://github.com/openETCS/modeling/tree/SysML\\_modeling/model/sysml/WP3-Initial-Architecture](https://github.com/openETCS/modeling/tree/SysML_modeling/model/sysml/WP3-Initial-Architecture).
- Steinke, U.: 2014, 'openETCS SCADE Modelling Guide'. [https://github.com/openETCS/modeling/blob/master/ModelingRules/SCADE\\_Modelling\\_Guide.pdf](https://github.com/openETCS/modeling/blob/master/ModelingRules/SCADE_Modelling_Guide.pdf).
- SUBSET-023: 2012, 'Glossary of Terms and Abbreviations, SUBSET-023'. ERA, v3.0.0 edition.
- SUBSET-026: 2012, 'System Requirements Specification, SUBSET-026'. ERA, v3.3.0 edition.
- SUBSET-034: 2014, 'Train Interface FIS, SUBSET-034'. ERA, v3.1.0 edition.
- SUBSET-041: 2012, 'Performance Requirements for Interoperability, SUBSET-041'. ERA, v3.1.0 edition.
- SUBSET-094: 2014, 'Functional requirements for on-board reference test facility, SUBSET-094'. ERA, v3.0.0 edition.
- Welvaarts, J. and B. Jacob: 2014, 'Requirements on openETCS API'. 11.05.2014 edition. [https://github.com/openETCS/requirements/blob/master/D2.7-Technical\\_Appendix/2014-05-13-Munich-Meeting/Bullit%20point%20openETCS%20requirements\\_20140511.pdf](https://github.com/openETCS/requirements/blob/master/D2.7-Technical_Appendix/2014-05-13-Munich-Meeting/Bullit%20point%20openETCS%20requirements_20140511.pdf).

X.680-X.693: 2008, 'X.680-X.693 : Information Technology - Abstract Syntax Notation One (ASN.1) & ASN.1 encoding rules'. ITU-T. Freely available from <http://www.itu.int/ITU-T/studygroups/com17/languages/index.html>.

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