

Moisture Sensitivity Level – MSL 1





ASDMB

FEATURES:

- Ultra Miniature Pure Silicon™ Clock Oscillator
- 2nd Generation MEMS Technology with reduced jiter by Discera
- Low Power Consumption < 10mA
- Exceptional Stability +/- 10ppm Over Temp. at -40 to +105°C, +/- 5ppm over -40 to +85°C
- Available in 30kG Shock Resistance Configuration
- Compact QFN Plastic Packaging

> APPLICATIONS:

- CCD Clock for VTR Camera
- Equipment Connected to PCs
- Low Profile Equipment
- Computers and Peripherals
- Lower Cost Crystal Oscillator Replacement
- Portable Electronics (MP3 Players, Games)
- Consumer Electronics such as TV's, DVR's, etc.
- Vibrant, Shock-Prone & Humid Environments for Industrial Equipment
- Demanding Military & Automotive Electronics



STANDARD SPECIFICATIONS:

Parameters	Minimum	Typical	Maximum	Units	Notes
Frequency Range:	1.0		150	MHz	
Operating Temperature:	0		+70	°C	See options
Storage Temperature:	-55		+150	°C	
Overall Frequency Stability*:	-50		+50	ppm	See options
Supply Voltage (Vdd):		+1.8 ~ +3.3		V	
Output Load:			15, 25, or 40	рF	Sac antions
Output Load.	10			kΩ	See options
Symmetry:	45		55	%	@1/2Vdd
Startup Time:		1.5	3.0	ms	
Disable Time:		20	100	ns	
Disable Stand-by Current:			15	uA	
Tri state Eunstian (Stand by):	"1" (VIH≥0.75*Vdd) or Open: Oscillation			V	
Tri-state Function (Stand-by):	7) "0"	/IL<0.25*Vdd)	: Hi Z	V	
Aging:	-5.0		+5.0	ppm	First year

Key Electrical Specifications – Vdd= 1.8V

Par	ameters		Minimum	Typical	Maximum	Units	Notes
1 41	1.0 to 39.99	99MHz		5	15	mA	CL=0p
	40.0 to 79.9999MHz 80.0 to 124.9999MHz			6	15	mA	RL=∞
				7	15	mA	T=25°C
	125.0 to 150			8	15	mA	(Standard CL: 15pF)
							1 /
	1.0 to 39.99			6	15	mA	CL=0p
Supply Current	40.0 to 79.9	999MHz		7	15	mA	RL=∞
(no load):	80.0 to 124.	9999MHz		8	15	mA	T=25°C
	125.0 to 150MHz 1.0 to 39.9999MHz			9	15	mA	(CL option: 25pF)
				7	15	mA	CL=0p
	40.0 to 79.9	999MHz		8	15	mA	RL=∞
	80.0 to 124.	9999MHz		9	15	mA	T=25°C
	125.0 to 150)MHz		10	15	mA	(CL option: 40pF)
Ontrod Walters		V_{OH}	$0.8*V_{dd}$			V	
Unithiit Voltage.		V_{OL}			0.2*V _{dd}	V	CL=15, 25, 40pF
Tr Tf Tf Tr Tr Tr Tr Tr		Tr		1.8	3.0	ns	CL=15pF; T=25°C
		Tf		1.0	3.0	ns	20%/80%*VDD
		Tr		1.5	3.0	ns	CL=25pF; T=25°C
		Tf		1.2	3.0	ns	20%/80%*VDD
		Tr		1.4	3.0	ns	CL=40pF; T=25°C
		Tf		1.1	3.0	ns	20%/80%*VDD





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Key Electrical Specifications – $V_{dd} = 1.8V$

	 100		F=100MHz CL=15pF
Cycle to Cycle Jitter:	 55	 ps	F=100MHz CL=25pF
	 55	 Î	F=100MHz CL=40pF
	 12		F=100MHz CL=15pF
Period Jitter RMS:	 12 10	 ps	F=100MHz CL=15pF F=100MHz CL=25pF

Key Electrical Specifications –V_{dd}= 2.5V

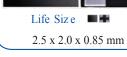
Parameters		Minimum	Typical	Maximum	Units	Notes	
1.0 to 39.9999MHz			6	15	mA	CL=0p	
40.0 to 79.99 80.0 to 124.9		999MHz		7	15	mA	RL=∞
		9999MHz		8	15	mA	T=25°C
	125.0 to 150MHz			9	15	mA	(Standard CL: 15pF)
	1.0 to 39.99	99MHz		7	15	mA	CL=0p
Supply Current	40.0 to 79.9	999MHz		8	15	mA	RL=∞
(no load):	80.0 to 124.	9999MHz		9	15	mA	T=25°C
	125.0 to 150)MHz		10	15	mA	(CL option: 25pF)
	1.0 to 39.99	99MHz		8	16	mA	CL=0p
	40.0 to 79.9	999MHz		9	16	mA	RL=∞
	80.0 to 124.	9999MHz		10	16	mA	T=25°C
	125.0 to 150)MHz		11	16	mA	(CL option: 40pF)
		V_{OH}	$0.8*V_{dd}$			V	
Output Voltage:		V_{OL}			0.2*V _{dd}	V	CL=15, 25pF
Output Voltage.		V_{OH}	$0.9*V_{dd}$			V	
		V_{OL}			$0.1*V_{dd}$	V	CL=40pF
		Tr		1.0	2.0	ns	CL=15pF; T=25°C
		Tf		0.9	2.0	ns	20%/80%*VDD
Rise Time:		Tr		1.1	2.0	ns	CL=25pF; T=25°C
Fall Time:		Tf		0.9	2.0	ns	20%/80%*VDD
		Tr		1.0	2.0	ns	CL=40pF; T=25°C
		Tf		0.9	2.0	ns	20%/80%*VDD
Period Jitter RMS:			6.5			F=100MHz CL=15pF	
			5		ps	F=100MHz CL=25pF	
			5			F=100MHz CL=40pF	
				80			F=100MHz CL=15pF
Cycle to Cycle Jit	ter:			40		ps F=100MHz	F=100MHz CL=25pF
			40			F=100MHz CL=40pF	











Key Electrical Specifications -V_{dd}= 3.3V

Parameters		Minimum	Typical	Maximum	Units	Notes	
	1.0 to 39.9999MHz			7	15	mA	CL=0p
40.0 to 79.9				8	15	mA	RL=∞
	80.0 to 124.9999			9	15	mA	T=25°C
	125.0 to 150			10	15	mA	(Standard CL: 15pF)
	1.0 to 39.99	99MHz		8	16	mA	CL=0p
Supply Current	40.0 to 79.9	999MHz		9	16	mA	RL=∞
(no load):	80.0 to 124.	9999MHz		10	16	mA	T=25°C
	125.0 to 150)MHz		11	16	mA	(CL option: 25pF)
	1.0 to 39.99	99MHz		8	16	mA	CL=0p
	40.0 to 79.9	999MHz		9	16	mA	RL=∞
	80.0 to 124.	9999MHz		10	16	mA	T=25°C
	125.0 to 150)MHz		11	16	mA	(CL option: 40pF)
		V_{OH}	$0.8*V_{dd}$			V	
Outrout Valtaga		V_{OL}			$0.2*V_{dd}$	V	CL=15pF
Output Voltage:		V_{OH}	$0.9*V_{dd}$			V	
		V_{OL}			$0.1*V_{dd}$	V	CL=25, 40pF
		Tr		1.0	2.0	ns	CL=15pF; T=25°C
		Tf		0.9	2.0	ns	20%/80%*VDD
Rise Time:		Tr		1.0	2.0	ns	CL=25pF; T=25°C
Fall Time:		Tf		0.9	2.0	ns	20%/80%*VDD
		Tr		0.8	2.0	ns	CL=40pF; T=25°C
				0.8	2.0	ns	20%/80%*VDD
	Period Jitter RMS:			6			F=100MHz CL=15pF
Period Jitter RMS				5		ps	F=100MHz CL=25pF
				5			F=100MHz CL=40pF
				80			F=100MHz CL=15pF
Cycle to Cycle Jit	ter:			40		ps	F=100MHz CL=25pF
				40			F=100MHz CL=40pF





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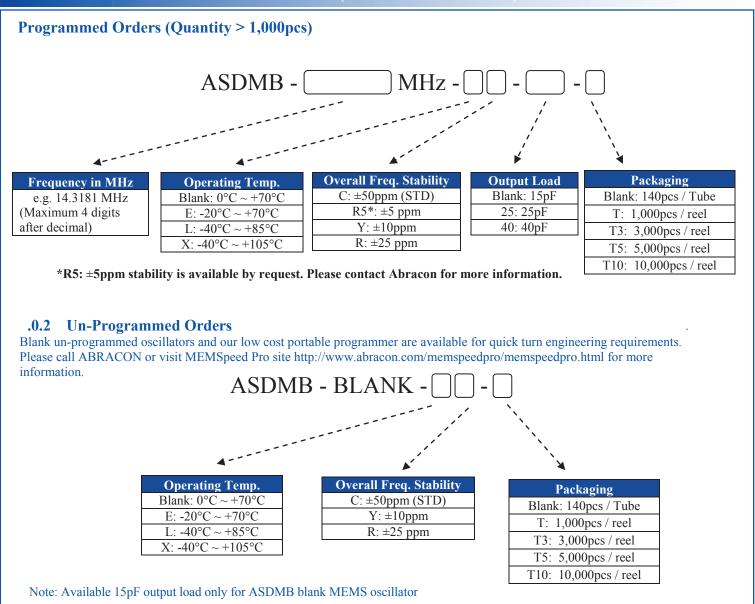




Absolute Maximum Ratings

Item	Minimum	Maximum	Unit	Condition
Supply Voltage	-0.3	+4.0	V	
Input Voltage	-0.3	Vdd+0.3	V	
Junction Temp.		+150	°C	
Storage Temp.	-55	+150	°C	
Soldering Temp.		+260	°C	40sec max
ESD			V	
HBM		4,000		
MM		200		
CDM		1,500		

OPTIONS AND PART IDENTIFICATION: (Left Blank if Standard)





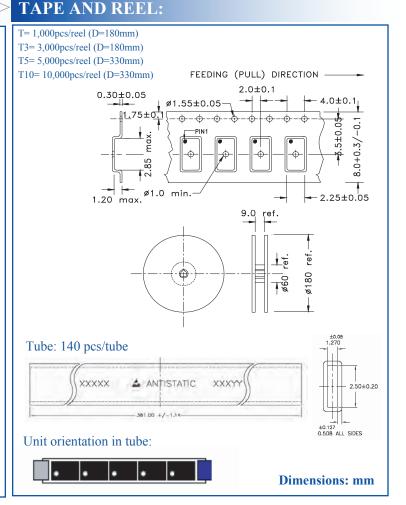




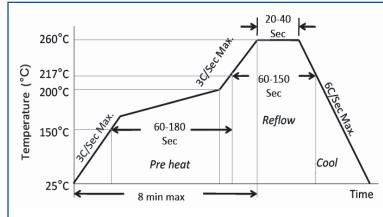




OUTLINE DIMENSIONS: 2.5±0.05 [0.098±0.002] 1.65 [0.065] #4 #3 #3 #4 0.7 [0.028] CO.25 #2 0.65±0.05 [0.026±0.002] 2.0±0.05 [0.079±0.002] 0.65±0.10 [0.026±0.004] Pin Terminal No Standby 1 2 GND 3 Output 0.85±0.05 [0.033±0.002] 4 VDD Recommended Land Pattern Note: Recommend using an approximately 0.01uF 0.7 [0.028] bypass capacitor between PIN 2 and 4. 0.85 [0.033] 0.2 [0.008] 0.65 [0.026] Dimensions: mm (inches)



REFLOW PROFILE:



Ramp-Up Rate (200°C to Peak Temp)	3°C/Sec Max.
Preheat Time 150°C to 200°C	60-180 Sec
Time maintained above 217°C	60-150 Sec
Peak Temperature	255-260°C
Time within 5°C of actual Peak	20-40 Sec
Ramp-Down Rate	6°C/Sec Max.
Time 25°C to Peak Temperature	8 min Max.



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