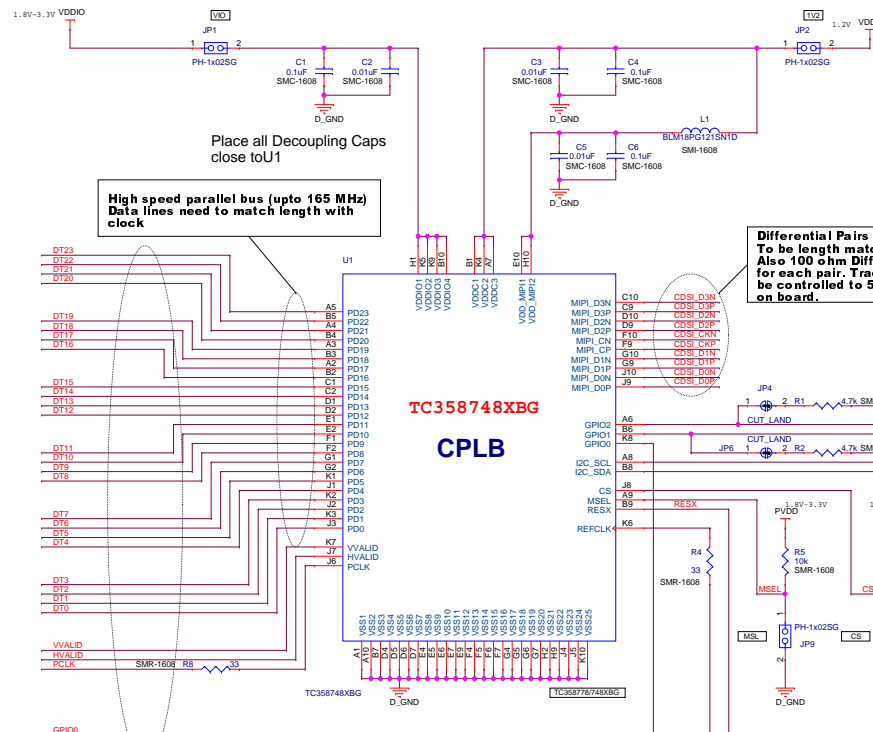
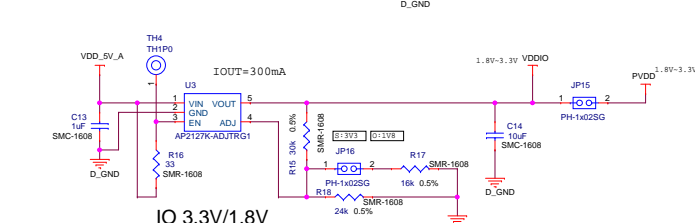
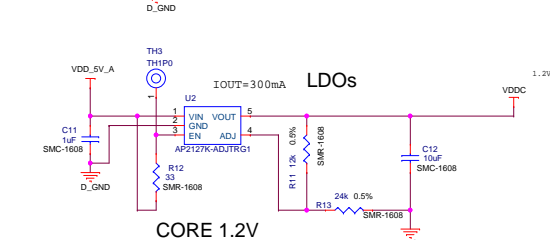
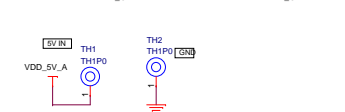
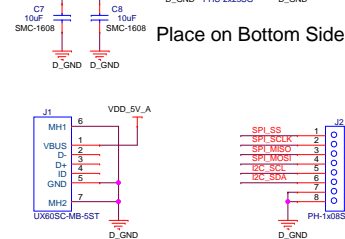
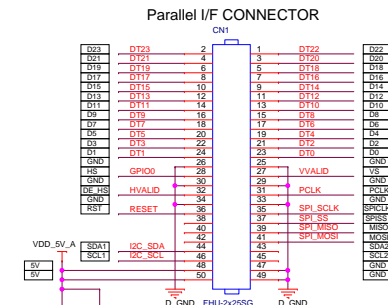


# MPD-CPLB-B1

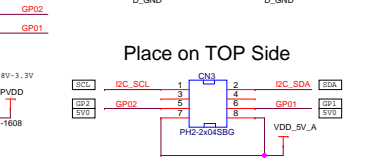
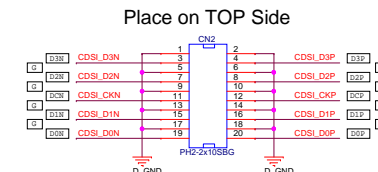
Rev. 1.2b

PAGE #	TITLE DESCRIPTION
1	COVER PAGE (THIS PAGE)
2	GPLB-WAIV

REV.	CONTENTS	DATE
1.0	ISSUED	2016/11/29
1.1	Change R14 value	2017/04/10
1.2	Change C7 /C8 value	2017/05/30
1.2b	Change R14 value	2017/07/25



**Differential Pairs in general:**  
To be length matched  
Also 100 ohm Differential impedance for each pair. Trace impedance should be controlled to 50 ohms from ground on board.



MSL JP9	CS JP10	TC358778	TC358748
Short	Short	Test Mode	CSI to Para(Parallel out on)
Short	Open	Test Mode	CSI to Para(Parallel out off (Hiz))
Open	Short	I2C	Para to CSI (I2C)
Open	Open	SPI	Para to CSI (SPI)

