

# Gigabit Ethernet HAT

## Schematic Document

2025-12-08

REV: 0.1

Sheet: /COVER/  
File: cover.kicad\_sch

**Title:**

Size: A4

Date:

KiCad E.D.A. 9.0.2

**Rev:**

Id: 1/7

# Revision History

DATE	REV	
25/12/31	0.1	The first edition

Sheet: /REVISION/  
File: revision.kicad\_sch

Title:

Size: A4Date:KiCad E.D.A. 9.0.2

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COVER

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REVISION

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LICENSE

PAGE 7

文件: license.kicad\_sch

MISC

PAGE 4

PHY0\_RXC<  
PHY0\_RXCTL<  
PHY0\_RXD0<  
PHY0\_RXD1<  
PHY0\_RXD2<  
PHY0\_RXD3<  
PHY0\_TXC<  
PHY0\_TXCTL<  
PHY0\_TXD0<  
PHY0\_TXD1<  
PHY0\_TXD2<  
PHY0\_TXD3<

PHY\_INT<  
PHY\_REFCLK<

PHY1\_RXC<  
PHY1\_RXCTL<  
PHY1\_RXD0<  
PHY1\_RXD1<  
PHY1\_RXD2<  
PHY1\_RXD3<  
PHY1\_TXC<  
PHY1\_TXCTL<  
PHY1\_TXD0<  
PHY1\_TXD1<  
PHY1\_TXD2<  
PHY1\_TXD3<

文件: misc.kicad\_sch

PHY1

PHY\_INT<  
PHY\_RXC<  
PHY\_RXCTL<  
PHY\_RXD0<  
PHY\_RXD1<  
PHY\_RXD2<  
PHY\_RXD3<  
PHY\_TXC<  
PHY\_TXCTL<  
PHY\_TXD0<  
PHY\_TXD1<  
PHY\_TXD2<  
PHY\_TXD3<

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PHY\_AD2< GND  
PHY\_AD1< GND  
PHY\_AD0< +1V8

PHY\_MDC<  
PHY\_MDIO<

PHY2

PHY\_INT<  
PHY\_RXC<  
PHY\_RXCTL<  
PHY\_RXD0<  
PHY\_RXD1<  
PHY\_RXD2<  
PHY\_RXD3<  
PHY\_TXC<  
PHY\_TXCTL<  
PHY\_TXD0<  
PHY\_TXD1<  
PHY\_TXD2<  
PHY\_TXD3<

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PHY\_AD2< GND  
PHY\_AD1< +1V8  
PHY\_AD0< GND

PHY\_MDC<  
PHY\_MDIO<

Sheet: /  
File: nethat.kicad\_sch

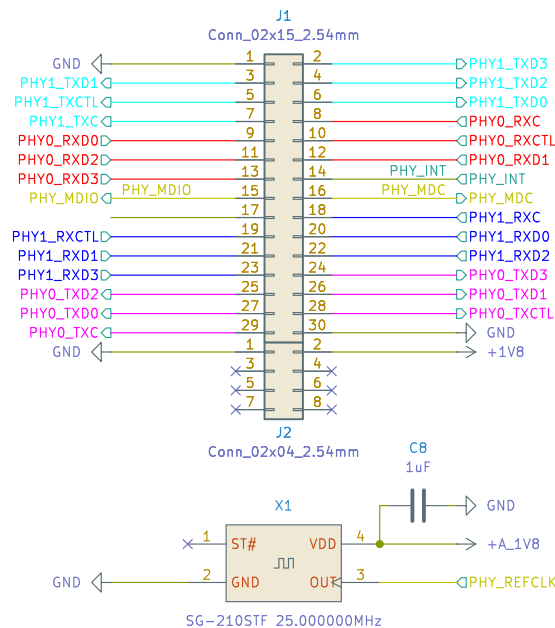
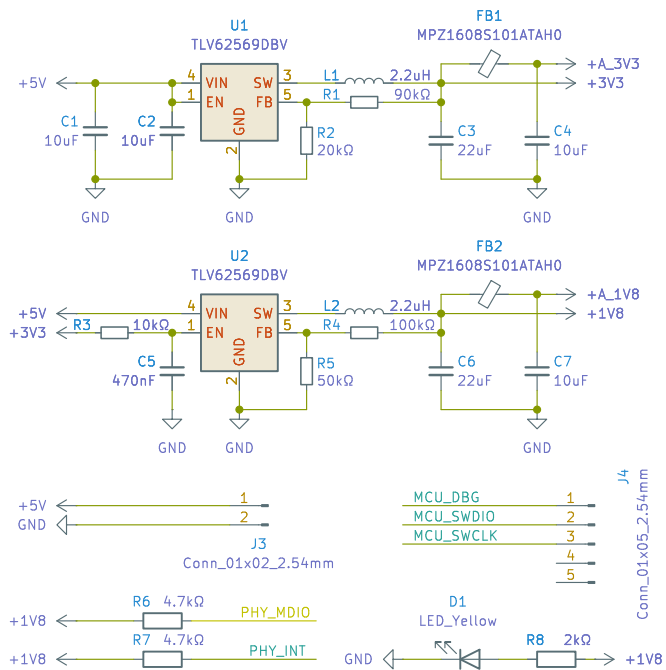
Title:

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File: misc.kicad\_sch

# Title:

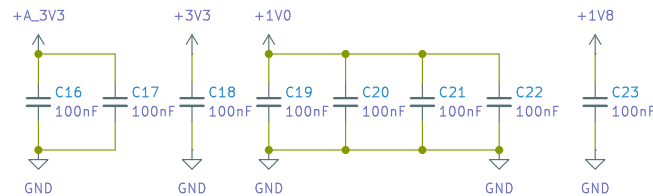
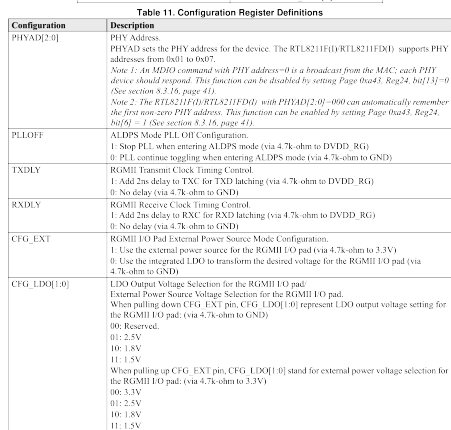
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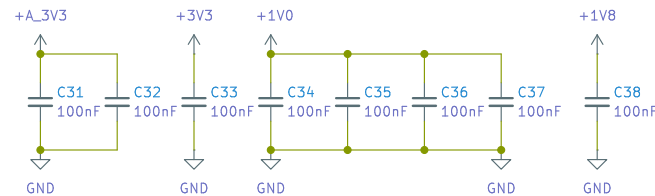
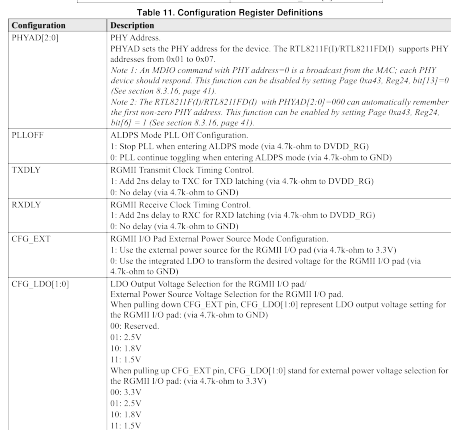
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