

EMCU Component Stress Analysis

- All EMCU components are being evaluated to insure that manufacturer absolute maximum ratings are not exceeded for: voltage, current, power, and temperature, where applicable.
- Moog Electronics Management Plan specifies SD-18 (*Naval Part Requirement & Application Guide*).
 - For COTS IC's, SD-18 Guideline "Normal Environ" category has been used to account for lack of specification for using COTS parts in "Severe Environ".
 - "Severe Environ" determines EMCU component derating levels, defined as: uninhabited environments, varying temperature extremes (-55 °C to +125 °C), medium to high shock & pressure, >10yr storage life, 10yr to 20yr application life.

Stress Derating Criteria

Connector (per RAC derating) :

- Pin Current = 70% of I_{max}
- Pin-Pin Voltage = 70% of V_{max}
- Temp = $T_{max} - 50^{\circ}C$

Capacitors:

• Film

- Voltage = 55% of V_{max}
- Temp = $T_{max} - 10^{\circ}C$

• Ceramic

- Voltage = 60% of V_{max}
- Temp = $T_{max} - 10^{\circ}C$

• Tantalum

- Voltage = 60% of (V_{max} @ temp)
- Temp = $T_{max} - 10^{\circ}C$

Resistors (Low Power) :

• Film

- Power = 70% of (P_{max} @ temp)
- Voltage = 70% of V_{max}

• Wirewound (high power)

- Power = 55% of (P_{max} @ temp)
- Voltage = 70% of V_{max}

Magnetics:

•Transformer

- $I = 80\%$ of I_{pk}
- $V = 80\%$ of V_{max}
- Temp (hot spot) = $T_{max} - 30^{\circ}\text{C}$

•Inductor

- $I = 80\%$ of I_{pk}
- $V = 80\%$ of V_{max}
- Temp (hot spot) = $T_{max} - 30^{\circ}\text{C}$

Discrete Semiconductors

(assume SD-18 "Normal" Environment)

•Rectifier

- Junction Temp = $T_{jmax} - 40^{\circ}\text{C}$
- $I_f = 85\%$ of I_{max}
- Reverse Voltage = 75% of V_{max}

•Zener

- Junction Temp = $T_{jmax} - 40^{\circ}\text{C}$
- Power Dissipation = 80% of P_{max}

•Transient Suppressor

- Junction Temp = $T_{jmax} - 40^{\circ}\text{C}$
- Power Dissipation = 80% of P_{max}

•Bipolar Transistor

- Junction Temp = $T_{jmax} - 40^{\circ}\text{C}$
- $V_{CE} = 75\%$ V_{CEmax}

•MOSFET

- Junction Temp = $T_{jmax} - 40^{\circ}\text{C}$
- $V_{ds} = 75\%$ of V_{dsmax}

Integrated Circuits

(assume SD-18 "Normal" Environment) :

•Linear IC's

- Junction Temp = $T_{jmax} - 40^{\circ}\text{C}$
- $V_{min} < V_{sup} < V_{max}$

•Digital IC's

- Junction Temp = $T_{jmax} - 40^{\circ}\text{C}$
- $V_{min} < V_{sup} < V_{max}$