Antonio Vásquez Briones

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Las Condes -Santiago - Chile

# Perfil Profesional

Ingeniero Civil en Automatización con más de seis años de experiencia, habiéndose desempeñado los últimos tres, como Ingeniero de Calidad de Software para productos de Optimización de Síntesis de Circuitos Integrados para una compañía de Estados Unidos. Tres años anteriores trabajando en Enseñanza Técnica en tópicos como Microcontroladores, Redes Industriales y Control Automático y años anteriores de experiencia como Integrador de tecnologías de operación e instrumentos de campo. Actualmente en busca de oportunidades en Transformación Digital y Desarrollo de Software.

# Educación

* **Universidad del Bío-Bío, Concepción Chile.**Ingeniero Civil en Automatizacion, 2014

Licenciado en Ciencias de la Ingeniería 2013

* **Liceo Industrial Metodista, Coronel Chile**.

Técnico en Electrónica, 2007.

# Experiencia Profesional

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-Also for HPDRT:

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| **04-2018 to date** |  | **Sr. Quality Assurance Engineer. Synopsys Inc. US, Sucursal Santiago.**  Desarrolla, mantiene y aplica estándares de calidad para el principal producto de la Compañía **Design Compiler**, un motor de optimización de síntesis de circuitos integrados, estándar en la industria de semiconductores, que permite obtener chips más pequeños, rápidos y con menor consumo de energía.   * Asegura la calidad y estabilidad durante todo el ciclo de desarrollo del producto, mediante regresiones periódicas masivas de nightly-builds, ejecutadas en granjas Unix, desarrollando e implementando mecanismos de extracción de resultados, seguimiento de cambios y monitoreo de la infraestructura critica. Aplicando posteriormente, criterios de aceptación que permitan aprobar o rechazar el lanzamiento de nuevas versiones y service-packs a clientes importantes de Silicon-Valley como Apple, AMD, NVIDIA, Google, Facebook, etc. * Diseña y ejecuta pruebas a nuevas funcionalidades del producto, detectando defectos y su causa raíz, asistiendo luego a los desarrolladores en la depuración del código. * Executes software test plans for new features, detecting issues and the root causes of them. Analyzes and writes test standards maintains documentation of test results to assist in debugging and modification of software. Analyzes test results to ensure existing functionality and recommends corrective action. * Work as technical lead on automation of processes along with partners from Shanghai, Bangalore and US. * During the stay at the company has attend training at Shanghai China Company Facilities. |
| **02-2018 and 07-2018** |  | **Capacita OTIC, Arduino Raspberry and IoT SENCE Bootcamp Teacher.**   * Leads the one-week SENCE intensive program for teachers from the TI department of INACAP-Chile. During 4 consecutive years. * The program consists in several lessons and activities to prepare teachers from all the country in IoT, Arduino, Raspberry and several open-source technologies. |
| **03-2015 to 03-2018** |  | **Universidad Tecnológica de Chile INACAP Concepción-Talcahuano, Full Time Instructor.**  **(*Technological University of Chile*)**   * Full-time Academic of the Electrical, Electronics and Automation department, teaches mainly Control Systems, Robotics, PLC’s, Microcontrollers and Industrial Networks classes. Teaches and trains adult and youth. * SENCE Corporate Training Programs like Industrial Instruments and PLC. * Design and Implementation of Domotic Technologies at facilities. * Leads periodic open workshops of Robotics and Programming for high-school students. |
| **07-2013 to 03-2015** |  | **Liceo Industrial Metodista de Coronel, Full time High-School Teacher.**   * Full-Time teacher for High-School Electronics Technicians, works mainly with teenagers. * **Lead Teacher of the Robotics Group. First Prize at *Inacap Robotics Challenge 2014.*** |
| **07-2011 to 07-2012** |  | **Industrial Instruments Field Technician**   * Integrator, Maintenance, commissioning and troubleshooting of field instrumentation for several power plants in BíoBío Region, Chile. Serves as English translator for companies from various locations around the world. |
| **03-2009 to 12-2012** |  | **Universidad del Bío-Bío, Campus Concepción.**   * Teaching Assistant at Calculus, Motion Control and Systems courses. |

# Additional Skills

* Self-learner, quick to getting up to speed, fast to learn (code). Proficient implementing theory and algorithms on code in order to give simple solution to real problems.
* Computer literacy: Python -Django, Pandas, Celery, Scikit Learn- C, C++, Tcl, Processing, OpenFrameworks (c++), ogre3d, ActionScript, General Shell Script, HTML, CSS, javascript.
* Operational /Industrial technologies: OPC-DA, OPC-UA, modbus, profibus, TCP/IP, user and integration experience.
* As hobby and free-time activity, enjoys play and script with front-end or visual languages, like Processing, Open Frameworks and hardware stuff like Arduino, ESP8266, and microchip microcontrollers. Tinker with sensors, midi controllers, and musical visualizations.
* Musician; song-composer, singer and guitarist.

**Other**

* *“Reconocimiento Lideres del Sur”* prize given by “El Sur” newspaper by his cooperation in the divulgation of technology and robotics to teenagers and young people at social risk. 27-dec-2017. [[news archive inacap](http://www.inacap.cl/tportalvp/aplicaciones/noticias_movil/noticia.php?t=63&n=54298)]