





EXTERNAL SIGNALS

The following signals must be integrated into all des

Signals

EXT_I2C_SCL/3.5Q te: Additional I2C pull-up r

EXT_I2C_SDA/3.5G

DW_SYNC/3.10C

STM_INTERRUPT/3.5F

Antennas __________RF1/3.6A Guarantee 120° offset in-between

RF2/3.6B

RF traces should respect the kee Furthermore, try to keep them as

Power Supply

(+3U3/3.10K) Be aware that the DecaWave is v

We suggest using the "MAX8887EZ You can find a reference layout igns using the design block:

resistors required to +3V3

ı antennas to maximize polarization difference and antenna diversity

epout zones and be surrounded by a via shield. short and straight as possible

ery sensitive regarding its power supply.

?K33+T" from Maxim Integrated.
at github.com/lab11/polypoint/pcb/tritag.

TriTag C
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Date: 20.07.18 18:15 Sheet: 4/4