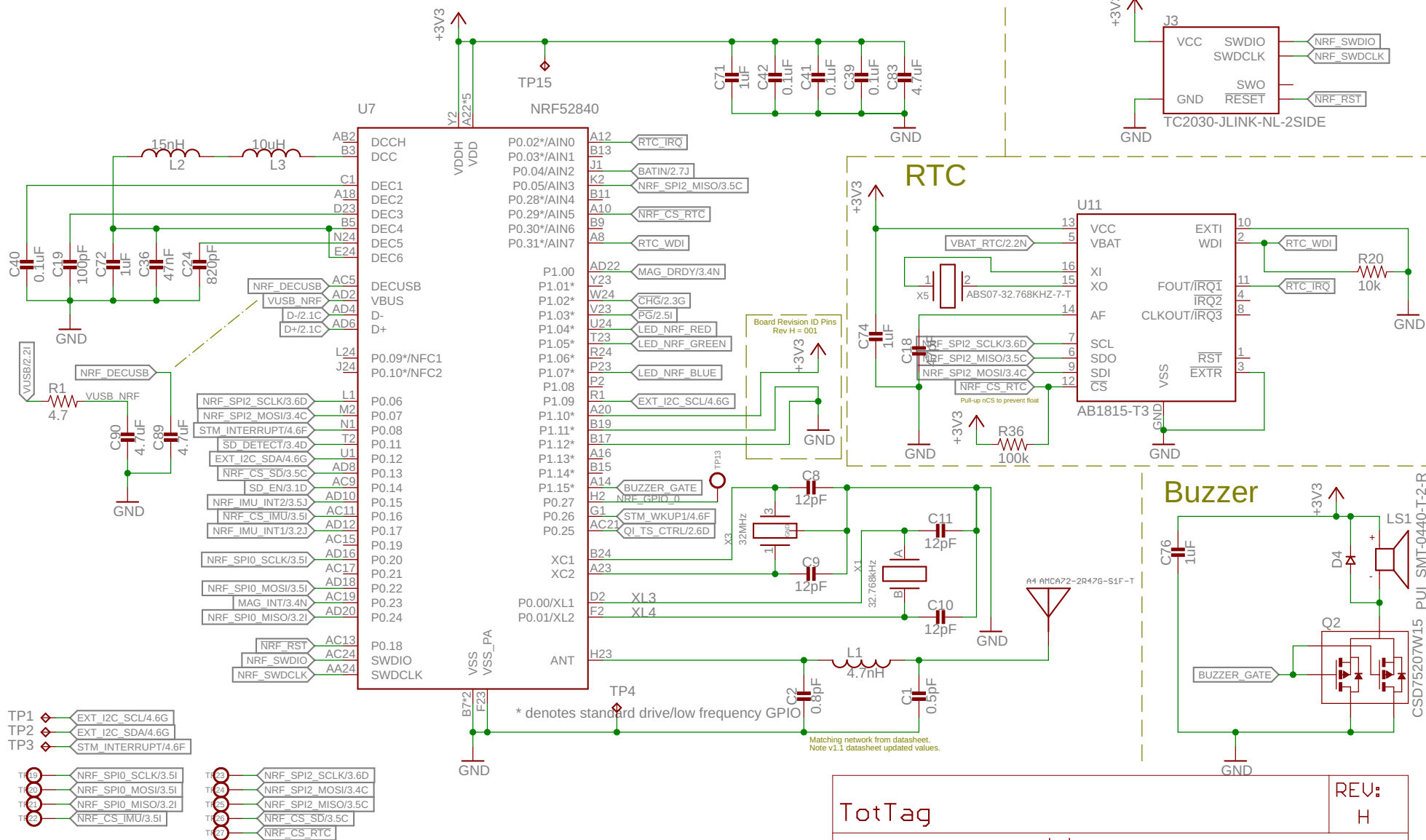


nRF52840 BLE

Do not use I2C pull-up
(NRF has internal pull-up)



LED J-LINK x2 (top/bottom)

RTC

Buzzer

TotTag

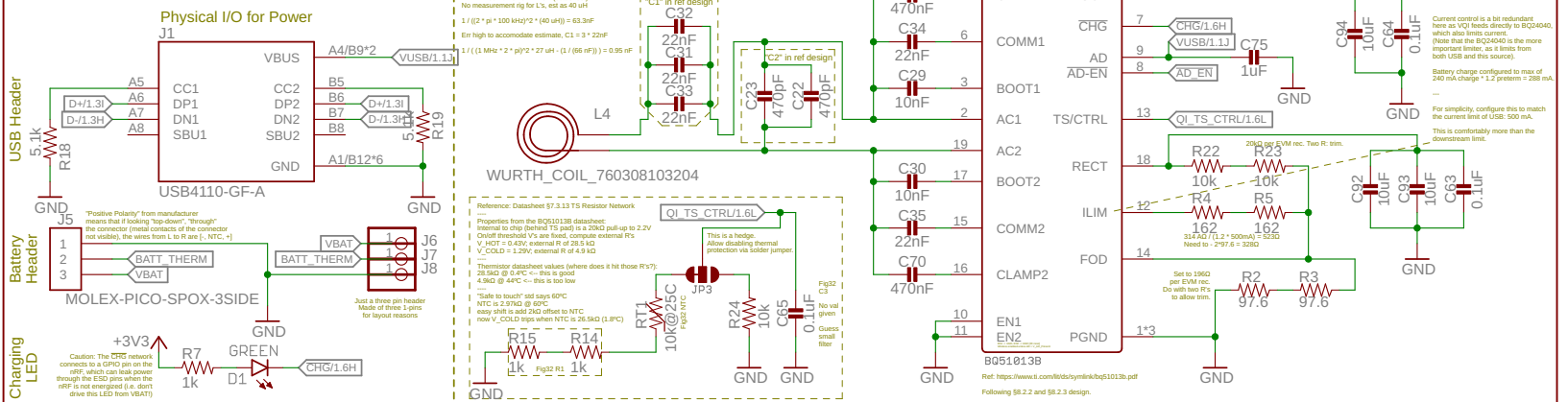
Author: Andreas Biri & Pat Pannuto

Date: 2/19/21 11:22

REV: H

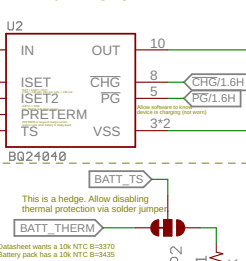
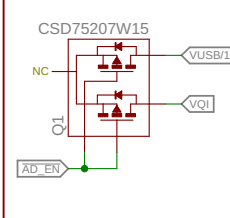
Sheet: 1/5

Physical I/O for Power



Attach VUSB to V_QI
when Qi charging IC allows

11. *Chrysomelidae* (Colorado potato beetle)



ISet controls constant charge current
Battery wants $0.2C \cdot 1200 \text{ mAh} = 240 \text{ mA}$

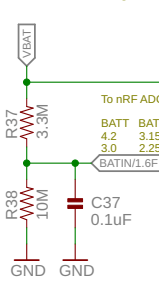
§9.2.1.2.1.1 Eqn
 $R_{\text{ISET}} = [540\Omega / 0.24A] = 2.25k\Omega$

Datasheet wants a 10k NTC B=3370
Battery pack has a 10k NTC B=3435

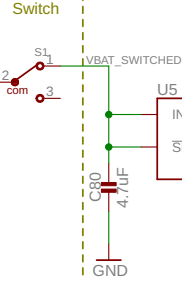
At 0°C:
 $1/273 = 1/298 + 1/3435 \cdot \ln(R/10000)$; R = 28.7 kΩ
 $1/273 = 1/298 + 1/3370 \cdot \ln(R/10000)$; R = 28.2 kΩ

At 60°C:
 $1/333 = 1/298 + 1/3435 \cdot \ln(R/10000)$; R = 2.98 kΩ
 $1/333 = 1/298 + 1/3370 \cdot \ln(R/10000)$; R = 3.05 kΩ

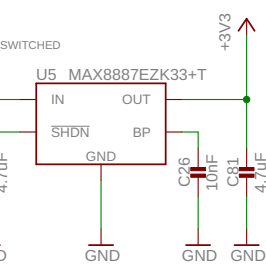
Close enough.



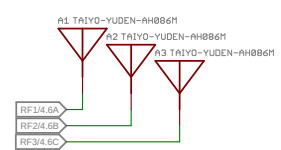
Switch



Must be a stable LDO for the DW1000.



A1 TAIYO-YUDEN-AH086M



VBAT_RTC/1.9G

VBAT ESR must be 1.5kΩ for optimal performance

1.41k R16

B1 +

90 Ohm

RTC Battery

-

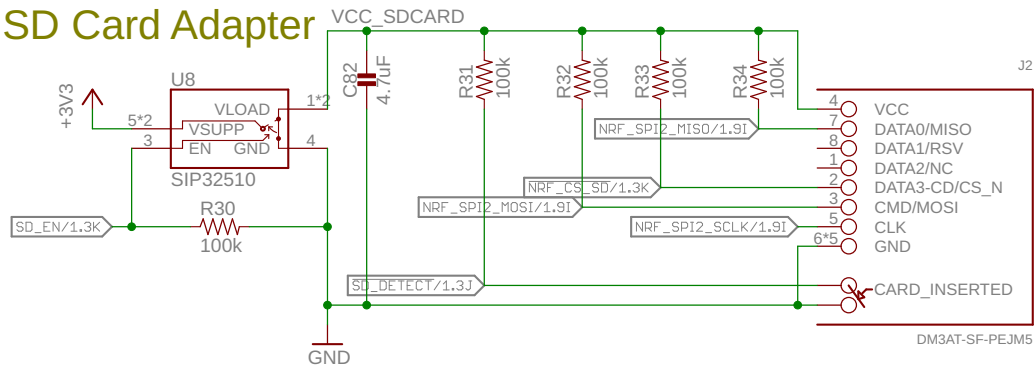
GND

Author: A

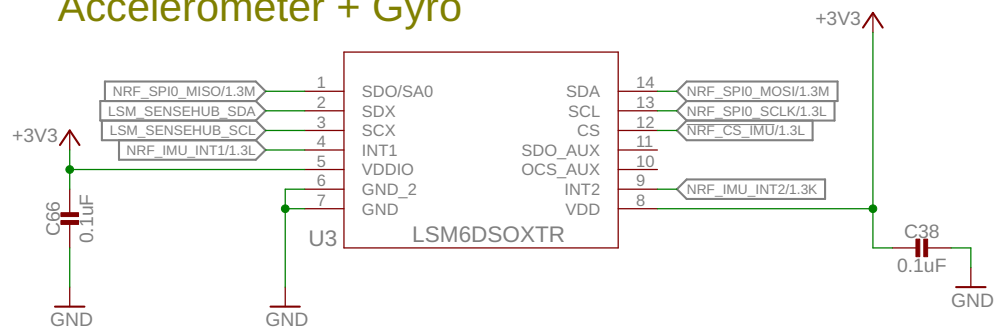
Date: 2/19/21 11:22

REV: H

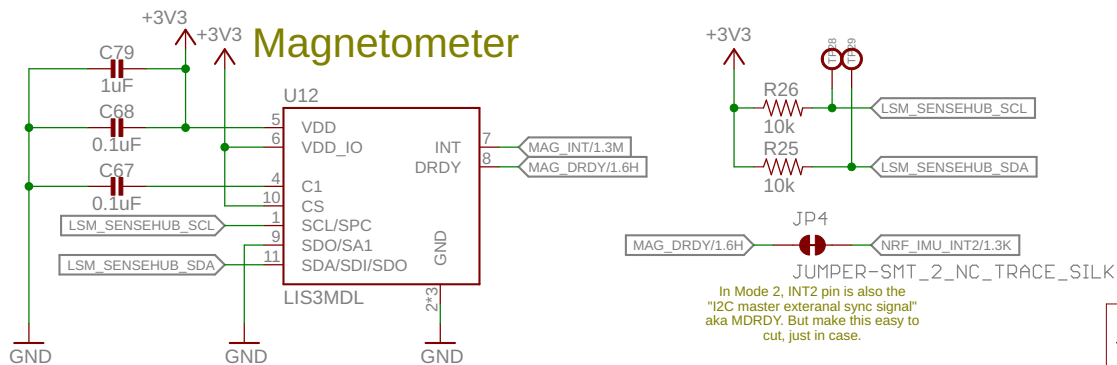
SD Card Adapter



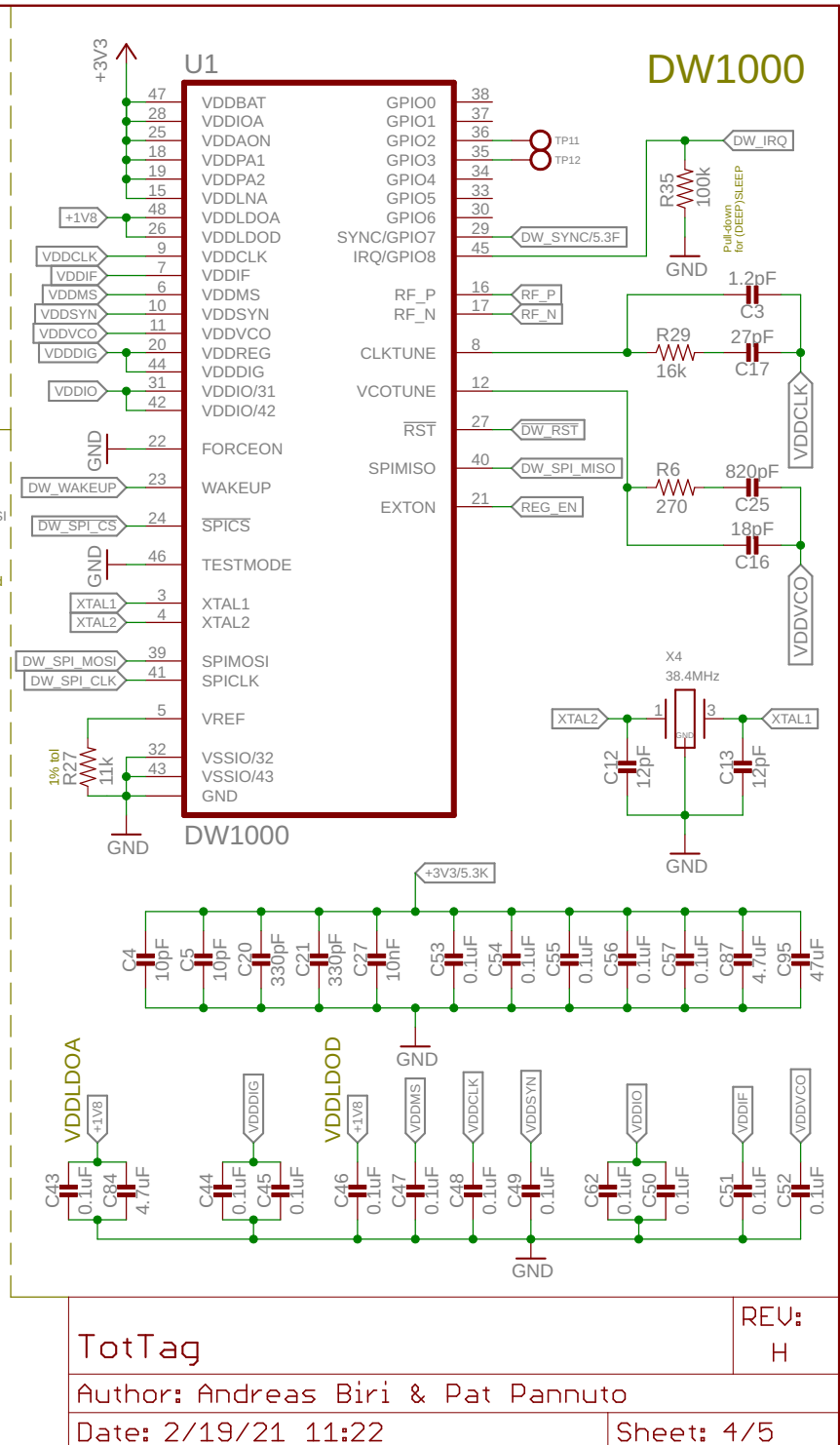
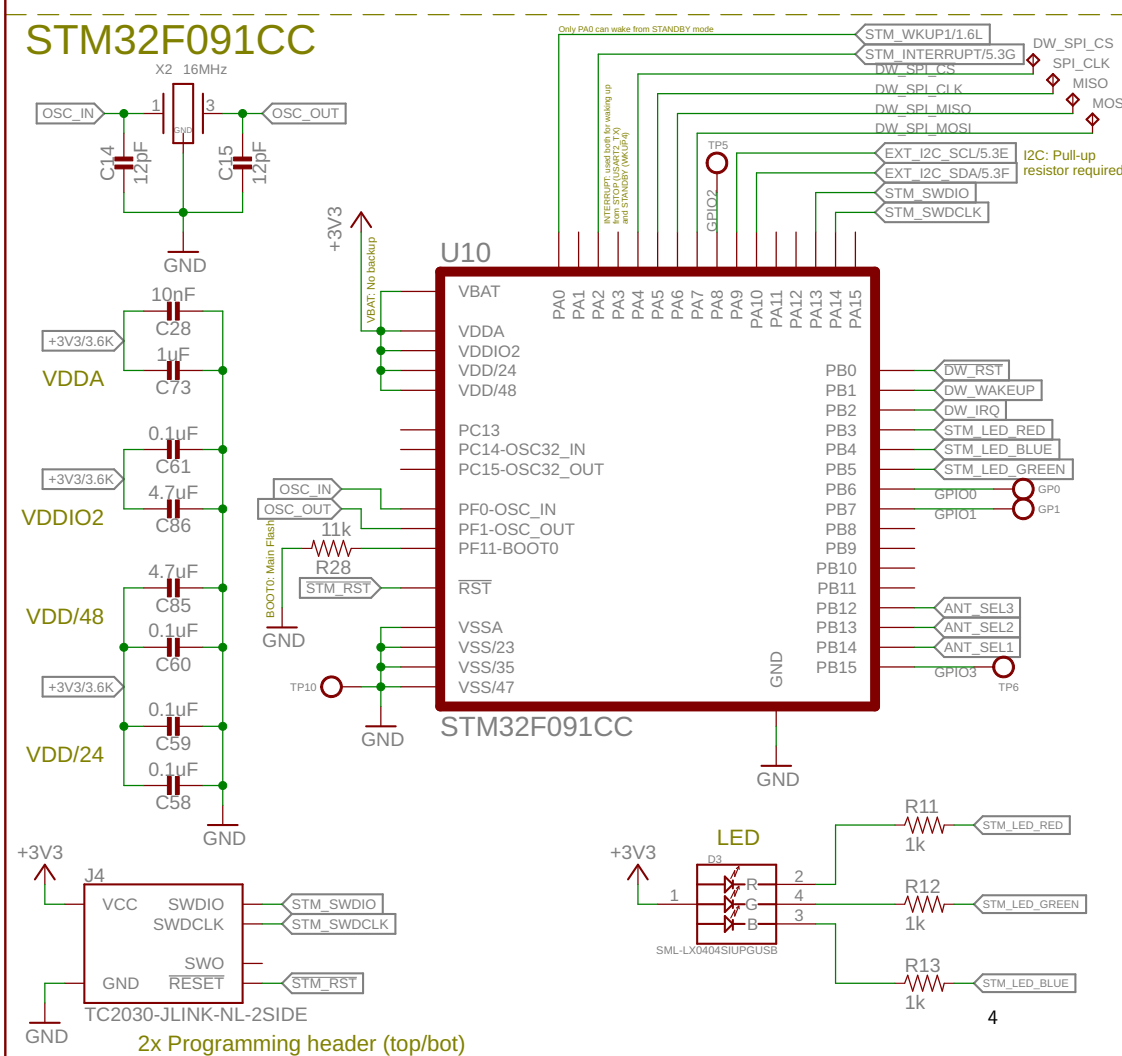
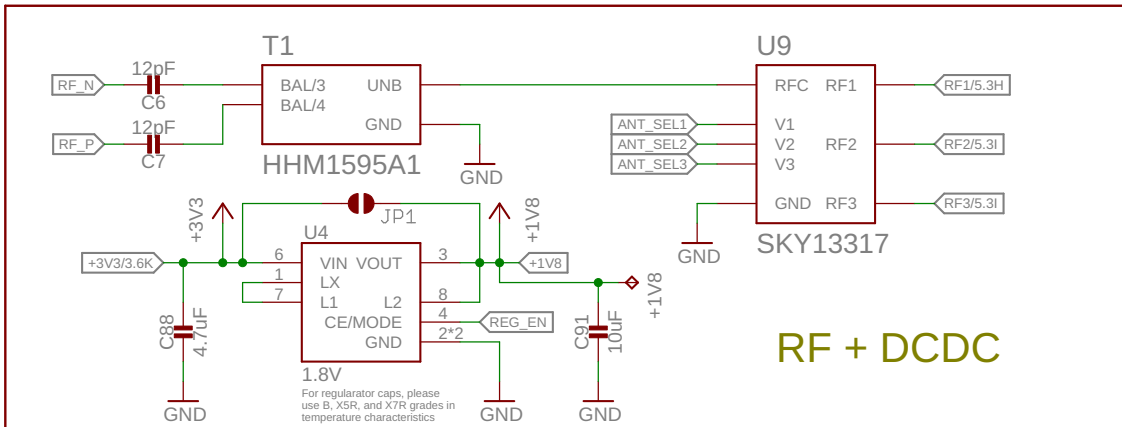
Accelerometer + Gyro



Magnetometer



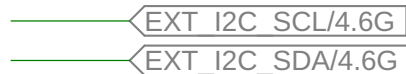
TotTag	REV: H
Author: Andreas Biri & Pat Pannuto	
Date: 2/19/21 11:22	Sheet: 3/5



EXTERNAL SIGNALS

The following signals must be integrated into all designs using the design block:

Signals



Note: Additional I2C pull-up resistors required to +3V3



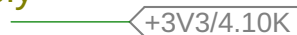
Antennas



Guarantee 120° offset in-between antennas to maximize polarization difference and antenna diversity

RF traces should respect the keepout zones and be surrounded by a via shield.
Furthermore, try to keep them as short and straight as possible

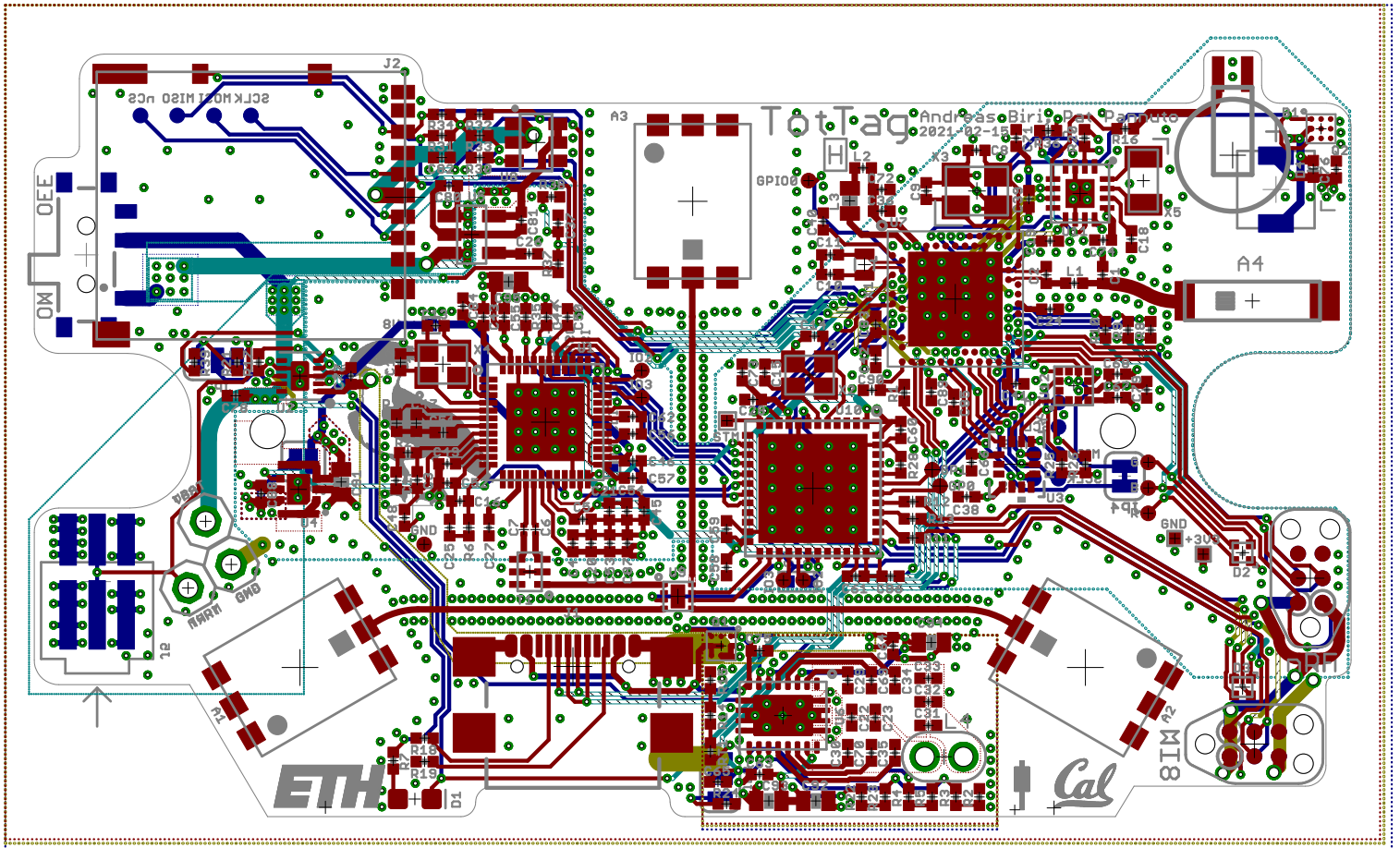
Power Supply

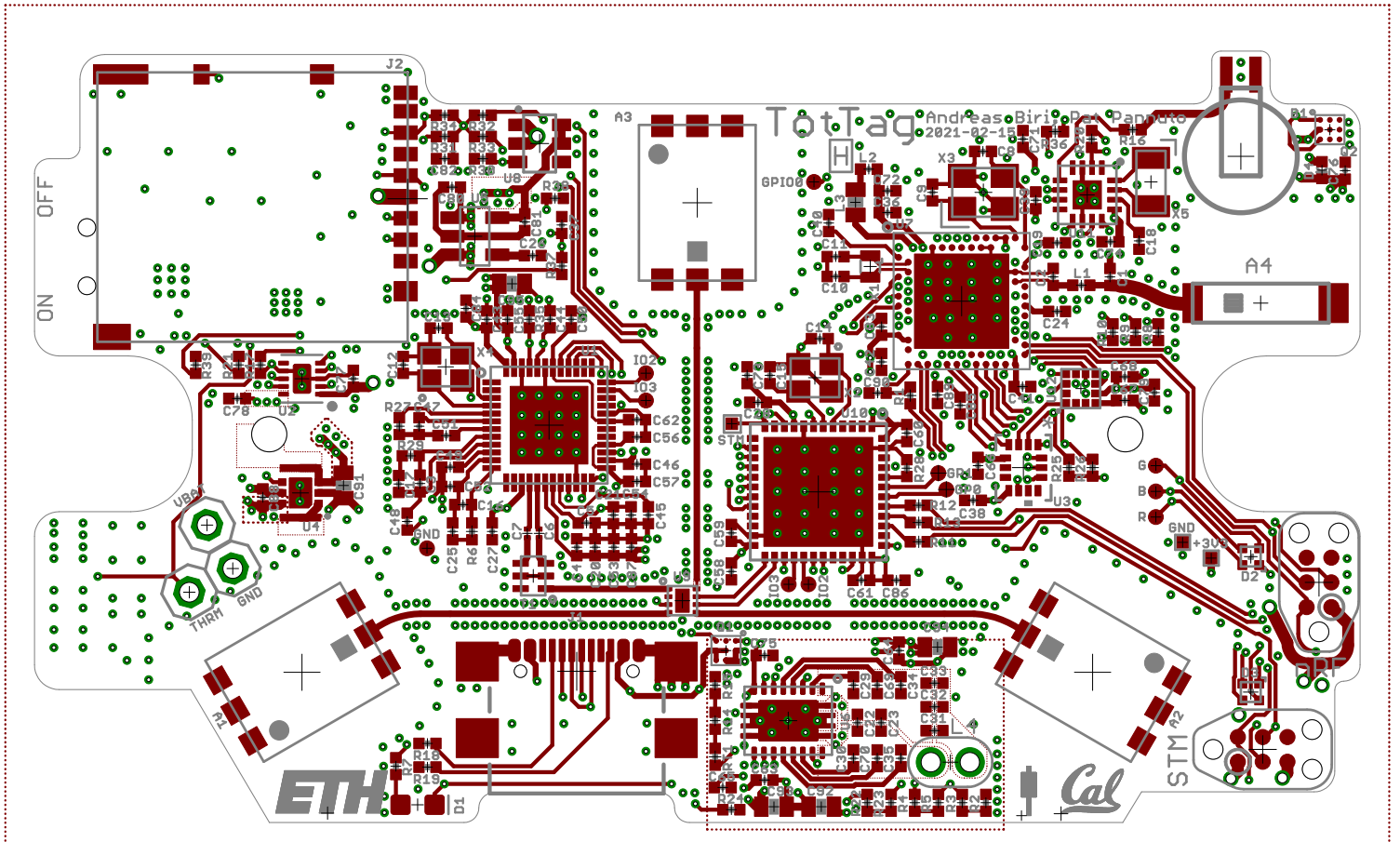


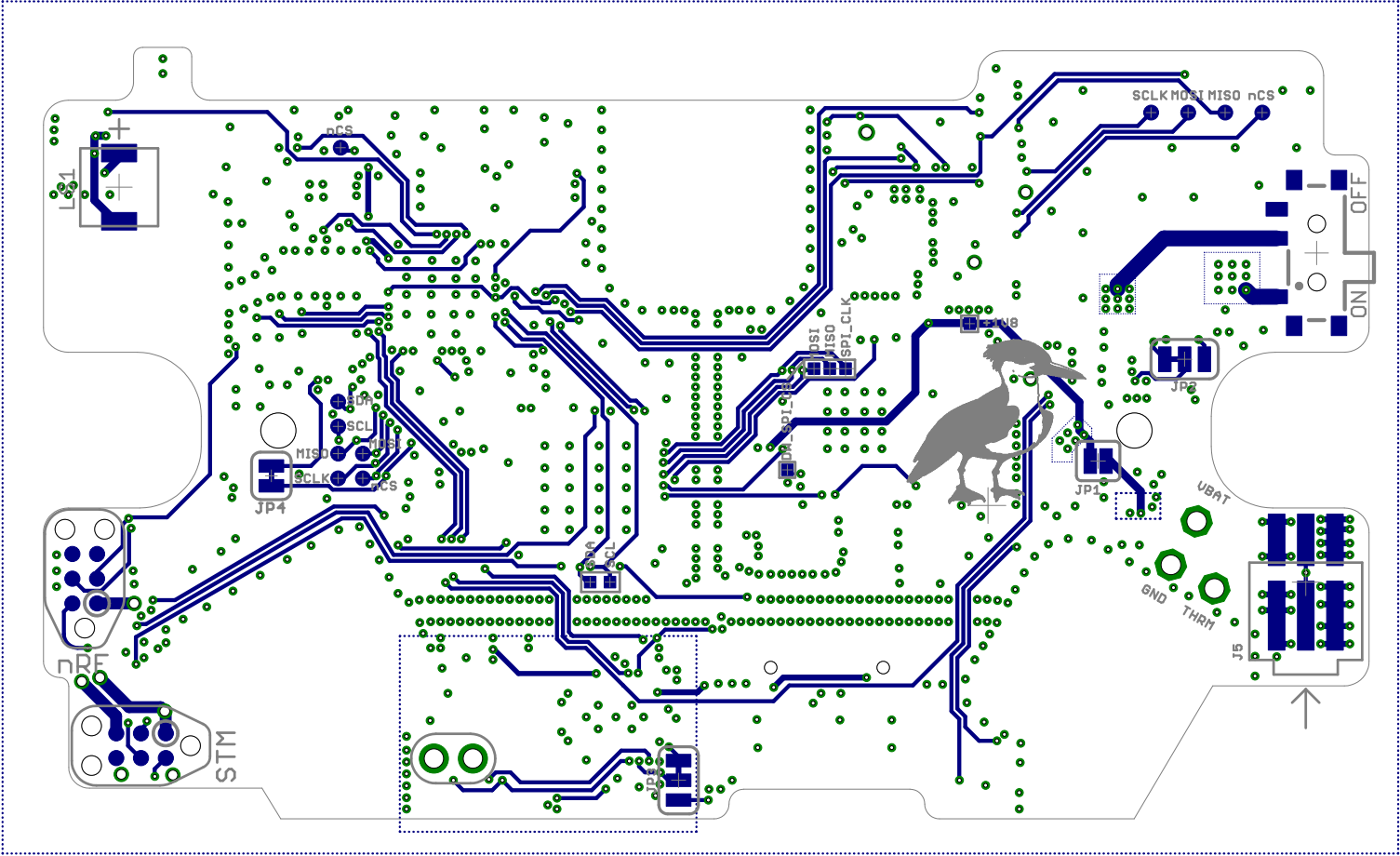
Be aware that the DecaWave is very sensitive regarding its power supply.

We suggest using the "MAX8887EZK33+T" from Maxim Integrated.
You can find a reference layout at github.com/lab11/totternary/hardware/tottag.

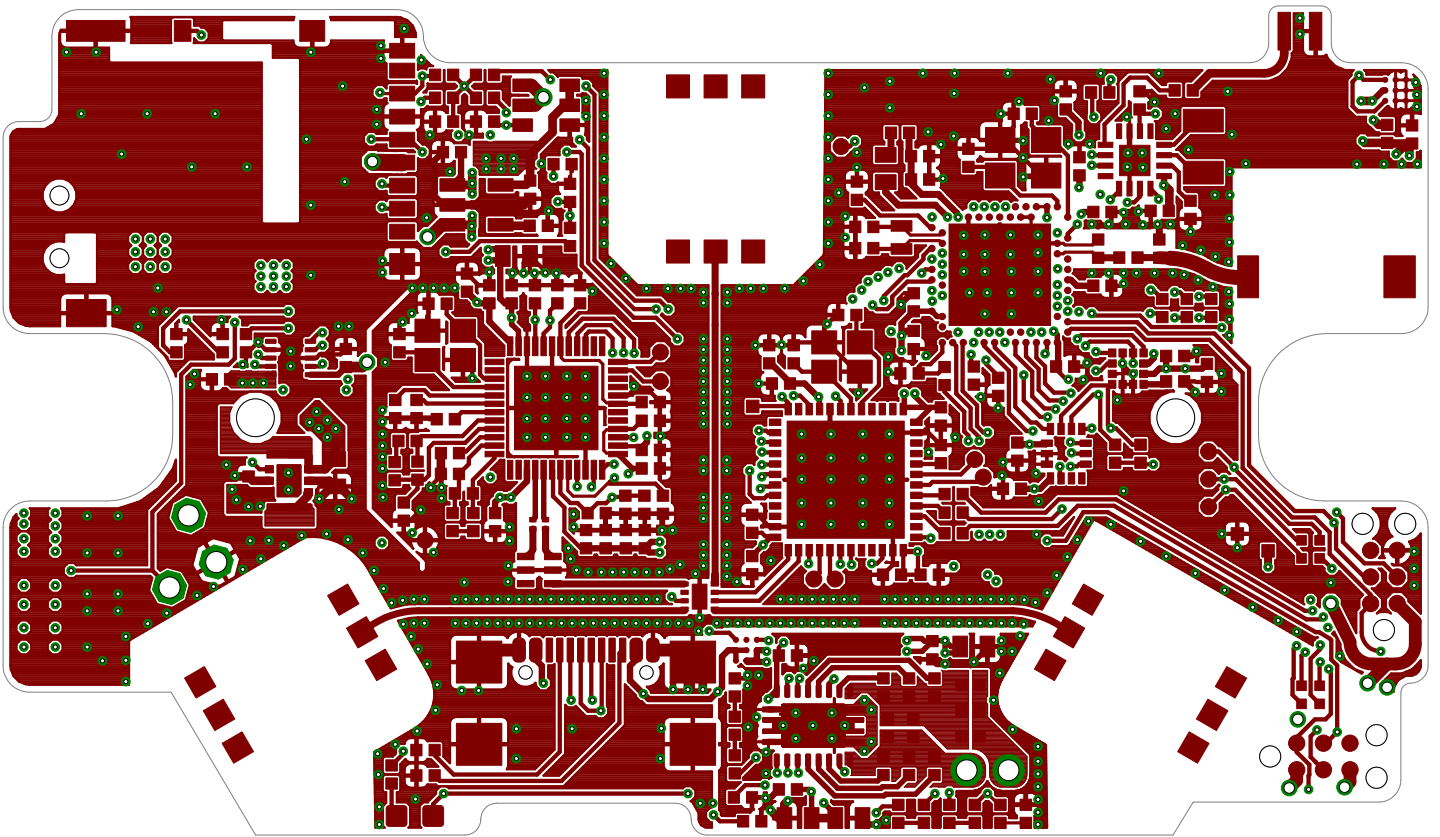
TotTag		REV: H
Author: Andreas Biri & Pat Pannuto		
Date: 2/19/21 11:22		Sheet: 5/5

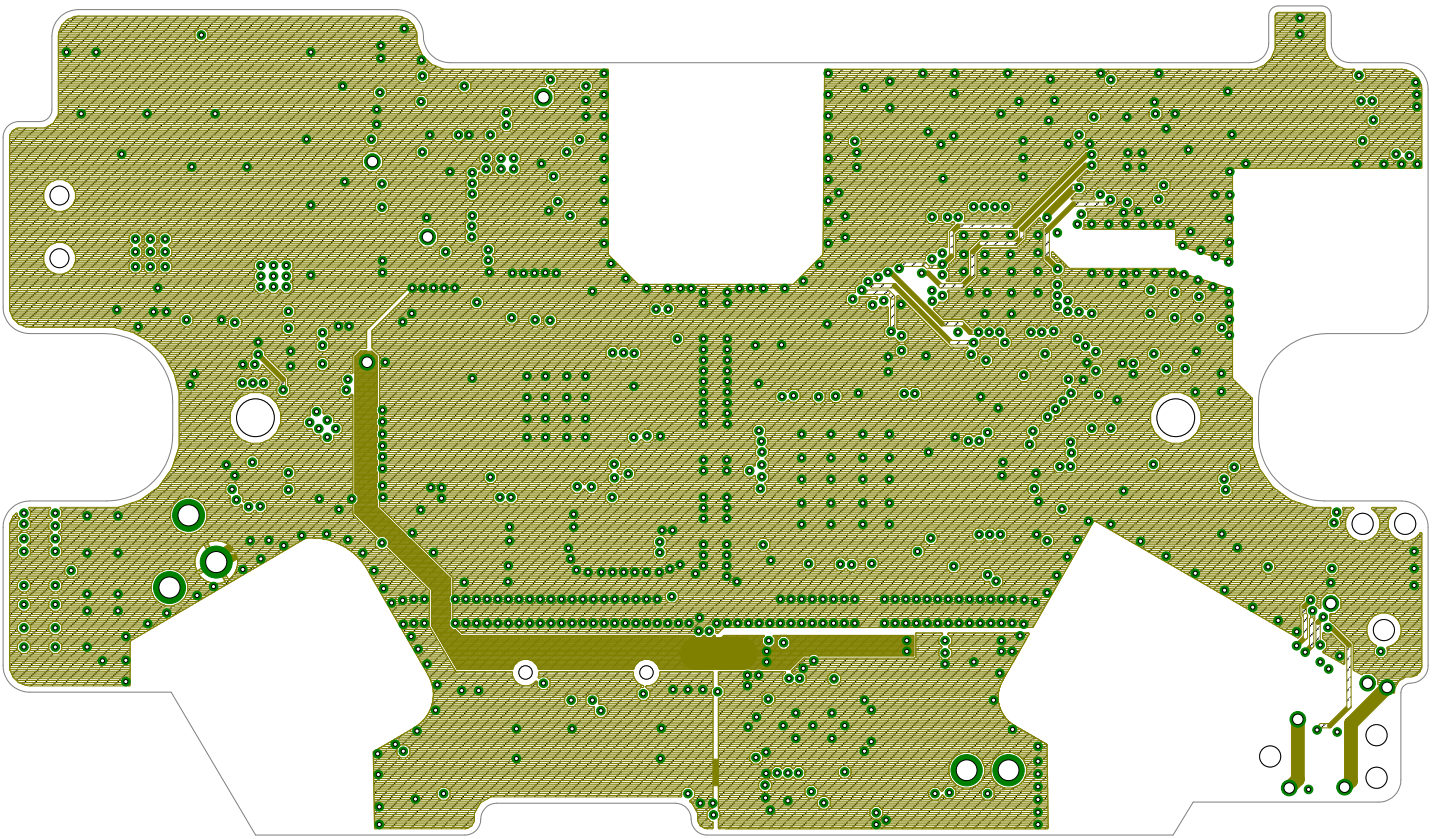


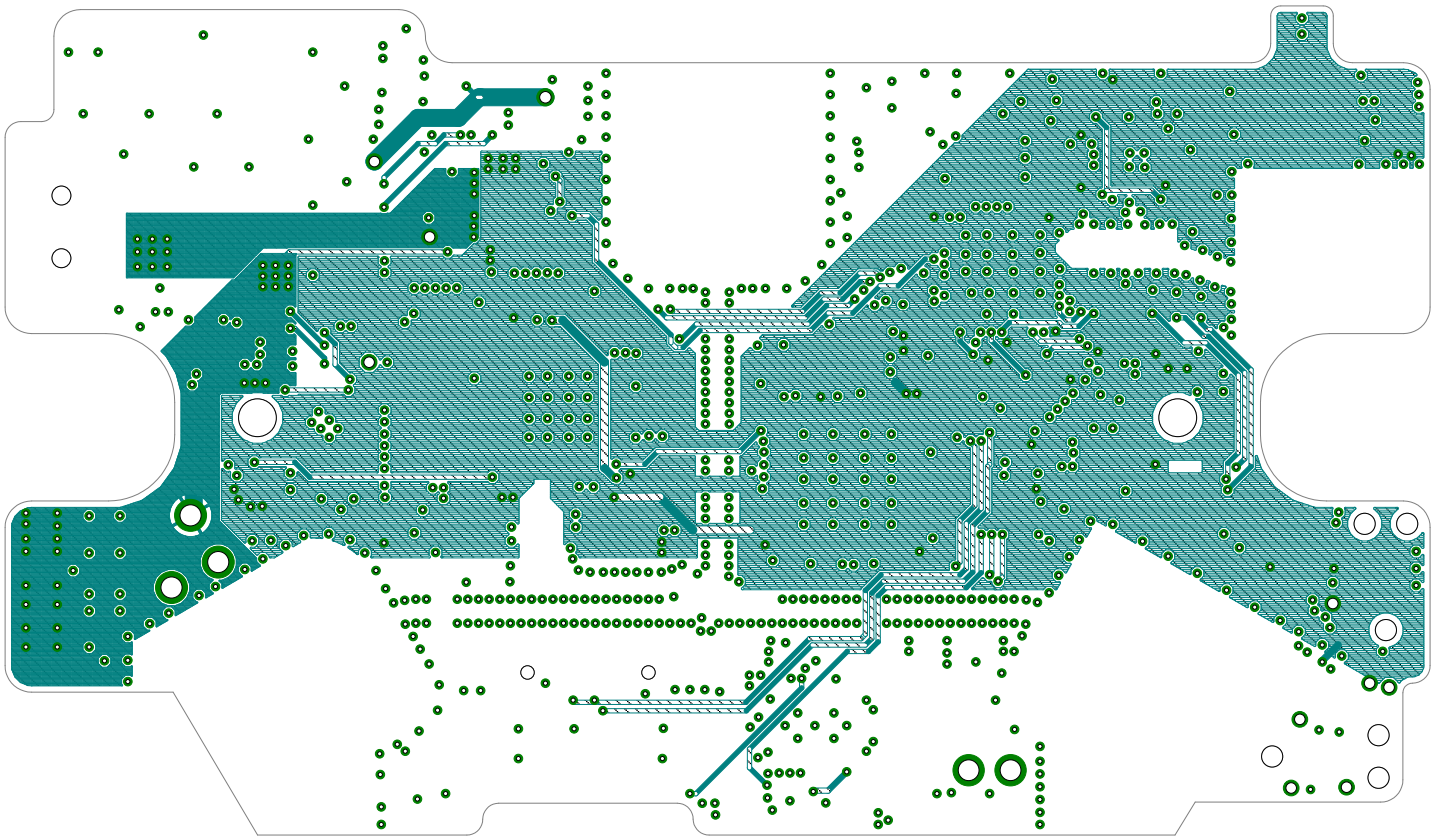




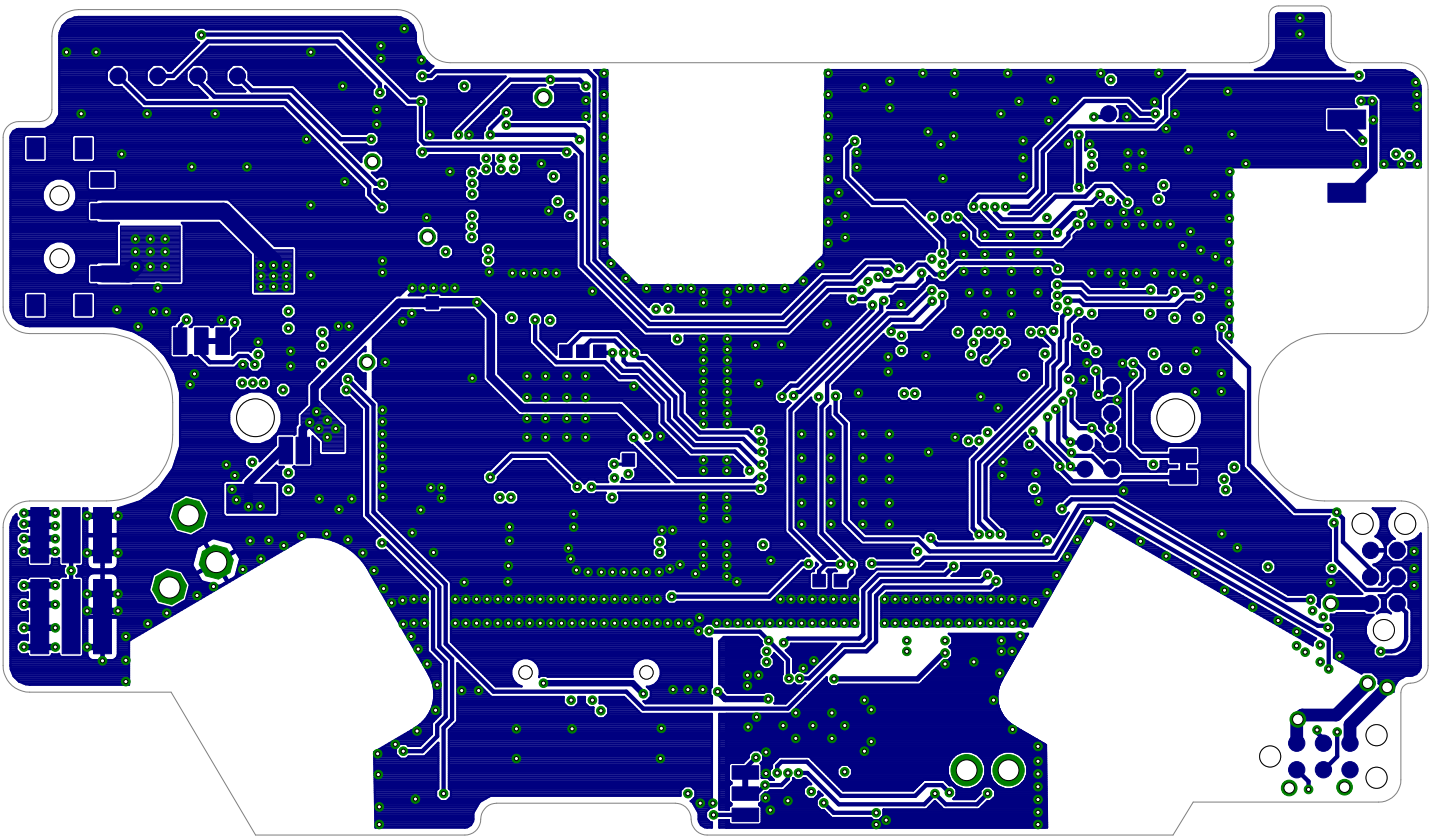
Top Copper Layer

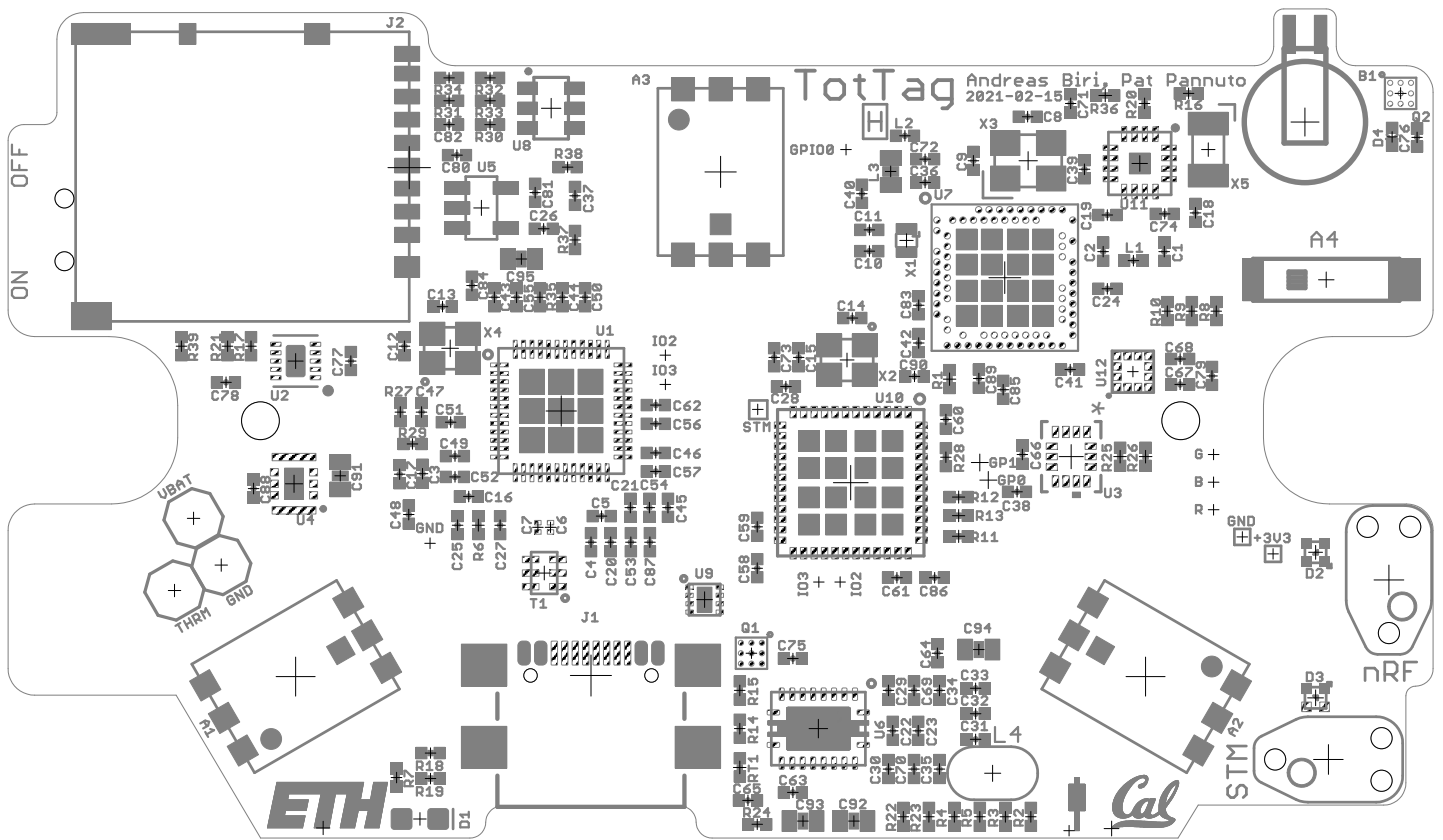




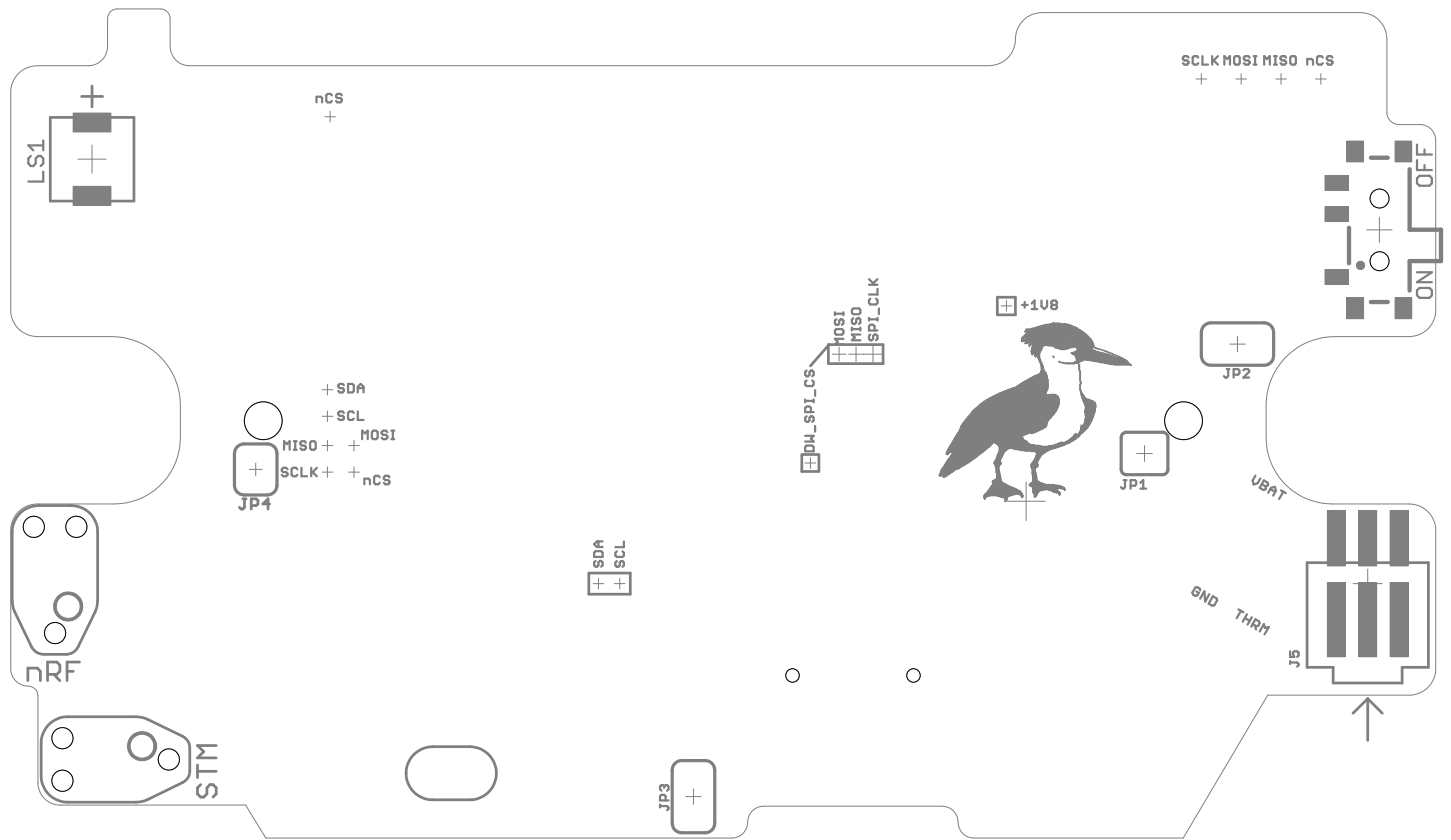


Bottom Copper Layer

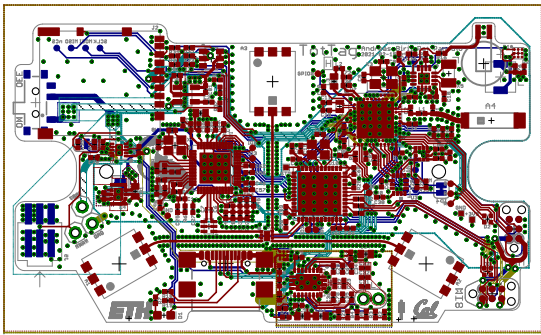


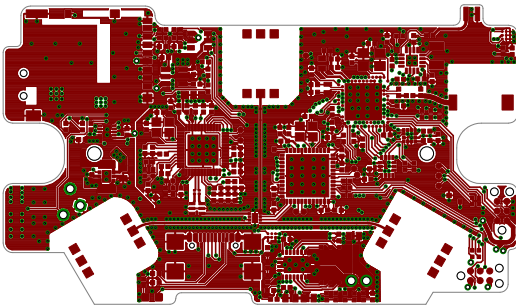


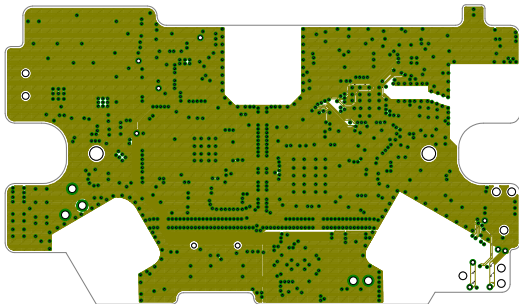
Bottom Paste Layer with Silkscreen

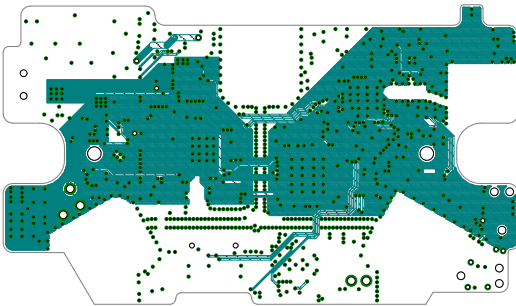


Top and Bottom Layers 1:1 Scale









Bottom Copper Layer 1:1 Scale

