







## EXTERNAL SIGNALS

The following signals must be integrated into all des

Signals

EXT\_I2C\_SCL/3.50 te: Additional I2C pull-up r

EXT\_I2C\_SDA/3.5G

(DW\_SYNC/3.10C

STM\_INTERRUPT/3.5F

Guarantee 120° offset in-between

⟨RF2/3.6B (RF3/3.6C

RF traces should respect the kee Furthermore, try to keep them as

Power Supply

(+3U3/3.10K

Be aware that the DecaWave is v

We suggest using the "MAX8887EZ You can find a reference layout

igns using the design block:

resistors required to +3V3

n antennas to maximize polarization difference and antenna diversity

epout zones and be surrounded by a via shield. short and straight as possible

ery sensitive regarding its power supply.

'K33+T" from Maxim Integrated.
at github.com/lab11/polypoint/pcb/tritag.

TriTag		REV: C
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