

EXTERNAL SIGNALS

The following signals must be integrated into all designs using the design block:

EXT_I2C_SCL/1.5G

EXT_I2C_SDA/1.5G

Note: Additional I2C pull-up resistors required to +3V3

-\DW_SYNC/1.10C

⟨INTERRUPT/1.5F

⟨RF1/1.6A

RF2/1.6B

-<RF3/1.6C

+3V3/1.10K

+1V8/1.10N

TriPoint	t		REV: C
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