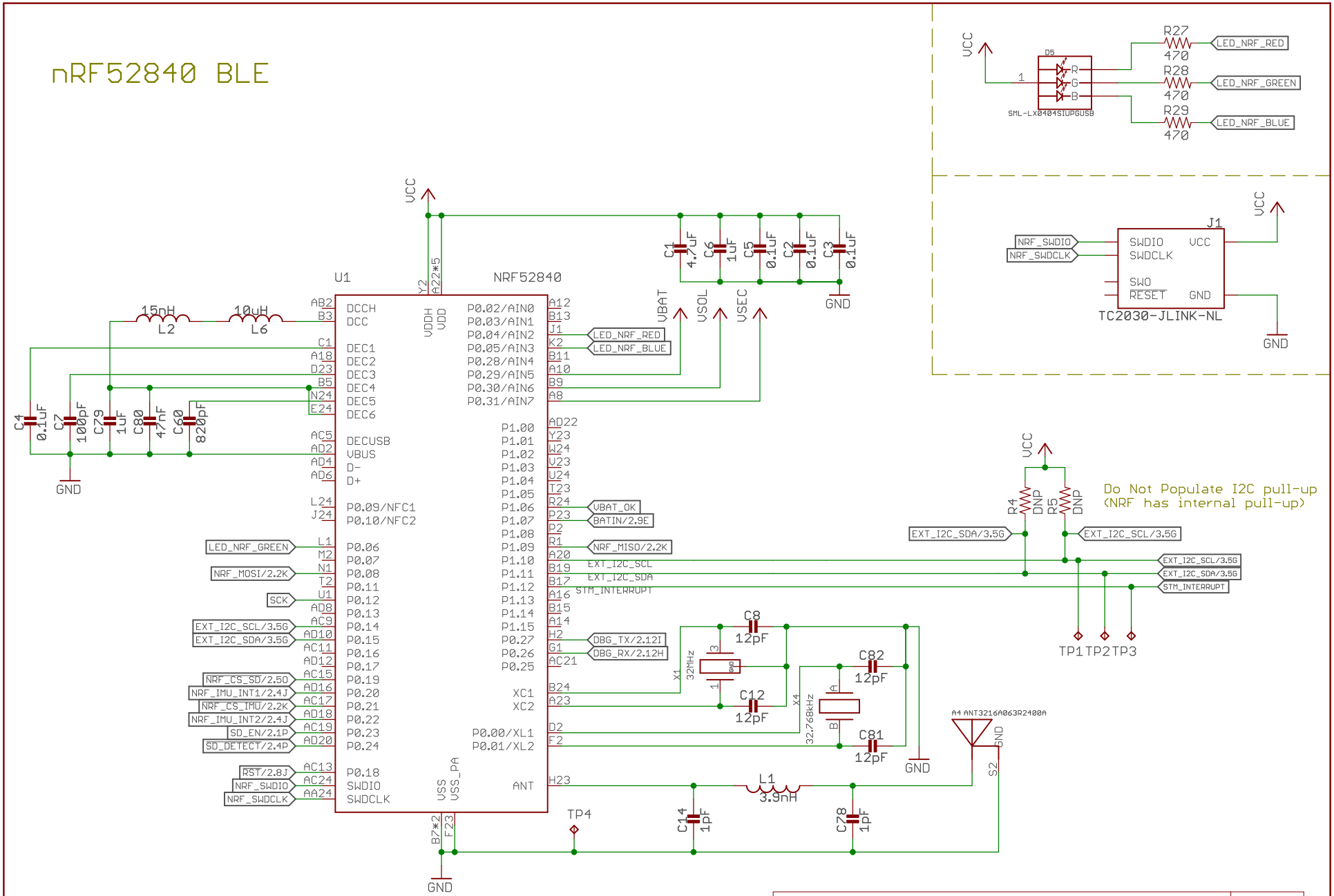
nRF52840 BLE



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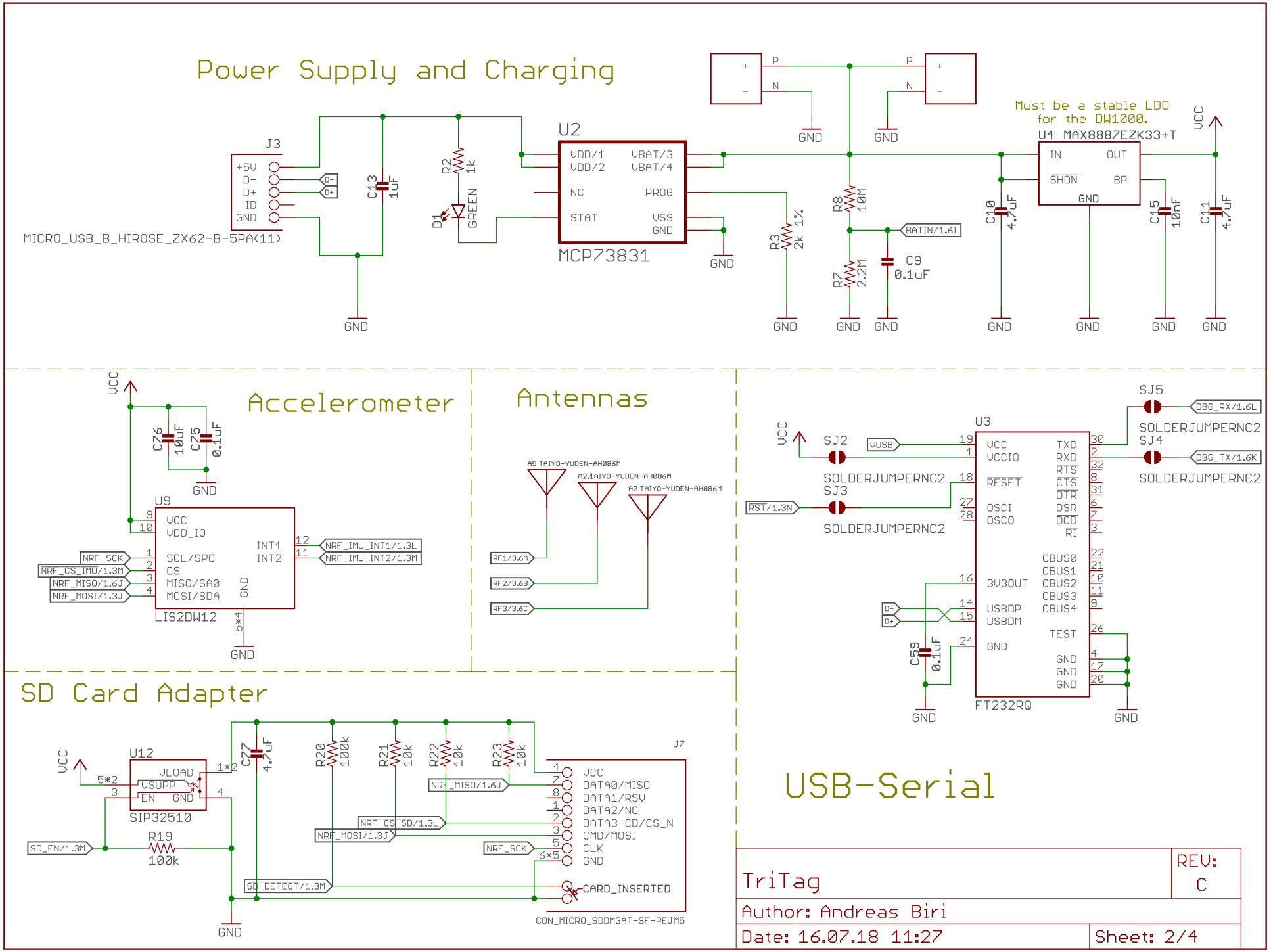
Power Supply and Charging
J3
MICRO_USB_B_HIROSE_ZX62-B-5PA(11)
+5V
D-
D+
ID
GND
C13
1uF
R2
1k
D1
GREEN
U2
MCP73831
VDD/1
VDD/2
NC
STAT
VBAT/3
VBAT/4
PROG
VSS
GND
R3
2k 1%
R8
10M
R7
2.2M
C9
0.1uF
BATIN/1.6I
Must be a stable LDO
for the DW1000.
U4 MAX8887EZK33+T
IN
OUT
SHDN
BP
C10
4.7uF
C15
10nF
C11
4.7uF
VCC
Accelerometer
C76
10uF
C75
0.1uF
U9
LIS2DW12
VCC
VDD_IO
SCL/SPC
CS
MISO/SA0
MOSI/SDA
INT1
INT2
GND
NRF_SCK
NRF_CS_IMU/1.3M
NRF_MISO/1.6J
NRF_MOSI/1.3J
NRF_IMU_INT1/1.3L
NRF_IMU_INT2/1.3M
Antennas
A5 TAIYO-YUDEN-AH086M
A2.IAIYO-YUDEN-AH086M
A2 TAIYO-YUDEN-AH086M
RF1/3.6A
RF2/3.6B
RF3/3.6C
SD Card Adapter
U12
SIP32510
VLOAD
VSUPP
EN
GND
SD_EN/1.3M
R19
100k
C77
4.7uF
R20
100k
R21
10k
R22
10k
R23
10k
J7
CON_MICRO_SDDM3AT-SF-PEJM5
VCC
DATA0/MISO
DATA1/RSV
DATA2/NC
DATA3-CD/CS_N
CMD/MOSI
CLK
GND
CARD_INSERTED
NRF_MISO/1.6J
NRF_CS_SD/1.3L
NRF_MOSI/1.3J
NRF_SCK
SD_DETECT/1.3M
U3
FT232RQ
SJ2
SJ3
SJ4
SJ5
SOLDERJUMPERNC2
VUSB
RST/1.3N
DBG_RX/1.6L
DBG_TX/1.6K
VCCIO
RESET
OSCI
OSCO
3V3OUT
USBDP
USBDM
TXD
RXD
RTS
CTS
DTR
DSR
DCD
RI
CBUS0
CBUS1
CBUS2
CBUS3
CBUS4
TEST
C59
0.1uF
USB-Serial
TriTag
REV:
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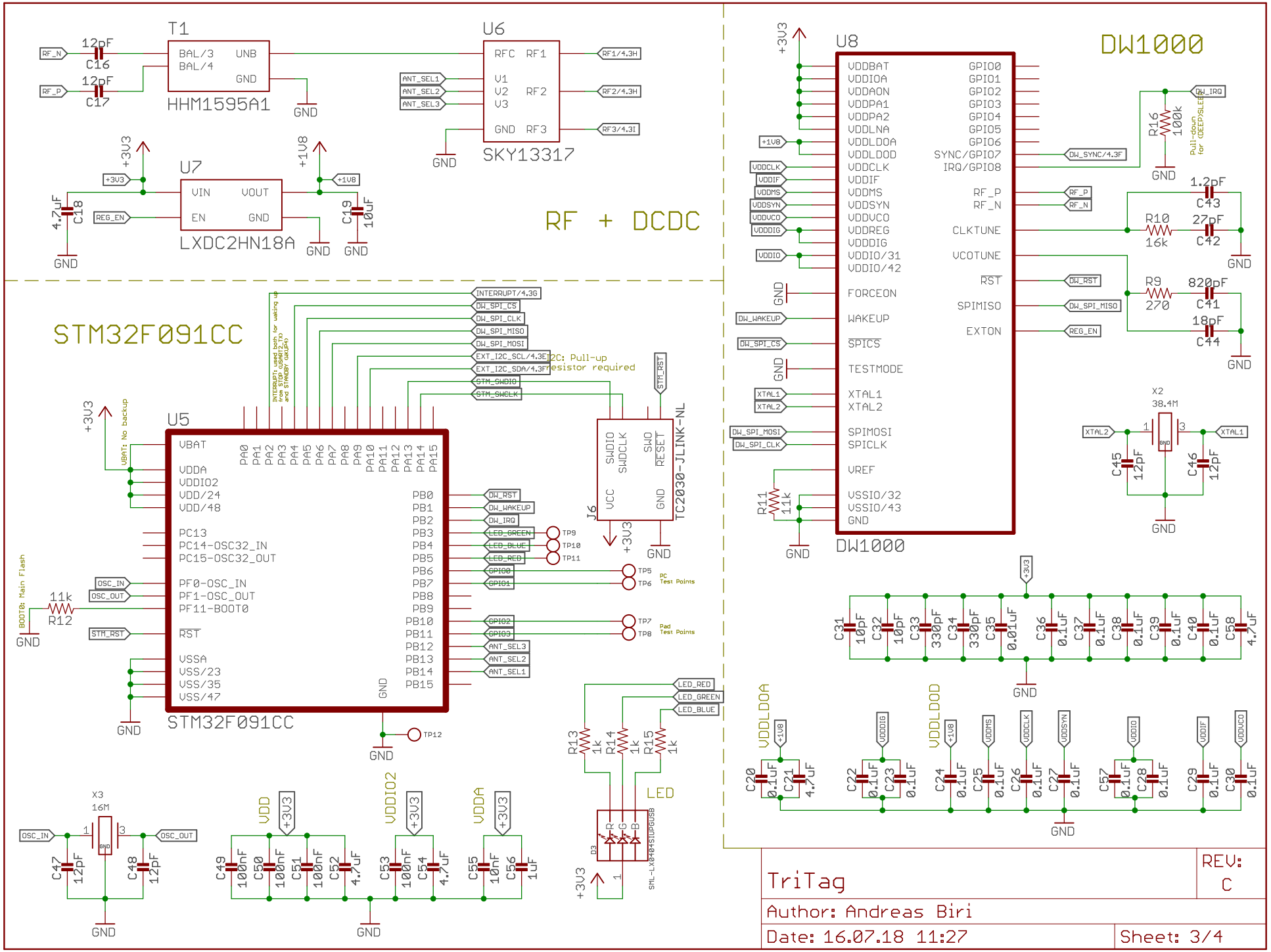
T1
HHM1595A1
U6
SKY13317
U7
LXDC2HN18A
RF + DCDC
U8
DW1000
STM32F091CC
U5
STM32F091CC
TC2030-JLINK-NL
LED
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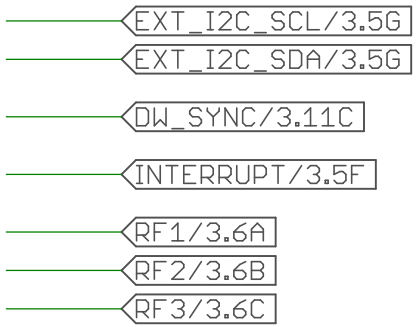
EXTERNAL SIGNALS

The following signals must be integrated into all designs using the design block:



Note: Additional I2C pull-up resistors required

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