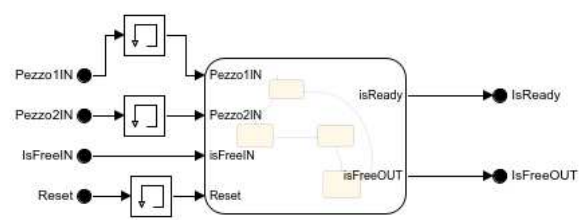


Check



scarcia

27-Mar-2020 10:06:08

Indice

[Model - Check](#)

[Machine - Check](#)

[System - Check](#)

[Chart - Chart5](#)

[Appendix](#)

Lista delle tabelle

- 1. [Chart Block Properties](#)
- 2. [Inport Block Properties](#)
- 3. [Memory Block Properties](#)
- 4. [Outport Block Properties](#)
- 5. [Block Type Count](#)
- 6. [Count:Check](#)
- 7. [Data Properties](#)

Model - Check

Indice

[Machine - Check](#)

Full Model Hierarchy

- 1. [Check](#)

Simulation Parameter	Value
Solver	VariableStepAuto
RelTol	1e-3
Refine	1
MaxOrder	5
ZeroCross	on

[\[more info\]](#)

Machine - Check

Machine	Check
Charts	Chart5

[\[more info\]](#)

System - Check

Indice

[Chart - Chart5](#)

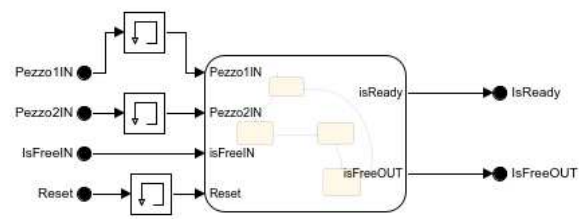


Tabella 1. Chart Block Properties

Name	Chart
Chart5	Chart5

Tabella 2. Inport Block Properties

Name	Port	Defined In Blk
Bus Element In4	4	Check (model)
IsFreeIN1	3	Check (model)
Pezzo1IN1	1	Check (model)
Pezzo2IN1	2	Check (model)

Tabella 3. Memory Block Properties

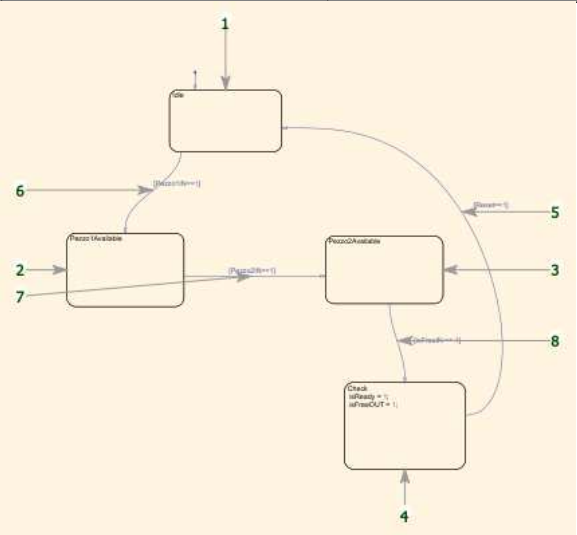
Name	Inherit Sample Time	Linearize Memory	Linearize As Delay	State Storage Class
Memory	off	off	off	Auto
Memory1	off	off	off	Auto
Memory2	off	off	off	Auto

Tabella 4. Output Block Properties

Name	Port	Port Name	Is Bus Element Port	Storage Class	Icon Display	Lock Scale	Unit	Var Size Sig	Signal Type	Ensure Output Is Virtual	Source Of Initial Output Value	Output When Disabled	Must Resolve To Signal Object	Output When Un Connected	Output When Unconnected Value	Vector Params As IDFor Out When Unconnected	Used By Blk
Bus Element Out1	1	IsReady	on	Auto	Port number	off	inherit	Inherit	auto	off	Dialog	held	off	off	0	off	Check (model)
Bus Element Out2	2	IsFreeOUT	on	Auto	Port number	off	inherit	Inherit	auto	off	Dialog	held	off	off	0	off	Check (model)

Chart - Chart5

Chart	Check/Chart5
States	Idle Pezzo1Available Pezzo2Available Check
Transitions	DefaultTransition6 [Reset==1] [Pezzo1IN==1] [Pezzo2IN==1] [isFreeIN == 1]
Data	isReady isFreeOUT Pezzo1IN Pezzo2IN isFreeIN Reset



- (1) [Idle](#)
- (2) [Pezzo1Available](#)
- (3) [Pezzo2Available](#)

- (4) [Check](#)
- (5) [Reset==1]
- (6) [Pezzo1IN==1]
- (7) [Pezzo2IN==1]
- (8) [isFreeIN == 1]

Stateflow Hierarchy

- 1. [Check](#)

[Chart5](#)

- 1.1.1. Data: [isReady](#), [isFreeOUT](#), [Pezzo1IN](#), [Pezzo2IN](#), [isFreeIN](#), [Reset](#)
- 1.1.2. Transition: DefaultTransition6, [Reset==1], [Pezzo1IN==1], [Pezzo2IN==1], [isFreeIN == 1]
- 1.1.3. [Check](#)
- 1.1.4. [Pezzo2Available](#)
- 1.1.5. [Pezzo1Available](#)
- 1.1.6. [Idle](#)

OR State	Check/Chart5/Idle
Label	Idle
OR State	Check/Chart5/Pezzo1Available
Label	Pezzo1Available
OR State	Check/Chart5/Pezzo2Available
Label	Pezzo2Available
OR State	Check/Chart5/Check
Label	Check isReady = 1; isFreeOUT = 1;

Appendix

Tabella 5. Block Type Count

BlockType	Count	Block Names
Inport	4	Bus Element In4 , IsFreeIN1 , Pezzo1IN1 , Pezzo2IN1
Memory	3	Memory , Memory1 , Memory2
Outport	2	Bus Element Out1 , Bus Element Out2
Chart	1	Chart5

Tabella 6. Count:[Check](#)

Count	Count	Objects
Data	6	isReady , isFreeOUT , Pezzo1IN , Pezzo2IN , isFreeIN , Reset
Transition	5	DefaultTransition6, [Reset==1], [Pezzo1IN==1], [Pezzo2IN==1], [isFreeIN == 1]
State	4	Idle , Pezzo1Available , Pezzo2Available , Check
Target	1	sfun
Machine	1	Check
Chart	1	Chart5

Tabella 7. Data Properties

Name	Parent	Data Type
isFreeIN	Chart5	Inherit: Same as Simulink
isFreeOUT	Chart5	Inherit: Same as Simulink
isReady	Chart5	Inherit: Same as Simulink
Pezzo1IN	Chart5	Inherit: Same as Simulink
Pezzo2IN	Chart5	Inherit: Same as Simulink
Reset	Chart5	Inherit: Same as Simulink
Target	Check/sfun	
Description	Default Simulink S-Function Target.	