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IMPERIAL COLLEGE OF SCIENCE, TECHNOLOGY AND MEDICINE

Popular Network Architectures (& BatchNorm)

LeNet-5 [7], MNIST, AlexNet [6], ImageNet, VGG [8], Inception (GoogleLeNet), BatchNorm [5], ResNet [2], DenseNet, Squeeze-Excite [3] Net U-Net, Data Augmentation

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1 Architectures

1.1 LeNet-5

LeNet [7] was initially designed for low-resolution, black and white image recognition, specifically for digits. It demonstrated that CNNs could reliably perform both tasks of object localization and recognition (on low-resolution black and white images).

The last steps of the LeNet-5 architecture employ fully connected layers to convert features into final predictions. The scenario of MNIST data didn't matter, because of its small number of output classes. This complicates the architecture when scaling up, influencing both computational resources and architecture.

The Convolutional Neural Network Architecture ensures some degree of shift, scale and distortion invariance: *local receptive fields, shared weights* (or weight replication), and spatial or temporal *sub-sampling*.

Once a feature has been detected, its exact location becomes less important. Only its approximate position relative to other features is relevant.

1.1.1 shared weights

This algorithm is particularly useful for shared-weight networks because the weight sharing creates ill-conditioning of the error surface. Because of the sharing, one single parameter in the first few layers can have an enormous influence on the output. Consequently, the second derivative of the error with resp ect to this parameter maybe very large, while it can be quite small for other parameters elsewhere in the network

1.1.2 sub-sampling

A simple way to reduce the precision with which the position of distinctive features are encoded in a feature map is to reduce the spatial resolution of the feature map. This can be achieved with a so-called sub-sampling layers which performs a local **averaging** and a sub-sampling, reducing the resolution of the feature map, and reducing the sensitivity of the output to shifts and distortions.

1.1.3 Loss

Maximum Likelihood Estimation Criterion (MLE), which is equivalent to the Mean Squared Error (MSE)

$$E(W) = \frac{1}{P}\sum_{p=1}^P y_{D^P}(Z^P,W)$$

Where y_{D^p} is the output of the D_p -th RBF unit, i.e. the one that corresponds to the correct class of the input pattern Z^p .

1.2 AlexNet 1 ARCHITECTURES

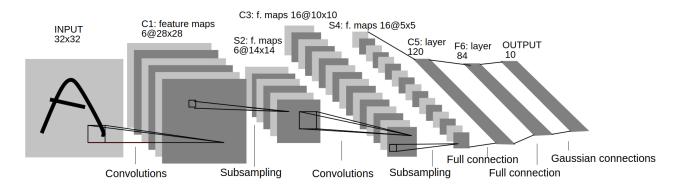


Figure 1: Architecture of LeNet-5 a Convolutional Neural Network, here for digits recognition. Each plane is a feature map, i.e. a set of units whose weights are constrained to be identical [7]

```
from https://pytorch.org/tutorials/beginner/blitz/neural_networks_tutorial.html
    https://www.analyticsvidhya.com/blog/2023/11/lenet-architectural-insights-and-practical

    → -implementation/
  import torch
  import torch.nn as nn
  import torch.nn.functional as F
  class Net(nn.Module):
      def __init__(self):
10
           super(Net, self).__init__()
11
          # 1 input image channel, 6 output channels, 5x5 square convolution
12
          # kernel
13
           self.conv1 = nn.Conv2d(1, 6, 5)
14
           self.pool1 = nn.AvgPool2d(2, stride=2)
15
           self.conv2 = nn.Conv2d(6, 16, 5)
16
           self.pool2 = nn.AvgPool2d(2, stride=2)
17
          # an affine operation: y = Wx + b
18
           self.fc1 = nn.Linear(16 * 5 * 5, 120) # 5*5 from image dimension
19
           self.fc2 = nn.Linear(120, 84)
20
           self.fc3 = nn.Linear(84, 10)
21
22
      def forward(self, x):
23
          x = self.conv1(x)
24
          x = F.relu(x)
25
          x = self.pool1(x)
26
          x = self.conv2(x)
2.7
          x = F.relu(x)
28
          x = self.pool2(x)
29
30
          x.torch.flatten(x,1) # flatten all dimensions except the batch dimension
31
32
          x = self.fc1(x)
33
          x = F.relu(x)
34
          x = self.fc2(x)
35
          x = F.relu(x)
36
          x = self.fc3(x)
```

code/LetNet.py

1.2 AlexNet

The AlexNet [6] was introduced in 2012 and introduced more layers for larger inputs and larger filters. Originally, the architecture was split into two columns/streams, but this was a workaround for the hardware limitations at the time.

1.2 AlexNet 1 ARCHITECTURES

It used the ImageNet dataset which contained color images with nature objects of larger size compared to MNIST (469×387 vs 28×28) and 1.2 million images vs 60k images.

Its key improvements on the LeNet were

- Add a dropout layer after two hidden dense layers (better robustness /regularization)
 Dropout allowed for much deeper networks by introducing regularization not just at the input layer but throughout multiple layers of the network. This made it possible to control the complexity of the model more effectively.
- Change activation function from sigmoid to ReLu (no more vanishing gradient) enabling training of deeper networks more efficiently.
- MaxPooling instead of Average Pooling
 This made the learned features more shift-invariant, which is important for object recognition.
 Max pooling generally retains the most salient features and discards less useful information, making the model more robust.
- Heavy data Augmentation like cropping, shifting, and rotation.
- Model ensembling
 Ensemble methods is a machine learning technique that combines several base models in order to produce one optimal predictive model
- Using softmax function for classification
- Increase in kernel size and stride
 design choice to accommodate the higher resolution of images in the ImageNet dataset compared to MNIST.

AlexNet is substantially more complex, being about 250 times more computationally expensive. However, it is only ten times larger parametrically than LeNet-5. This way AlexNet is notorious for its high memory usage.

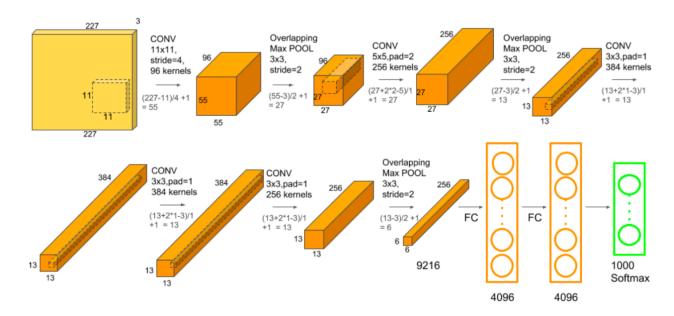


Figure 2: Architecture of AlexNet

1.3 VGG 1 ARCHITECTURES

```
import torch
  import torch.nn as nn
  class AlexNet(nn.Module):
      def __init__(self, num_classes: int = 1000, dropout: float = 0.5) -> None:
          super(AlexNet, self).__init__()
          self.features = nn.Sequential(
               nn.Conv2d(3, 64, kernel_size=11, stride=4, padding=2),
               nn.ReLU(inplace=True),
               nn.MaxPool2d(kernel_size=3, stride=2),
10
               nn.Conv2d(64, 192, kernel_size=5, padding=2),
               nn.ReLU(inplace=True),
               nn.MaxPool2d(kernel_size=3, stride=2),
13
               nn.Conv2d(192, 384, kernel_size=3, padding=1),
14
               nn.ReLU(inplace=True),
               nn.Conv2d(384, 256, kernel_size=3, padding=1),
16
               nn.ReLU(inplace=True),
               nn.Conv2d(256, 256, kernel_size=3, padding=1),
18
               nn.ReLU(inplace=True),
19
20
               nn.MaxPool2d(kernel_size=3, stride=2),
21
          self.avgpool = nn.AdaptiveAvgPool2d((6, 6))
22
          self.classifier = nn.Sequential(
23
               nn. Dropout (p=dropout),
24
               nn.Linear(256 * 6 * 6, 4096),
25
               nn.ReLU(inplace=True),
26
               nn.Dropout(p=dropout),
27
               nn.Linear (4096, 4096),
28
               nn.ReLU(inplace=True),
29
               nn.Linear(4096, num_classes),
30
          )
31
32
33
      def forward(self, x: torch.Tensor) -> torch.Tensor:
34
          x = self.features(x)
          x = self.avgpool(x)
35
          x = torch.flatten(x, 1)
36
          x = self.classifier(x)
37
          return x
```

code/AlexNet.py

1.3 VGG

The Visual Geometry Group [8] uses the 'bigger means better' philosophy. VGG introduced the notion of repeated blocks.

- NOT Add more dense layers computationally expensive
- NOT Add more convolutional layers as the network grows, specifying each convolutional layer individually becomes tedious
- **Group Layers into blocks** these blocks can easily be parameterized, creating a more organized, modular architecture

1.3 VGG 1 ARCHITECTURES

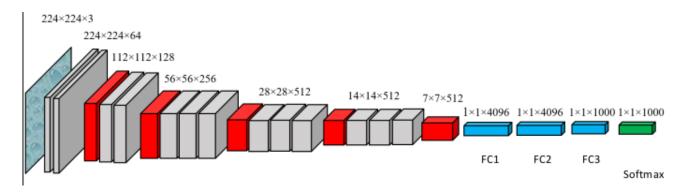


Figure 3: Architecture of VGG

```
from functools import partial
     from typing import Any, cast, Dict, List, Optional, Union
     import torch
     import torch.nn as nn
      cfgs: Dict[str, List[Union[str, int]]] = {
                "A": [64, "M", 128, "M", 256, 256, "M", 512, 512, "M", 512, 512, "M"],
                "B": [64, 64, "M", 128, 128, "M", 256, 256, "M", 512, 512, "M", 512, 512, "M"],
                "D": [64, 64, "M", 128, 128, "M", 256, 256, 256, "M", 512, 512, 512, "M", 512, 512,
10

→ 512, "M"],
                "E": \; [64,\; 64,\; "M"\,,\; 128,\; 128,\; "M"\,,\; 256,\; 256,\; 256,\; 256,\; "M"\,,\; 512,\; 512,\; 512,\; 512,\; "M"\,,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 512,\; 
11
               → 512, 512, 512, 512, "M"],
12
      class VGG(nn.Module):
14
                def __init__(
15
                           self, features: nn.Module, num_classes: int = 1000, init_weights: bool = True,
16
                \hookrightarrow dropout: float = 0.5
                ) -> None:
17
                          super(VGG, self).__init__()
18
                           self.features = features
19
                           self.avgpool = nn.AdaptiveAvgPool2d((7, 7))
20
                           self.classifier = nn.Sequential(
21
                                     nn.Linear(512 * 7 * 7, 4096),
22
                                     nn.ReLU(True),
23
                                     nn.Dropout(p=dropout),
24
                                     nn.Linear(4096, 4096),
25
                                     nn.ReLU(True),
26
                                     nn. Dropout (p=dropout),
27
                                     nn.Linear (4096, num_classes),
28
29
                           if init_weights:
30
                                     for m in self.modules():
31
                                                if isinstance(m, nn.Conv2d):
32
                                                          nn.init.kaiming_normal_(m.weight, mode="fan_out", nonlinearity="relu"
33
               \hookrightarrow )
                                                           if m. bias is not None:
34
                                                                     nn.init.constant_(m.bias, 0)
                                                elif isinstance (m, nn.BatchNorm2d):
36
                                                          nn.init.constant_(m.weight, 1)
37
                                                          nn.init.constant_(m.bias, 0)
38
                                                elif isinstance (m, nn. Linear):
39
                                                          nn.init.normal_(m.weight, 0, 0.01)
40
41
                                                          nn.init.constant_(m.bias, 0)
42
                def forward(self, x: torch.Tensor) -> torch.Tensor:
43
                          x = self.features(x)
44
                          x = self.avgpool(x)
45
                          x = torch.flatten(x, 1)
46
```

```
x = self.classifier(x)
47
48
           return x
49
50
  def make_layers(cfg: List[Union[str, int]], batch_norm: bool = False) -> nn.Sequential:
51
      layers: List[nn.Module] = []
52
      in_channels = 3
53
      for v in cfg:
54
           if v == "M":
55
               layers += [nn.MaxPool2d(kernel_size=2, stride=2)]
56
57
               v = cast(int, v)
58
               conv2d = nn.Conv2d(in_channels, v, kernel_size=3, padding=1)
59
               if batch_norm:
60
                   layers += [conv2d, nn.BatchNorm2d(v), nn.ReLU(inplace=True)]
61
               else:
62
                   layers += [conv2d, nn.ReLU(inplace=True)]
63
               in_channels = v
64
      return nn. Sequential (*layers)
65
66
67
  def _vgg(arch: str, cfg: str, batch_norm: bool, progress: bool, **kwargs: Any ) -> VGG:
68
      model = VGG(make_layers(cfgs[cfg], batch_norm=batch_norm, **kwargs))
69
      return model
70
71
  def vgg16(progress: bool = True, **kwargs: Any) -> VGG:
      return _vgg('vgg_16', "D", False, progress, **kwargs)
```

code/VGG.py

1.3.1 fewer wide convolutions or more narrow convolutions?

Recent comprehensive analysis from papers has shown that using more layers of narrow convolutions outperforms using fewer wide ones. This has been a general trend in network design: having more layers of simpler functions is generally more powerful than fewer layers of more complex functions.

1.3.2 The VGG block

Several 3×3 convolutions, padded by one maintains the spatial dimensions from the input to the output layer, and at the end, max-pooling layer of 2×2 and stride of 2 halves the resolution.

Combining these blocks with dense layers, creates an entire family of architectures just by varying the number of blocks.

1.3.3 Performance

VGG tends to be a lot slower when compared to the throughput of AlexNet (Figure 9), however, it makes up for in terms of accuracy. While VGG might require more computational resources, it generally provides superior performance.

1.4 Inception

The Inception architecture [9] is both deep and introduces the concept of parallel paths within the network which offers multiple pathways for data to flow. The architecture combines the best of different types of convolutions and pooling layers to enhance its performance.

The decision was:

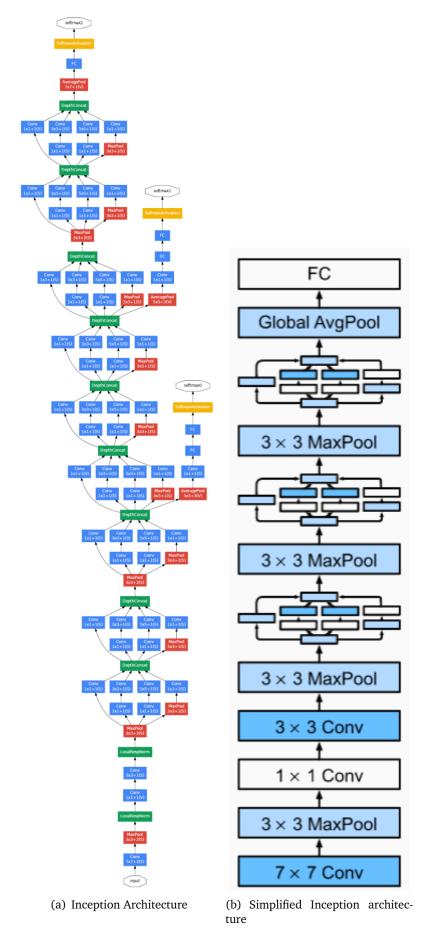


Figure 4: Inception code can be found at http://d2l.ai chapter named 8.4. Multi-Branch Networks (GoogLeNet)

if you opt for 5×5 convolutions, you will end up with many parameters, leading to a lot of computational cost and possibly overfitting, even though it might be more expressive. On the other hand, if you go with 1×1 convolutions, you'll have a more controlled, memory-efficient architecture, but it may not perform as well.

"The most straightforward way of improving the performance of deep neural networks is by increasing their size. This includes both increasing the depth - the number of levels - of the network and its width: the number of units at each level. This is as an easy and safe way of training higher quality models, especially given the availability of a large amount of labeled training data" [9]

This comes with two major drawbacks

- Bigger size typically means a larger number of parameters, which makes the enlarged network more prone to overfitting, especially if the number of labeled examples in the training set is limited.
 - This can become a major bottleneck, since the creation of high quality training sets can be tricky and expensive, especially if expert human raters are necessary to distinguish between fine-grained visual categories like those in ImageNet
- Another drawback of uniformly increased network size is the dramatically increased use of computational resources. For example, in a deep vision network, if two convolutional layers are chained, any uniform increase in the number of their filters results in a quadratic increase of computation. If the added capacity is used inefficiently (for example, if most weights end up to be close to zero), then a lot of computation is wasted.

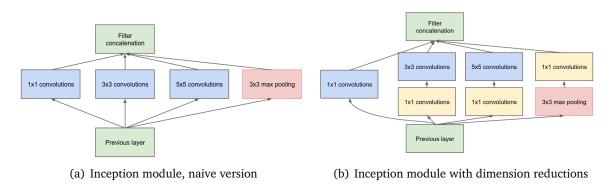


Figure 5: Inception Module

1.4.1 Naiive Inception Module

"In order to avoid patch-alignment issues, current incarnations of the Inception architecture are restricted to filter sizes 1×1 , 3×3 and 5×5 , however this decision was based more on convenience rather than necessity. It also means that the suggested architecture is a combination of all those layers with their output filter banks concatenated into a single output vector forming the input of the next stage. Additionally, since pooling operations have been essential for the success in current state of the art convolutional networks, it suggests that adding an alternative parallel pooling path in each such stage should have additional beneficial effect, too (Figure 5(a))" [9].

The Problem with the naiive approach is that "even a modest number of 5×5 convolutions can be prohibitively expensive on top of a convolutional layer with a large number of filters. This problem becomes even more pronounced once pooling units are added to the mix: their number of output filters equals to the number of filters in the previous stage. The merging of the output of the pooling layer with the outputs of convolutional layers would lead to an inevitable increase in the number of outputs from stage to stage. Even while this architecture might cover the optimal sparse structure, it would do it very inefficiently, leading to a computational blow up within a few stages" [9].

1.4.2 Detailed Inception Module

This leads to a second proposed architecture (Figure 5(b)). "judiciously applying dimension reductions and projections wherever the computational requirements would increase too much otherwise. This is based on the success of embeddings: even low dimensional embeddings might contain a lot of information about a relatively large image patch. However, embeddings represent information in a dense, compressed form and compressed information is harder to model. We would like to keep our representation sparse at most places and compress the signals only whenever they have to be aggregated en masse. That is, 1×1 convolutions are used to compute reductions before the expensive 3×3 and 5×5 convolutions. Besides being used as reductions, they also include the use of rectified linear activation which makes them dual-purpose" [9]

1.4.3 How the Modules fit together

"As these "Inception modules" are stacked on top of each other, their output correlation statistics are bound to vary: as features of higher abstraction are captured by higher layers, their spatial concentration is expected to decrease suggesting that the ratio of 3×3 and 5×5 convolutions should increase as we move to higher layers" [9].

Now, you might be wondering, how do all these different types of convolutions fit together in the Inception block? The key to making this work is using appropriate padding. For example, the 3×3 convolutions use half-padding by one, and the 5×5 convolutions use half-padding by two. This ensures that the dimensions of the inputs and outputs align and remain the same. Once that's taken care of, you simply stack all these layers together.

1.4.4 Channels

The first inception block has channels sizes specified.

The first Inception block uses 64 channels for the 1×1 convolutions. For the 3×3 convolutions, it uses 128 channels. And for the 5×5 convolutions, 32 channels are used, primarily because 5×5 convolutions already come with a large number of parameters—25 times 32 in this case. When it comes to max pooling, a few other dimensions are included. The goal is to have all these different parts sum up to 256 channels.

1.4.5 Advantages

The Inception block is designed to use a relatively low number of parameters (better than just striaght up sticking with one dimension of convolution) and floating-point operations (FLOPs), without sacrificing performance.

It has less computational operations:

$$k^2 \times \begin{bmatrix} fixed & fixed \\ c_{in} \end{bmatrix} \times c_{out} \times \begin{bmatrix} m_h \times m_w \end{bmatrix}$$

Consider a convolutional kernel of size $k \times k$, c relating to the channels and dimension of the image m.

We have the flexibility to adjust $c_{in} \wedge c_{out}$.

$$\frac{fixed}{c_{in} \times m_h \times m_w} \times \sum_{j \in paths} (k_j^2 \times c_{out,j})$$

By carefully allocating resources - varying the number of channels and kernel sizes - we can optimize the network's performance.

1.5 ReSNet 1 ARCHITECTURES

1.4.6 Channels

The Inception blocks are designed in such a way that they need fewer parameters and less computational complexity than a single 3x3 or 5x5 convolutional layer, as shown in Table 1. If we were to have 256 channels in the output layer, Inception needs only 16,000 parameters and costs only 128 Mega FLOPS, whereas a 3x3 convolutional layer will need 44,000 parameters and cost 346 Mega FLOPS, and a 5x5 convolutional layer will need 1,22,000 parameters and cost 963 Mega FLOPS. So Inception blocks, essentially get the same job done as single convolutional layers, with much better memory and compute efficiency, from here

1.5 ReSNet

1.5.1 Motivation: Does adding layers improve accuracy?

Gradients don't properly propagating through many layers. Once you add more layers, you change the function class, making it more versatile but also different; More layers means more parameters, granting us the power to model more complex functions.

As you add more layers, the function class becomes more powerful, but also diverge - adding more layers might get you closer to this truth initially, but after a certain point you could drift away.

In an ideal world, you'd like more layers to create a new function which neatly nests the previous function - making this similar to the last. The goal is to have function classes that are both increasingly powerful and nested as we add layers.

1.5.2 Residual Networks

ResNet [2] parameterizes around an identity function f(x) = x instead of a zero function f(x) = 0. Now, as you tune your parameters, you start to diverge from this identity function. The beauty is that you don't have to learn the identity function from scratch, reducing unnecessary computational overhead. It implies a different inductive bias and allows for the addition of new layers without disrupting the outputs of previous layers.

Now, the functions don't precisely implement nested function classes, but it comes remarkably close in an approach called the ResNet block (Figure 6), where the input is added to the output residual.

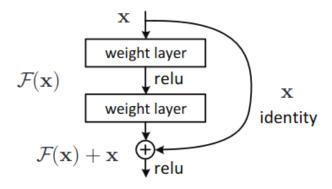


Figure 6: Residual learning: building block from [2]

Each block generally contains two or more convolutions, each followed by a batch normalization and a Rectified Linear Unit (ReLU). The innovation lies in adding the original input back to the output of this sequence. In some cases, we might run a 1x1 convolution on the input before adding it to the output, especially if we need to change the dimensionality of the input to match the output.

This setup makes it easier for the network to learn an identity mapping, essentially enabling the network to decide whether or not a change is beneficial.

1.6 DenseNet 1 ARCHITECTURES

code/ResNet.py

1.5.3 The Architecture

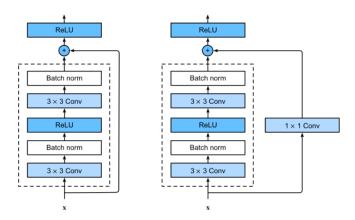


Figure 7: ResNet Module, code at http:d21.ai 8.6. Residual Networks (ResNet) and ResNeXt

A typical ResNet module may include a block with down-sampling; this usually employs a stride of 2. Following this, several other standard ResNet blocks are stacked. Down-sampling in each module is commonly performed using a stride of 2, serving to reduce the spatial dimensions of the feature maps. To introduce complexity and non-linearity into the network, 1x1 convolutions are often employed within the ResNet blocks. Finally, these blocks are stacked together to form a deep and complex network architecture.

Residual connections are a defining feature that contribute to the network's expressiveness and allow for training deeper models without facing vanishing or exploding gradients. To manage dimensionality, ResNet uses pooling and strides just like earlier architectures. Batch normalization layers are interspersed throughout to control the capacity and stabilize the training process.

1.6 DenseNet

Densely Connected Convolution Networks [4] have the characteristic that each layer in a DenseNet are connected to every other layer in a feed-forward manner, quite unlike traditional CNNs where each layer is only connected to its immediate successor.

- The key advantage of this dense connectivity is feature reuse: each layer gets the feature maps from all the preceding layers, not just the last one. This makes it efficient and requires fewer parameters for high performance. So, you get a network that's both computationally and memory-efficient.
- Dense connections also help alleviate the vanishing gradient problem by improving the gradient flow through the network, making optimization easier.
- It is also scalable, since you can easily adjust the number of layers and other hyper-parameters to make it suitable for a variety of tasks and data sizes.

1.7 SqueezeNet 1 ARCHITECTURES

Occasionally need to reduce resolution (transition layer) (Figures on presentation)

1.7 SqueezeNet

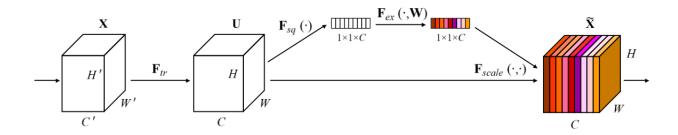


Figure 8: SqueezeNet architecture from [3]

SqueezeNets [3] use attention mechanisms.

1.7.1 Attention

In machine learning, attention allows a model to focus on significant features within the data, rather than indiscriminately treating all features equally. Think of it like spotlighting the essential parts in an otherwise noisy scene.

The paradox of needing to know what you are recognizing before you actually recognize it is addressed in the Squeeze-and-Excite Network (SE-Net) through the use of attention mechanisms.

1.7.2 Implementation

In a traditional network, information moves relatively slowly from layer to layer. This can mean that it takes several layers for information from one corner of an image to influence the other corner.

The network calculates a global descriptor for each channel. These descriptors go through a small neural network that outputs new descriptors. These new descriptors are then used to scale or 'excite' the original channels. This is computationally efficient and offers a more refined way to focus on what's crucial in the image.

1. **Squeeze** - takes a global average pooling of each channel to get a single descriptor that captures the global information of that channel.

2.

3. **Descriptor** - These descriptors are then fed into a small neural network (usually a fully- connected layer followed by a ReLU and another fully-connected layer) to create a new set of descriptors. These new descriptors are designed to capture which channels are more important given the current global context of the image

4.

5. **Excite** - The output descriptors are then used to scale the original channels. This scaling effectively "excites" or enhances the channels that are likely to be more important for a given task, based on the global context of the image

1.8 Evaluation 1 ARCHITECTURES

6. **Re-weighting Channels** - So if the global context suggests that the image likely contains a cat, the "cat channel" would be scaled up, emphasizing features relevant to cats and making it easier for subsequent layers to detect the cat. This procedure is computationally efficient and allows the network to focus on relevant features without needing to explicitly know in advance what those features represent (like a "cat" or a "dog"). It's a way of allowing the network to dynamically allocate its attention based on the overall features in the image, resolving the paradox.

1.8 Evaluation

1.8.1 Performance

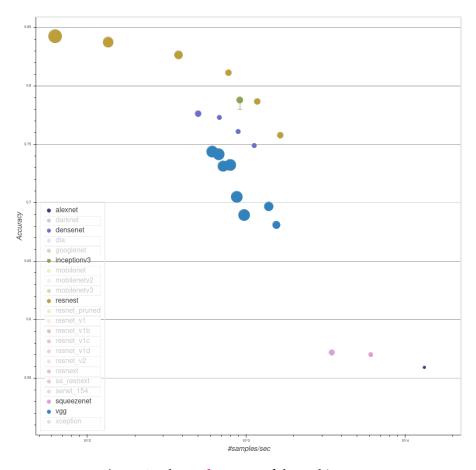


Figure 9: The performance of the architectures

1.8.2 What we learnt

- Dense layers are computationally and memory intensive. Real-world problems with big input tensors and many classes will prohibit their use.
- 1×1 convolutions act like a multi-layer perceptron per pixel
- Scientists are humans and need a while to understand the power of new approaches. Eventually they do but a lot of vanity is involved in the process
- If not sure, just take all options and let the optimization decide or even learn this through trial and error (genetic algorithm, AmoebaNet)
- Deeper is not necessarily better if the function space is not regularized.
- ResNet is the workhorse of Deep Learning (For now)

• Lot's of variations have been proposed but it often boils down to how you train a network and for what purpose

2 Batch Norm

The problem deep networks face is with convergence, making training difficult and time consuming. There is a strategy to perform "deep supervision" which involves backpropagating from intermediate stages to help the network learn - however, this has its limitations [TODO].

2.1 Motivation

Very deep models involve the composition of several functions or layers. The gradient tells how to update each parameter, under the assumption that the other layers do not change. In practice, we update all of the layers simultaneously; gradients flow from the top layer down to the bottom layers of the network. As a result, the last layers start to adapt first, followed by the layers below them, creating a cascade of adaptations throughout the network.

However, this leads to a problem: As the bottom layers adapt, they change the features that are fed back up to the top layers. This means that the top layers, which had already started to adapt, have to readjust to these new inputs. Training Deep Neural Networks is complicated by the fact that the distribution of each layer's inputs changes during training, as the parameters of the previous layers change [1]. Each layer's learning destabilizes the next, causing a slow convergence process that takes a long time for all layers to adapt properly.

2.2 Solution

Batch normalization can be applied to any input or hidden layer in a network [1].

Batch Norm mitigates this issue by normalizing the features within each mini-batch, thereby stabilizing the training process and speeding up convergence. The idea is to make minor corrections to the layers by adjusting their mean and variance during training.

$$\mu_{B} = \frac{1}{|B|} \sum_{i \in B} x_{i} \quad \sigma_{B}^{2} = \frac{1}{|B|} \sum_{i \in B} (x_{i} - \mu_{B})^{2} + \epsilon$$

$$x_{i+1} = \gamma \frac{x_{i} - \mu_{B}}{\sigma_{B}} + \beta$$
(1)

Where γ is variance and β is the mean. |B| represents the size of the mini-batch, and x_i are the individual data points in that mini-batch. ϵ is a small positive value such as 10^{-8} introduced to avoid the potential problem of undefined gradient.

We compute the mean and variance of a given mini-batch during training. Then, re-normalize each input feature by subtracting its mean and dividing it by its standard deviation. The model also learns two parameters to scale (γ) and shift (β) the normalized features.

We then adjust these first and second moments separately from the rest of the network learning, leading to Equation 1.

2.3 Advantages

This way, we are able to "normalize" each mini-batch separately, making the training of deep networks more stable and faster (dramatically reducing the number of training epochs required to train

2.4 Updated Aim REFERENCES

deep networks). The normalization can be undone by the network by learning appropriate γ and β parameters if it sees a benefit to it.

2.4 Updated Aim

The original motivation of batch normalization is reducing covariate shift. This is not correct. Batch-Norm actually worsens covariate shift, yet it still aids in model convergence.

BatchNorm is effectively acting as a form of regularization by introducing noise into the model.

Here's how it works: You calculate the mean and variance of a mini-batch, let's say, of 64 observations. Since you're working with a small sample, both the mean and the variance are subject to noise. You then normalize the features using these noisy statistics, introducing a random scale and shift into your model at each batch. This random noise acts as a regularizing factor, which is why you often don't need additional regularization techniques like dropout when you're using BatchNorm. However, this property makes BatchNorm sensitive to the size of the mini-batch.

If your mini-batch is too large, you're not introducing enough noise for effective regularization. If it's too small, the noise level becomes counterproductive, affecting convergence. This mini-batch size sensitivity becomes especially significant in multi-GPU settings, where batch sizes are often adjusted.

2.5 Application

If you're working with a dense layer, a single normalization is applied to all the activations in that layer.

In the case of a convolutional layer, a separate normalization is performed for each channel.

2.6 Randomization during use

You don't want this randomness when you're using the model for predictions. So, you fix the gamma and beta parameters that the model has learned during training. Instead of using batch statistics, you use the running average for the mean and variance to normalize the features.

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