# ALTERA DE2

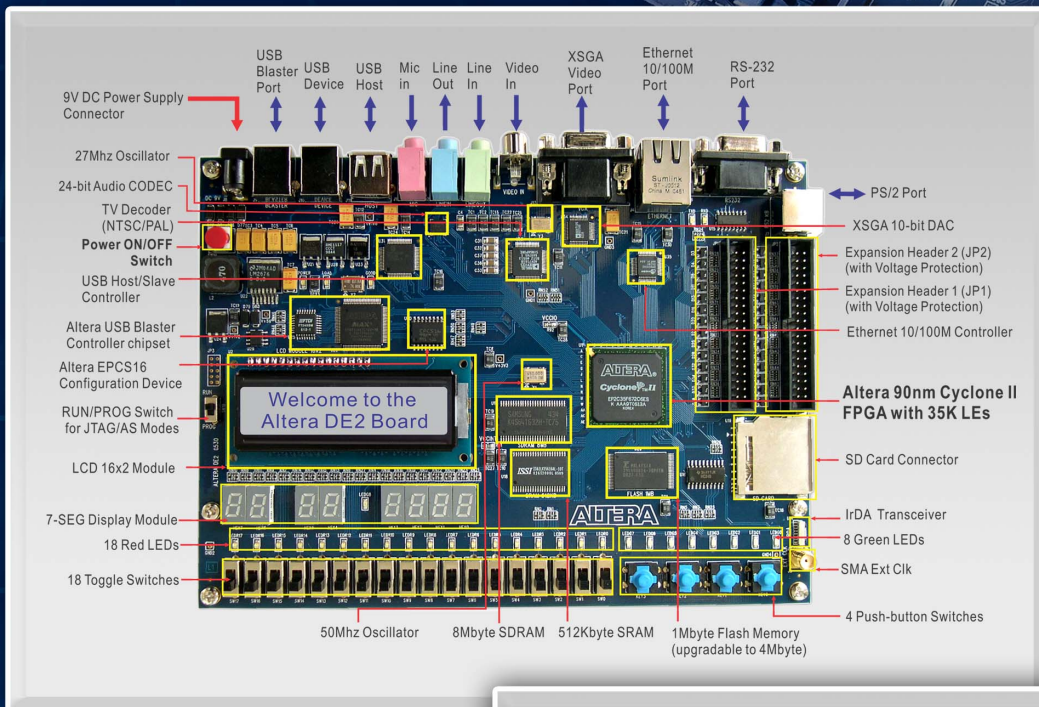## Development and Education Board

### Learning through Innovation

Cyclone II →

Nios II →

USB UNIVERSAL SERIAL BUS →



- USB Blaster Port
- USB Device
- USB Host
- Mic in
- Line Out
- Line In
- Video In
- XSGA Video Port
- Ethernet 10/100M Port
- RS-232 Port

- 9V DC Power Supply Connector
- 27Mhz Oscillator
- 24-bit Audio CODEC
- TV Decoder (NTSC/PAL)
- **Power ON/OFF Switch**
- USB Host/Slave Controller
- Altera USB Blaster Controller chipset
- Altera EPCS16 Configuration Device
- RUN/PROG Switch for JTAG/AS Modes
- LCD 16x2 Module
- 7-SEG Display Module
- 18 Red LEDs
- 18 Toggle Switches

- PS/2 Port
- XSGA 10-bit DAC
- Expansion Header 2 (JP2) (with Voltage Protection)
- Expansion Header 1 (JP1) (with Voltage Protection)
- Ethernet 10/100M Controller
- **Altera 90nm Cyclone II FPGA with 35K LEs**
- SD Card Connector
- IrDA Transceiver
- 8 Green LEDs
- SMA Ext Clk
- 4 Push-button Switches

- 50Mhz Oscillator
- 8Mbyte SDRAM
- 512Kbyte SRAM
- 1Mbyte Flash Memory (upgradable to 4Mbyte)

Welcome to the Altera DE2 Board

Manufactured by Terasic Exclusively for Altera

## Development and Education Board

- Altera Cyclone II (2C35) FPGA with 35,000 LEs
- 16Mbit Serial Configuration Device for AS mode
- Built-in USB Blaster with Enhanced API Link IP
- TV Decoder for NTSC/PAL/Multiformat systems
- 24-bit CD-Quality Audio CODEC
- VGA DAC (10-bit DAC)
- USB Host and Device
- Ethernet 100/10Mbps
- SRAM, SDRAM, Flash, SD Card Connector
- RS232, IrDA, PS/2
- 16x2 LCD Panel

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