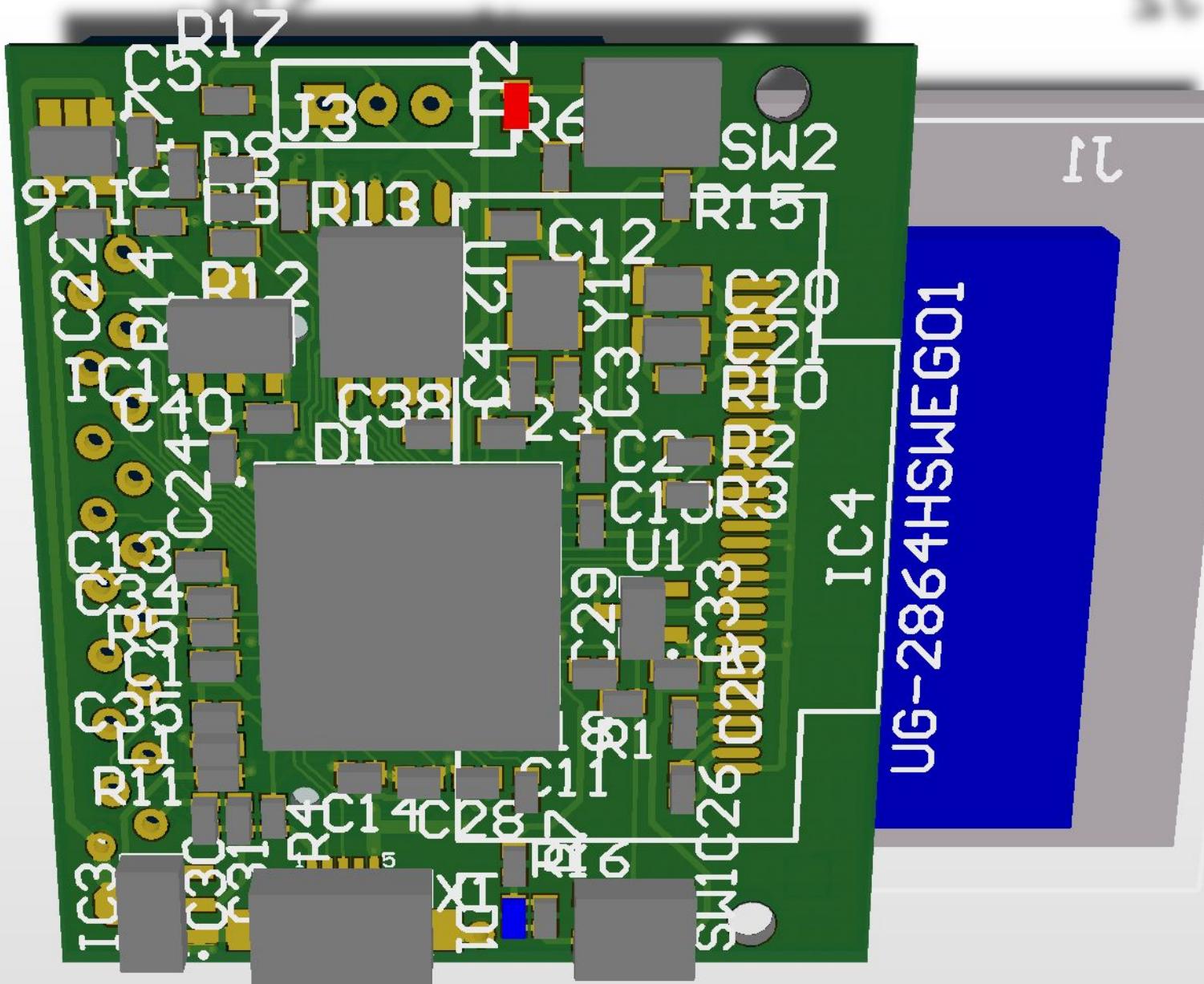
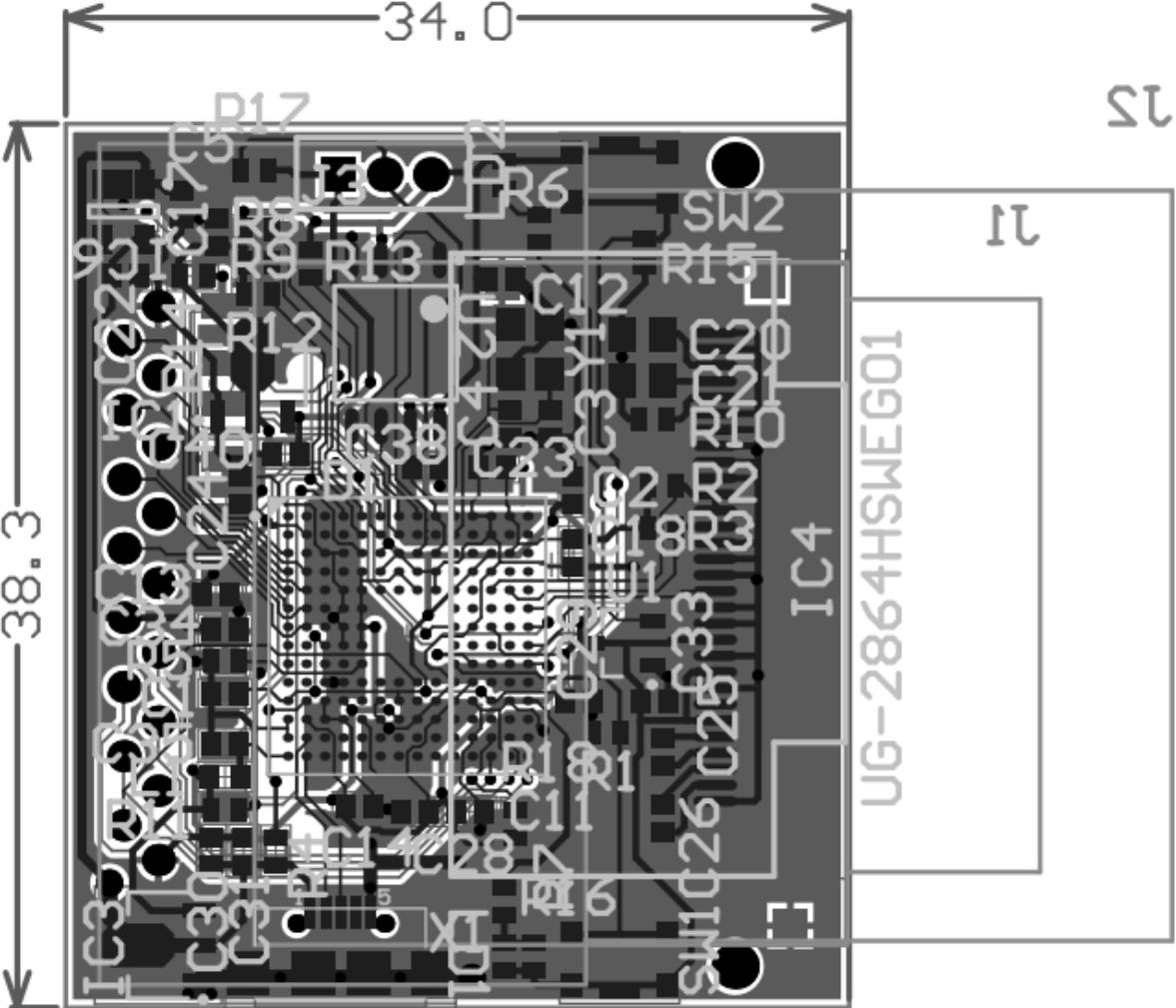
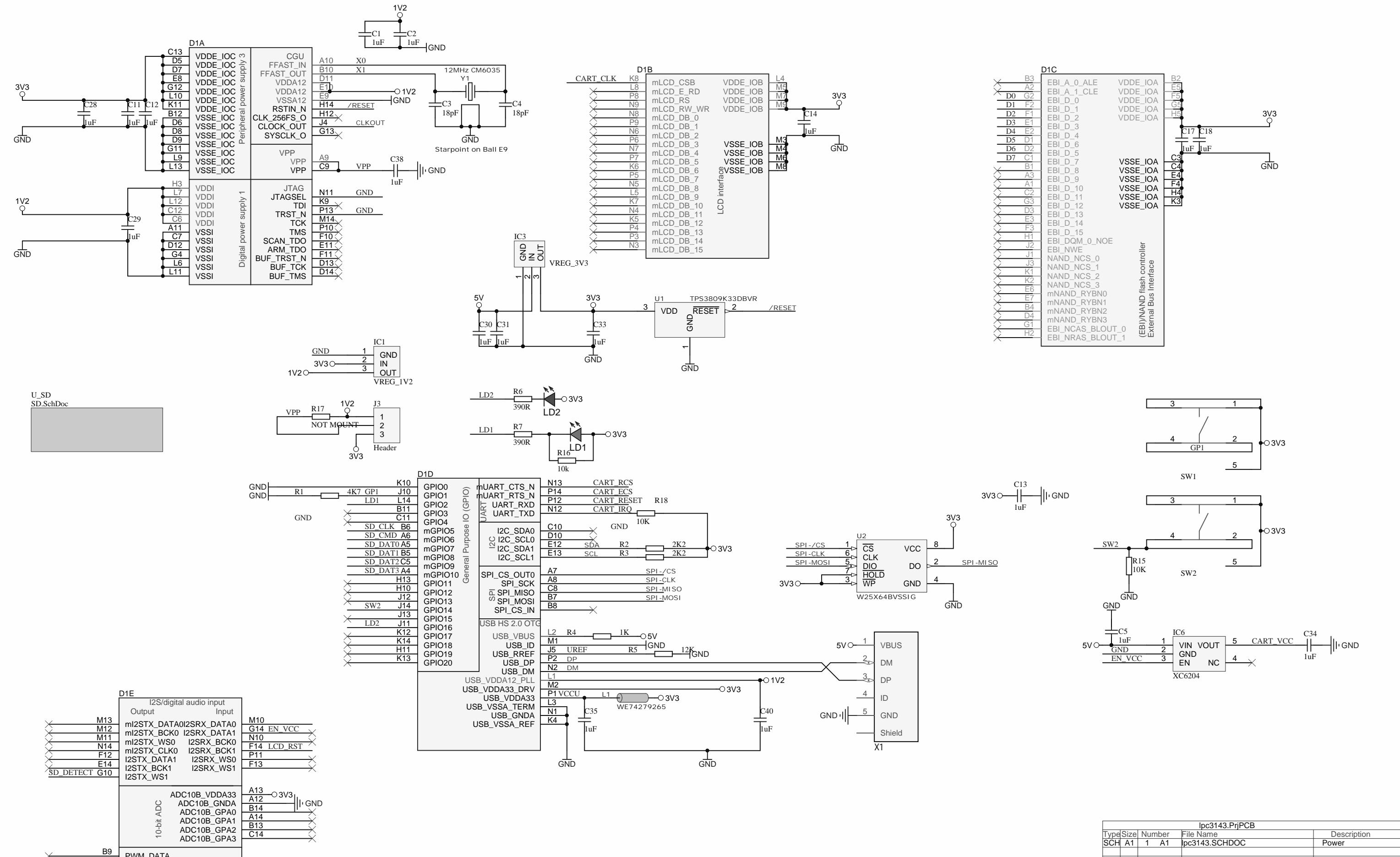


Comment	Description	Designator	Quantity	Value
CAP-NP		C1, C2, C5, C11, C12, C13, C14, C17, C18, C22, C23, C24, C25, C26, C28, C29, C30, C31, C33, C34, C35, C38, C40	23	1uF
CAP-NP		C3, C4	2	18pF
CAP-NP		C20, C21	2	4u7
LPC3143		D1	1	LPC3143
SOT89 voltage regulator	VREG_SOT89	IC1	1	VREG_1V2
SOT89 voltage regulator	VREG_SOT89	IC3	1	VREG_3V3
UG-2864HSWEG01		IC4	1	UG-2864HSWEG01
XC6204	XC6204	IC6	1	XC6204
SD-ATTEND-104D		J1	1	SD-ATTEND-104D
System-17 Connector	NDS Slot-1	J2	1	nds_slot1
Header	Header, 3-Pin	J3	1	SIP3
WE74279265	600Ohm/100MHz, 0,7Ohm/DC, 200mA	L1	1	
BLUELED-0805		LD1	1	BLUELED-0805
REDLED-0805		LD2	1	REDLED-0805
RES	R1		1	4K7
RES	R2, R3		2	2K2
RES	R4		1	1K
RES	R5		1	12K
RES	R6, R7		2	390R
RES	R8, R9, R11, R12, R13, R14, R15, R16, R18		9	10k
RES	R10		1	390K
RES	R17		1	NOT MOUNT
KMR2	KMR2 Tactile Switch	SW1, SW2	2	KMR2
TPS3809K33DBVR		U1	1	
W25X64BVSSIG		U2	1	
USB micro-AB Rec.	5-pin Female SMT USB mini-AB (OTG) Receptacle Molex	X1	1	CON_USB_micro_AB
12MHz CM6035		Y1	1	







Ipc3143.PrjPCB					
Type	Size	Number	File Name	Description	
SCH	A1	1	Ipc3143.SCHDOC	Power	
JOB			Ipc3143.OutJob		
PCB			Ipc3143.PCBDOC	107x140mm	
Revision Number		Date	File Name	Who	Description
A		01/05/10	Ipc3143.PriPCB	VP	New release

Title		
Size A4	Number	Revision
Date:	2/26/2021	Sheet of
File:	E:\BACKUP-4-12-2012\main SchDrcDrawn By:	

J1	
DAT3	1 SD_DAT3
CMD	2 SD_CMD
VSS	3 GND
VDD	4 V3
CLK	5 SD_CLK
VSS	6 GND
DAT0	7 SD_DAT0
DAT1	8 SD_DAT1
DAT2	9 SD_DAT2
CD	10 SD_DETECT
CSW	11 GND
WP	12 SD_WP
GND	13 GND

SD-ATTEND-104D

The diagram shows the connection of six SD pins (SD_DAT0, SD_DAT1, SD_DAT2, SD_DAT3, SD_CMD, SD_DETECT) to the 3V3 power rail. Each pin is connected to 3V3 through a 10kΩ resistor (R8-R13) and to ground through another 10kΩ resistor (R14). The ground connections for all resistors converge at a single point, which is then connected to the GND rail. Additionally, two capacitors, C23 (1uF) and C24 (1uF), are connected between the 3V3 rail and the GND rail, positioned near the SD connector.

J2

GND1	1	
CLK	2	CART_CLK
NC3	3	CARD_NC3
RCS	4	CART_RCS
RESET	5	CART_RESET
ECS	6	CART_ECS
IRQ	7	CART_IRQ
VCC3V3	8	CART_VCC
D0	9	D0
D1	10	D1
D2	11	D2
D3	12	D3
D4	13	D4
D5	14	D5
D6	15	D6
D7	16	D7
GND2	17	

System-17 Connector

UG-2864HSWEG01

Title		
Size A4	Number	Revision
Date:	2/26/2021	Sheet of
File:	F:\BACKUP-4-12-2012\..\SD.SchDoc	Drawn By:

