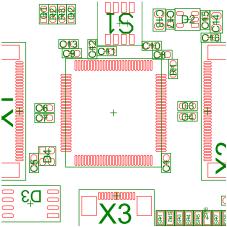
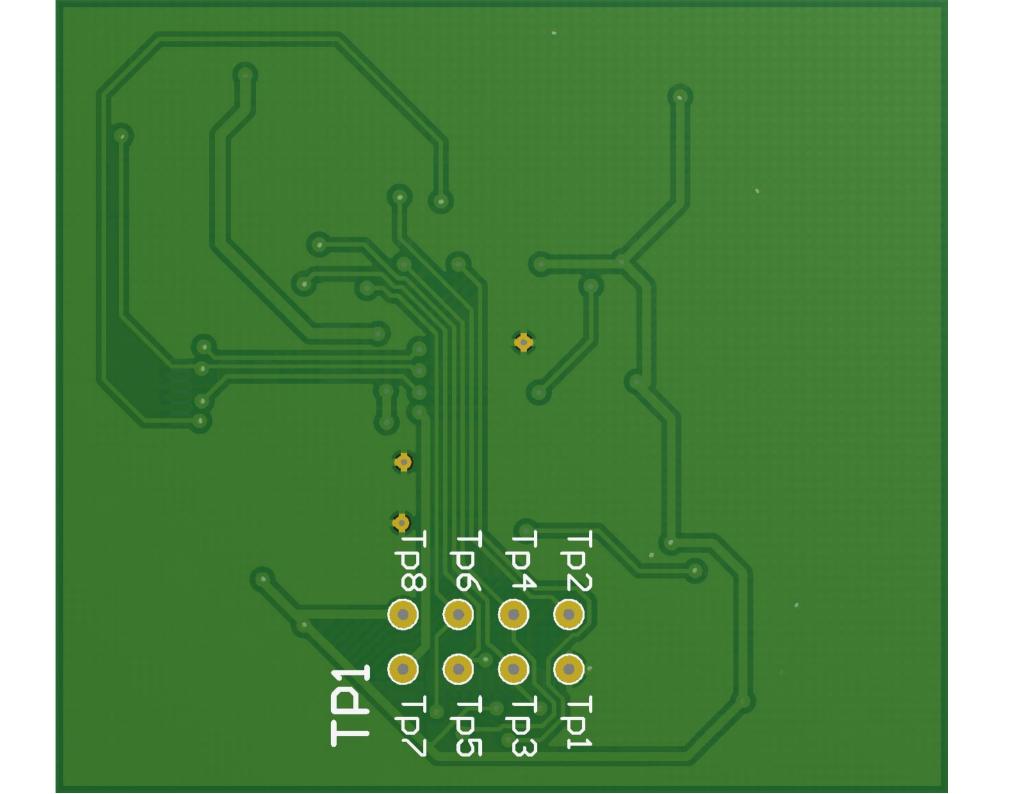
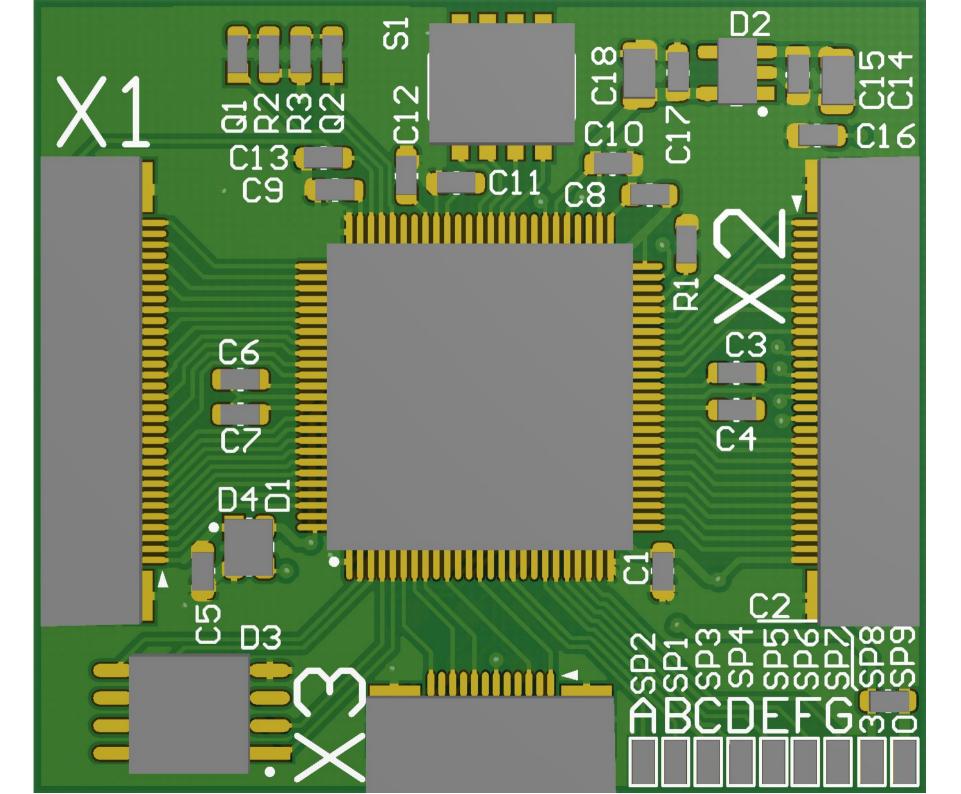


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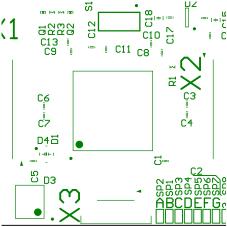


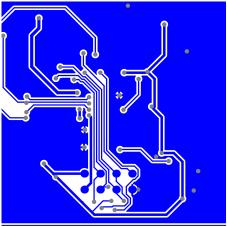


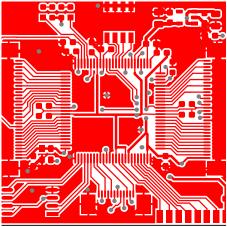


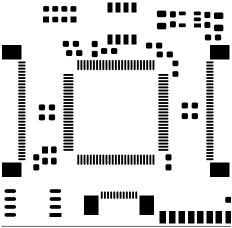


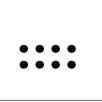
Tp2() Tp4() Tp6() Tp8() 0 Tp1 0 Tp3 0 Tp7

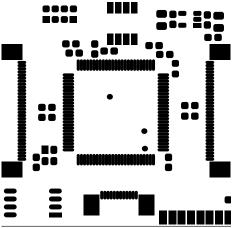


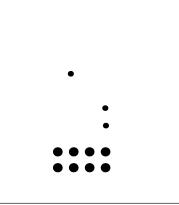












		Component list Source Data From: Project: Variant:			Bill of Materials For Project [WKF.PRJPCB] (No PCB Document Selected)						
					WKF.PRJPCB WKF.PRJPCB None		Alburra				
							Altıum.				
		Report Date: 2/26/2021 Print Date: 26-Feb-21			9:38:37 AM 9:38:43 AM		Think it, Design it, Build it™				
Description	Footprint	Quantity		Designator		re Column Name Error:Manufacturer1 F	nn Name Error:Un	uit Column Name Error:Manufacture	Column Name Error:Manufacturer2	Pnn Name Error:Unit	Total Price (\$)
pacitor	CAPC1608X09[0603]N	12	C1, C2,	C3, C4, C5, C6, C7, C8, C9, C10, C11, C12							
pacitor	CAPC1608X09[0603]N	2	C13, C1	6							
	CAPC2012X11[0805]N	2	C14, C1	8							
	CAPC1608X09[0603]N	2	C15, C1	7							
	TSQFP50P1600X1400X120-100N	1	D1								
	SOT95P280X125-5N SOT23-5	1	D2								
Memory Configuration: 2M x 8bit; Clock Frequency:50M+tz; Supply Voltage Range:2:7V to 3.6V; Memory IC Case Style:SOIC; No. of;	SOIC127P800X216-8N 8S2 0.208" Body	1	D3								
	QP_L2.5W2H0.9mmR	1	D4								
	Vd1608X06[0603]N AC	1	Q1								
	Vd1608X06[0603]N AC	1	Q2								
	RESC1608X06[0603]N	3	R1, R2,	R3							
	CHS-04TA2	1	S1								
Header, 30-Fin, Dual row	3-1734839-2	2	X1, X2								
	1-1734839-2	1	X3	-							
										TOTAL PRICE	0.000