

A

B

C

D

RST0	1	/RESET	GND	40	GND
GND	2			39	ACTIVE_0
DD7_0	3	GND	IDE40/ACTIVE	38	IDE_CS1_0
DD8_0	4	DD7	/IDE_CS1	37	IDE_CS0_0
DD6_0	5	DD8	/IDE_CS0	36	DA2_0
DD9_0	6	DD6	DA2	35	DA0_0
DD5_0	7	DD9	DA0	34	✗ DA1_0
DD10_0	8	DD5	n/c	33	✗ DA1_0
DD4_0	9	DD10	DA1	32	IOCS16_0
DD11_0	10	DD4	/IOCS16	31	IRQR_0
DD3_0	11	DD11	IRQR	30	GND
DD12_0	12	DD3	GND	29	✗ ALE_0
DD2_0	13	DD12	n/c	28	✗ ALE_0
DD13_0	14	DD2	ALE	27	IO_CH_RDY_0
DD1_0	15	DD13	IO_CH_RDY	26	GND
DD14_0	16	DD1	GND	25	IOR_0
DD0_0	17	DD14	/IOR	24	GND
DD15_0	18	DD0	GND	23	IOW_0
GND	19	DD15	/IOW	22	GND
KEY_0	20	GND	GND	21	✗
		KEY	n/c		

IDE1

Con1	1	2	Con2
+5V	3	4	GND
VCC	5	6	Con3
Con4	7	8	Con5
Con6	9	10	Con7
Con8	11	12	Con9
Con10	13	14	Con11
Con12	15	16	Con13
Con14	17	18	Con15
Con16	19	20	Con17
Con18	21	22	Con19
JTAG1	23	24	JTAG2
JTAG3	25	26	JTAG4
Con24	27	28	Con25
Con26	29	30	Con27
Con28	31	32	Con29
Con30	33	34	Con31
Con32	35	36	Con33
Con34	37	38	GPIO4
Con36	39	40	Con37
Con38	41	42	Con39
Con40	43	44	Con41
Con42	45	46	Con43
Con44	47	48	Con45
GND	49	50	GND

Header 25X2

Con1	1	2	Con2
+5V	3	4	GND
VCC	5	6	Con3
Con4	7	8	Con5
Con6	9	10	Con7
Con8	11	12	Con9
Con10	13	14	Con11
Con12	15	16	Con13
Con14	17	18	Con15
Con16	19	20	Con17
Con18	21	22	Con19
JTAG1	23	24	JTAG2
JTAG3	25	26	JTAG4
Con24	27	28	Con25
Con26	29	30	Con27
Con28	31	32	Con29
Con30	33	34	Con31
Con32	35	36	Con33
Con34	37	38	GPIO4
Con36	39	40	Con37
Con38	41	42	Con39
Con40	43	44	Con41
Con42	45	46	Con43
Con44	47	48	Con45
GND	49	50	GND

Header 25X2

TMS	R3	JTAG1
	OR	
TDO	R4	JTAG2
	OR	
TDI	R5	JTAG3
	OR	
TCK	R6	JTAG4
	OR	

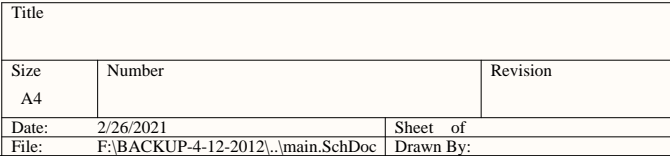
	1	2	
	3	4	
	5	6	
	7	8	
B_N_STCS0	9	10	B_N_STCS1
	11	12	
B_N_WE	13	14	B_N_OE
BA15	15	16	BA14
BA13	17	18	BA12
BA11	19	20	BA10
BA9	21	22	BA8
BA7	23	24	BA6
BA5	25	26	BA4
BA3	27	28	BA2
BA1	29	30	BA0
	31	32	
BD0	33	34	BD1
BD2	35	36	BD3
BD4	37	38	BD5
BD6	39	40	BD7
BD8	41	42	BD9
BD10	43	44	BD11
BD12	45	46	BD13
BD14	47	48	BD15
GND	49	50	GND

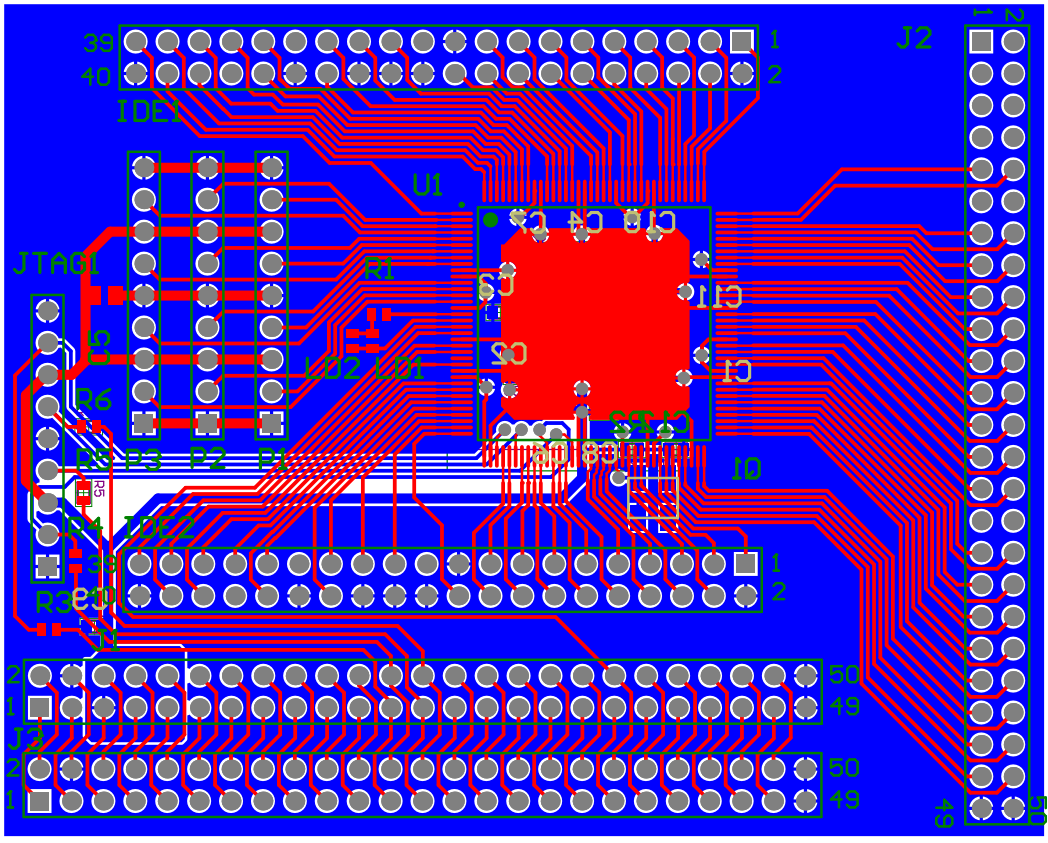
Header 25X2

RST1	1	/RESET	GND	40	GND
GND	2			39	ACTIVE_1
DD7_1	3	GND	IDE40/ACTIVE	38	IDE_CS1_1
DD8_1	4	DD7	/IDE_CS1	37	IDE_CS0_1
DD6_1	5	DD8	/IDE_CS0	36	DA2_1
DD9_1	6	DD6	DA2	35	DA0_1
DD5_1	7	DD9	DA0	34	✗ DA1_1
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DD12_1	12	DD3	GND	29	✗ ALE_1
DD2_1	13	DD12	n/c	28	✗ ALE_1
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DD1_1	15	DD13	IO_CH_RDY	26	GND
DD14_1	16	DD1	GND	25	IOR_1
DD0_1	17	DD14	/IOR	24	GND
DD15_1	18	DD0	GND	23	IOW_1
GND	19	DD15	/IOW	22	GND
KEY_1	20	GND	GND	21	✗
		KEY	n/c		

IDE2

Title		
Size	Number	Revision
A4		
Date:	2/26/2021	Sheet of
File:	F:\BACKUP-4-12-2012\.\Connectors.Sch	Down By:





Comment	Description	Designator	Footprint	LibRef	Quantity
CAP-NP		C1, C2, C3, C4, C6, C7, C8, C9, C10, C11, C12	CAPC1608N	1uF, 1uF, 1uF, 1uF, 1uF, 1uF, 1uF, 1uF, 1uF, 10uF, 1uF	11
CAP-NP		C5	CAPC2012N	C0805	1
IDE40_con	IDE40_con	IDE1, IDE2	IDE40_con	IDE40_con	2
Header 25X2	Header, 25-Pin, Dual row	J1, J2, J3	HDR2X25	Header 25X2	3
Header 9	Header, 9-Pin	JTAG1, P1, P2, P3	HDR1X9	Header 9	4
REDLED-0805		LD1, LD2	REDLED-0805	REDLED-0805	2
oscillator	oscillator	Q1	oscillator_5x3_2mm	oscillator	1
RES		R1, R2, R3, R4, R5, R6	RESC1609N	R0603	6
LCMXO640C-3T144C	MachXO 640 Crossover F	U1	TQFP144_N	LCMXO640C-3T144C	1

