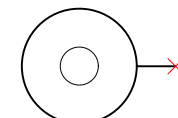


1

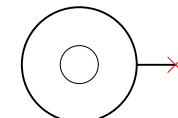
2

3

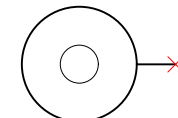
4

U_A-Headers
A-Headers.SchDocU_Analog
Analog.SchDocU_DDR3-RAM
DDR3-RAM.SchDocU_USB-PHY
USB-PHY.SchDocU_PowerSupply
PowerSupply.SchDocU_FTDI
FTDI.SchDoc

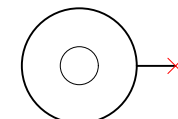
Mount.Hole 3.2mm (unplated)



Mount.Hole 3.2mm (unplated)



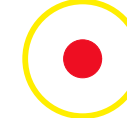
Mount.Hole 3.2mm (unplated)



Mount.Hole 3.2mm (unplated)

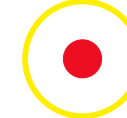
U_FPGA_B34
FPGA_B34.SchDocU_FPGA_B35
FPGA_B35.SchDocU_FPGA_MIO
FPGA_MIO.SchDocU_FPGA_RAM
FPGA_RAM.SchDocU_FPGA_PWR
FPGA_PWR.SchDoc

PM1



FIDU-DOT - small

PM2



FIDU-DOT - small

PM3



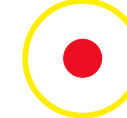
FIDU-DOT - small

PM4



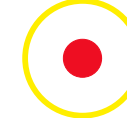
FIDU-DOT - small

PM5



FIDU-DOT - small

PM6



FIDU-DOT - small

Hardware released under CERN OHL version 1.2
<http://www.ohwr.org/projects/cernohl/wiki>



Title:

TE0723

A4

Number:

TE0723
[No Variations]

Rev.

01

Date: 08.07.2015

Copyright: Trenz Electronic GmbH

Page1 of 12

Filename:

TE0723.SchDoc

1

2

3

4

U1A

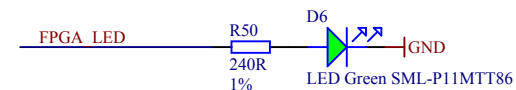
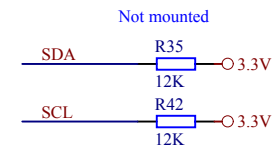
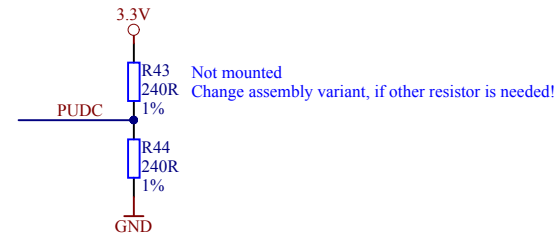
BANK 34

IO_L1P_T0_34	G11	AMUX_S1	Analog MUX
IO_L1N_T0_34	H12	AMUX_S2	Analog MUX
IO_L2P_T0_34	G12	AMUX_S0	Analog MUX
IO_L2N_T0_34	H13	BDBUS0	FTDI PORT B
IO_L3P_T0_DQS_PUDC_B_34	G14		PUDC
IO_L3N_T0_DQS_34	H14	BDBUS1	FTDI PORT B
IO_L4P_T0_34	J15	BDBUS2	FTDI PORT B
IO_L4N_T0_34	K15	BDBUS4	FTDI PORT B
IO_L5P_T0_34	J13	B34_L5_P ✗	NC
IO_L5N_T0_34	J14	BDBUS3	FTDI PORT B
IO_L6P_T0_34	H11	FPGA_LED	FPGA LED
IO_L6N_T0_VREF_34	J11	B34_L6_N ✗	NC
IO_L7P_T1_34	N13	BCBUS3	FTDI PORT B
IO_L7N_T1_34	N14	BCBUS1	FTDI PORT B
IO_L8P_T1_34	L15	BDBUS5	FTDI PORT B
IO_L8N_T1_34	M15	BDBUS7	FTDI PORT B
IO_L9P_T1_DQS_34	L14	BDBUS6	FTDI PORT B
IO_L9N_T1_DQS_34	M14	BCBUS0	FTDI PORT B
IO_L10P_T1_34	K13	B34_L10_P ✗	NC
IO_L10N_T1_34	L13	B34_L10_N ✗	NC
IO_L11P_T1_SRCC_34	K11	B34_L11_P ✗	NC
IO_L11N_T1_SRCC_34	K12	B34_L11_N ✗	NC
IO_L12P_T1_MRCC_34	L12	B34_L12_P ✗	NC
IO_L12N_T1_MRCC_34	M12	B34_L12_N ✗	NC
IO_L13P_T2_MRCC_34	N11	RXD	DIGITAL HEADER
IO_L13N_T2_MRCC_34	N12	D2	DIGITAL HEADER
IO_L15P_T2_DQS_34	P15	BCBUS2	FTDI PORT B
IO_L15N_T2_DQS_34	R15	BCBUS4	FTDI PORT B
IO_L16P_T2_34	P11	D12	DIGITAL HEADER
IO_L16N_T2_34	R11	D11	DIGITAL HEADER
IO_L17P_T2_34	R12	D13	DIGITAL HEADER
IO_L17N_T2_34	R13	SDA	DIGITAL HEADER
IO_L18P_T2_34	P13	SCL	DIGITAL HEADER
IO_L18N_T2_34	P14	BCBUS7	FTDI PORT B
IO_L19P_T3_34	M9	B34_L19_P ✗	NC
IO_L19N_T3_VREF_34	N9	B34_L19_N ✗	NC
IO_L20P_T3_34	R7	D4	DIGITAL HEADER
IO_L20N_T3_34	R8	D6	DIGITAL HEADER
IO_L21P_T3_DQS_34	M10	B34_L21_P ✗	NC
IO_L21N_T3_DQS_34	M11	TXD	DIGITAL HEADER
IO_L22P_T3_34	N7	D3	DIGITAL HEADER
IO_L22N_T3_34	N8	D7	DIGITAL HEADER
IO_L23P_T3_34	P8	D5	DIGITAL HEADER
IO_L23N_T3_34	P9	D8	DIGITAL HEADER
IO_L24P_T3_34	P10	D10	DIGITAL HEADER
IO_L24N_T3_34	R10	D9	DIGITAL HEADER

XC7Z7010-CLG225

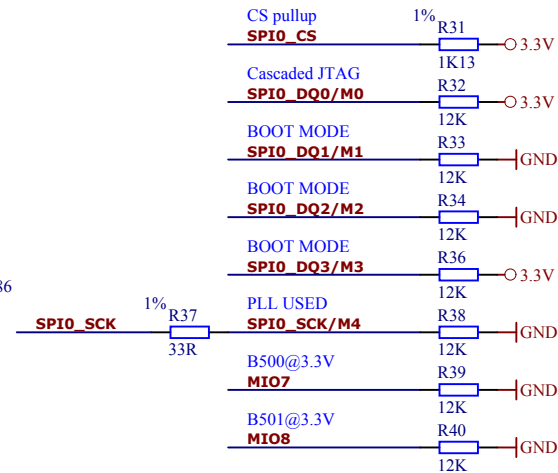
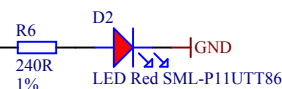
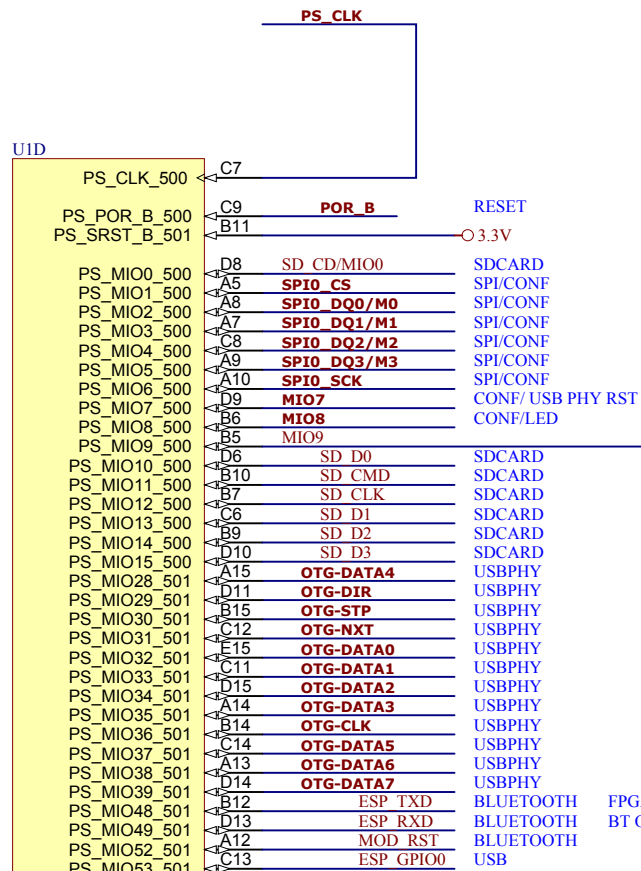
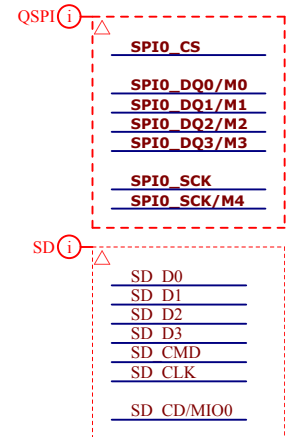
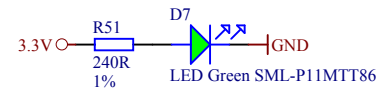
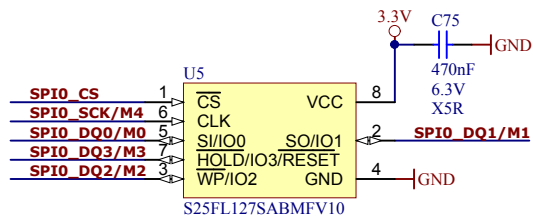
B34

①



Title: TE0723 - FPGA B34			
A4	Number: TE0723 [No Variations]	Rev. 01	
Date: 08.07.2015	Copyright: Trenz Electronic GmbH		Page2 of 12
Filename: FPGA_B34.SchDoc			

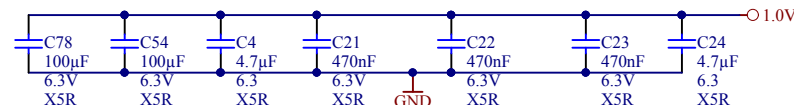
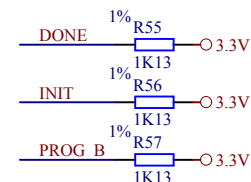
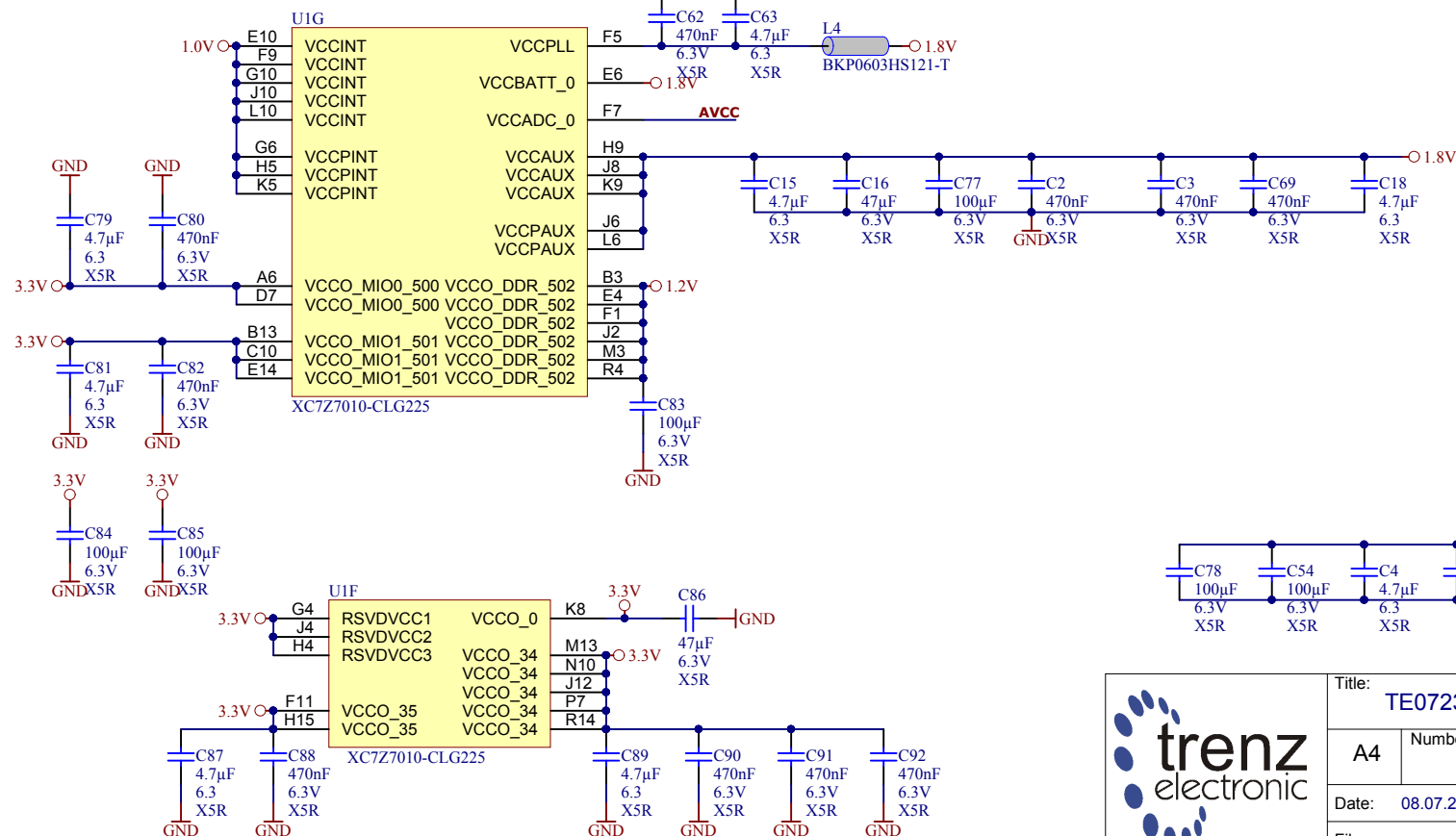
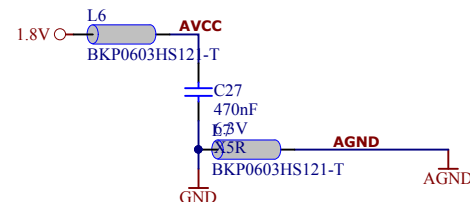
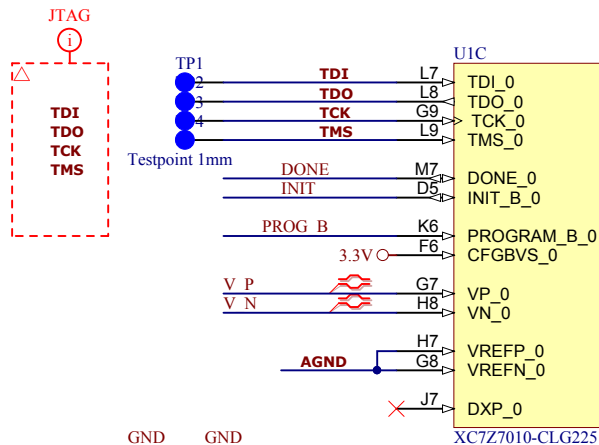
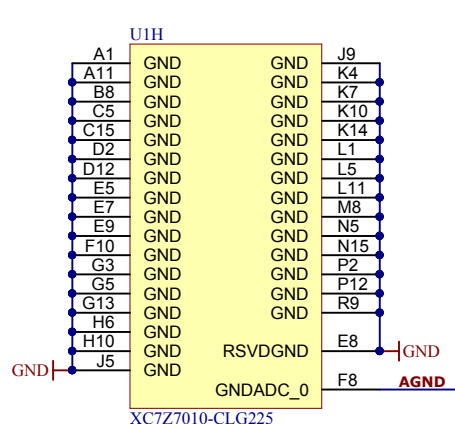




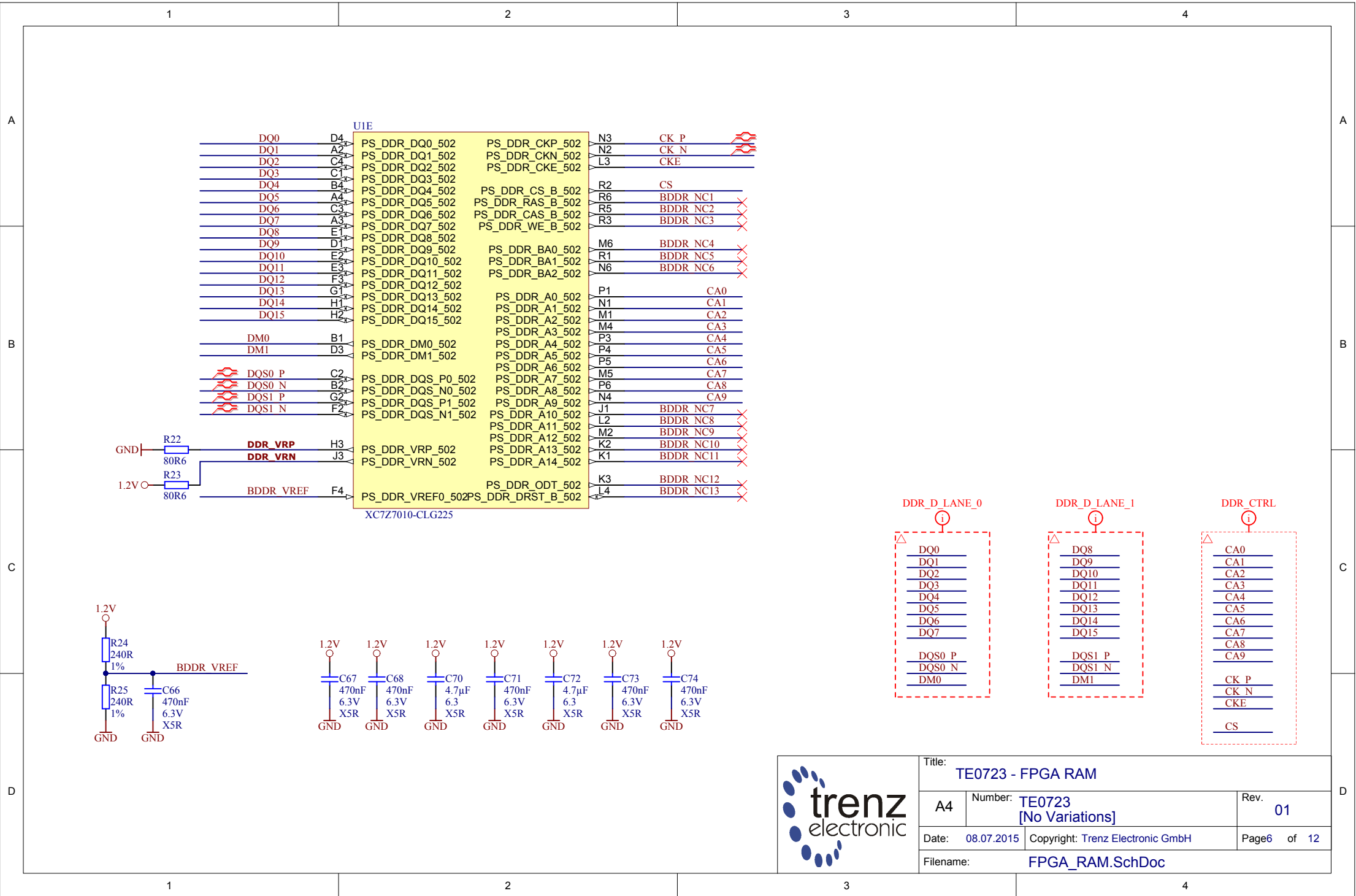
FPGA OUT -> BT IN
BT OUT -> FPGA IN



Title: TE0723 - FPGA MIO			
A4	Number: TE0723 [No Variations]	Rev. 01	
Date: 08.07.2015	Copyright: Trenz Electronic GmbH		Page 4 of 12
Filename: FPGA_MIO.SchDoc			

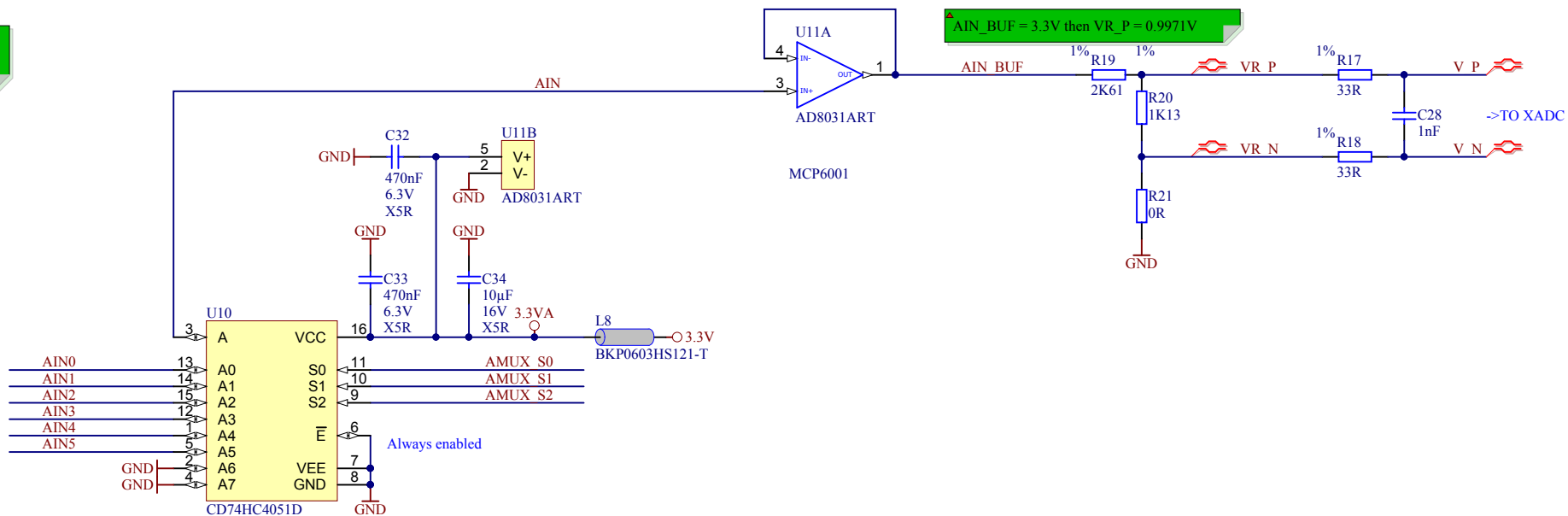


Title: TE0723 - FPGA PWR			
A4	Number: TE0723 [No Variations]	Rev. 01	
Date: 08.07.2015	Copyright: Trenz Electronic GmbH		Page5 of 12
Filename: FPGA_PWR.SchDoc			



Default:
 * AMUX disabled
 * Digital buffers disabled

->FROM AD HEADER



Title: TE0723 - Analog Input		
A4	Number: TE0723 [No Variations]	Rev. 01
Date: 08.07.2015	Copyright: Trenz Electronic GmbH	Page 7 of 12
Filename: Analog.SchDoc		

A

B

C

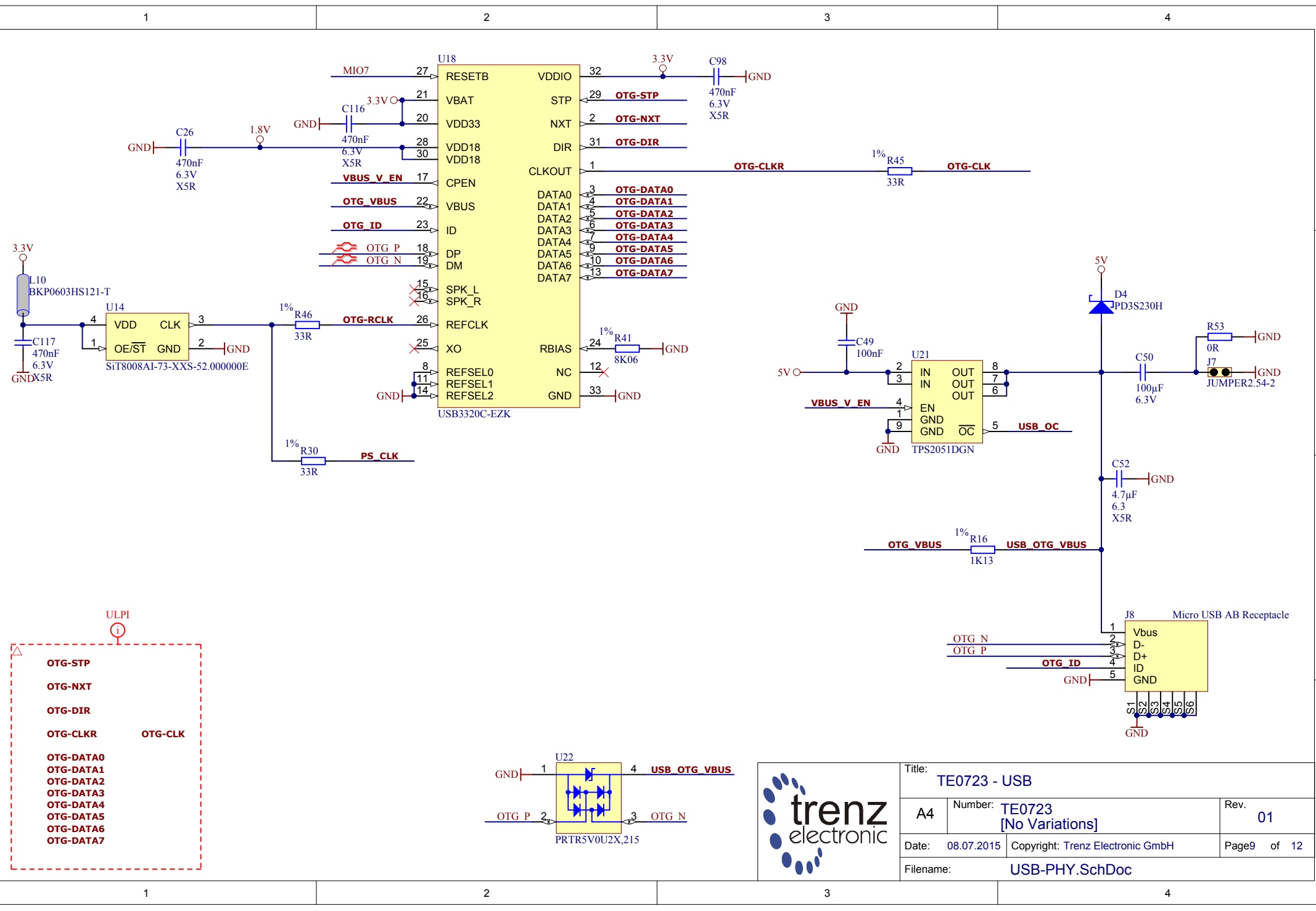
D

A

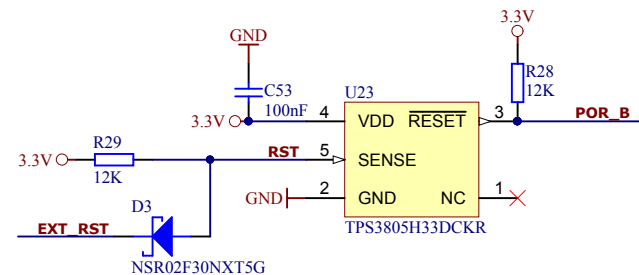
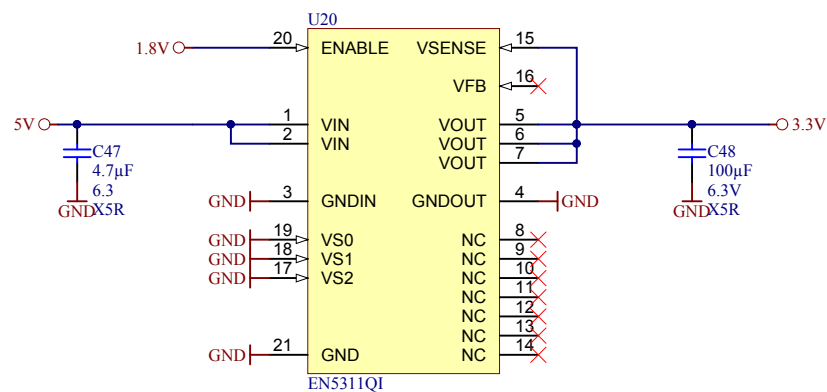
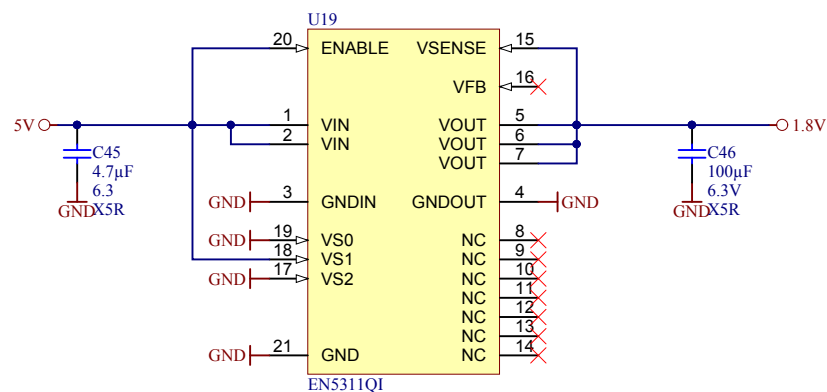
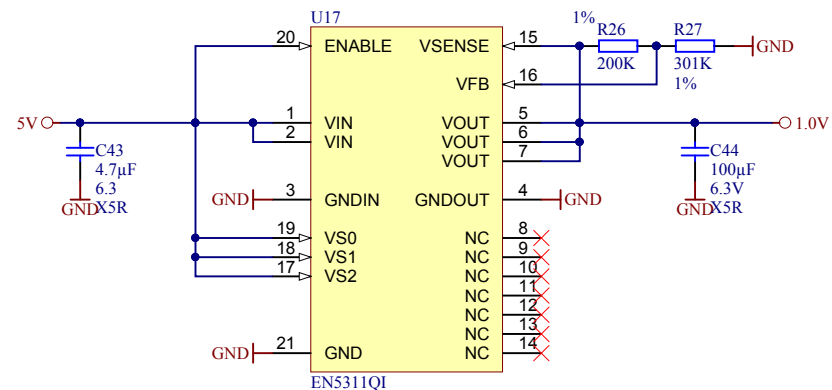
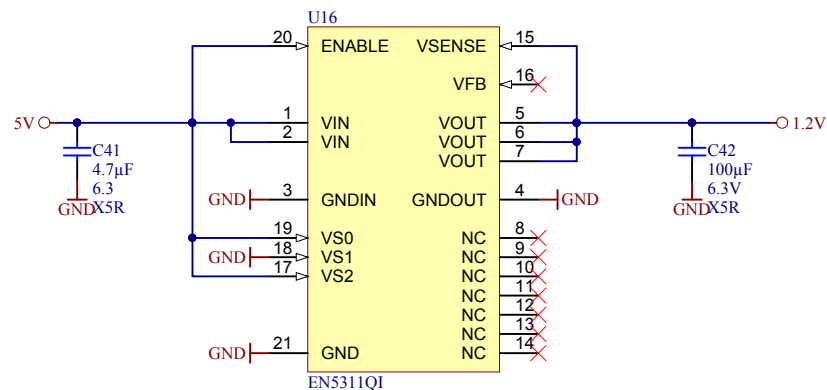
B

C

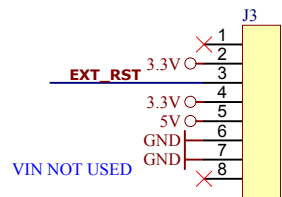
D



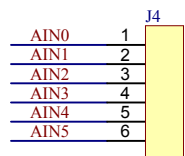
Title: TE0723 - USB			
A4	Number: TE0723 [No Variations]	Rev. 01	
Date: 08.07.2015	Copyright: Trenz Electronic GmbH		Page9 of 12
Filename: USB-PHY.SchDoc			



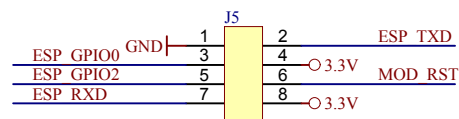
Title: TE0723 - PowerSupply		
A4	Number: TE0723 [No Variations]	Rev. 01
Date: 08.07.2015	Copyright: Trenz Electronic GmbH	Page 11 of 12
Filename: PowerSupply.SchDoc		



Female Header, 2.54mm, 1row, 8pol.

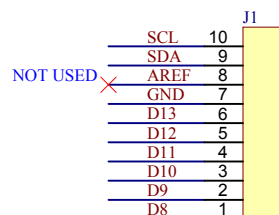
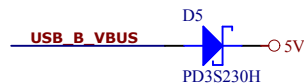


Female Header, 2.54mm, 1row, 6pol.

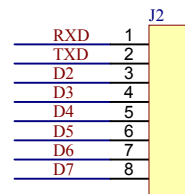


Pin Header 2.54 2Row 8Pins

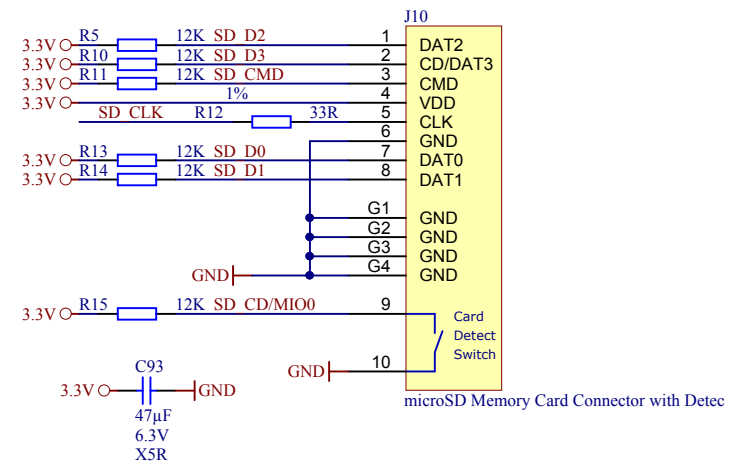
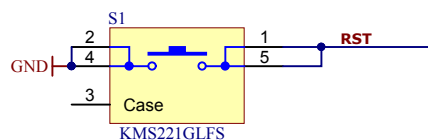
Need to change on female connector



Female Header, 2.54mm, 1row, 10pol.



Female Header, 2.54mm, 1row, 8pol.



microSD Memory Card Connector with Detect

Title: TE0723 - Connectors		
A4	Number: TE0723 [No Variations]	Rev. 01
Date: 08.07.2015	Copyright: Trenz Electronic GmbH	Page 12 of 12
Filename: A-Headers.SchDoc		

