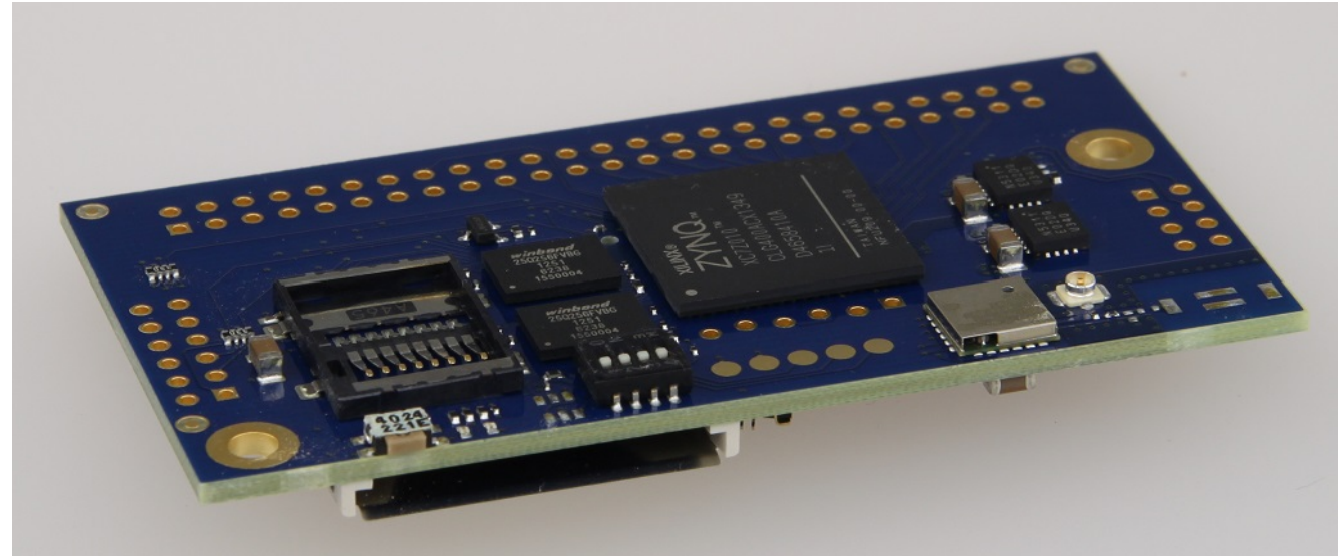





open source
hardware

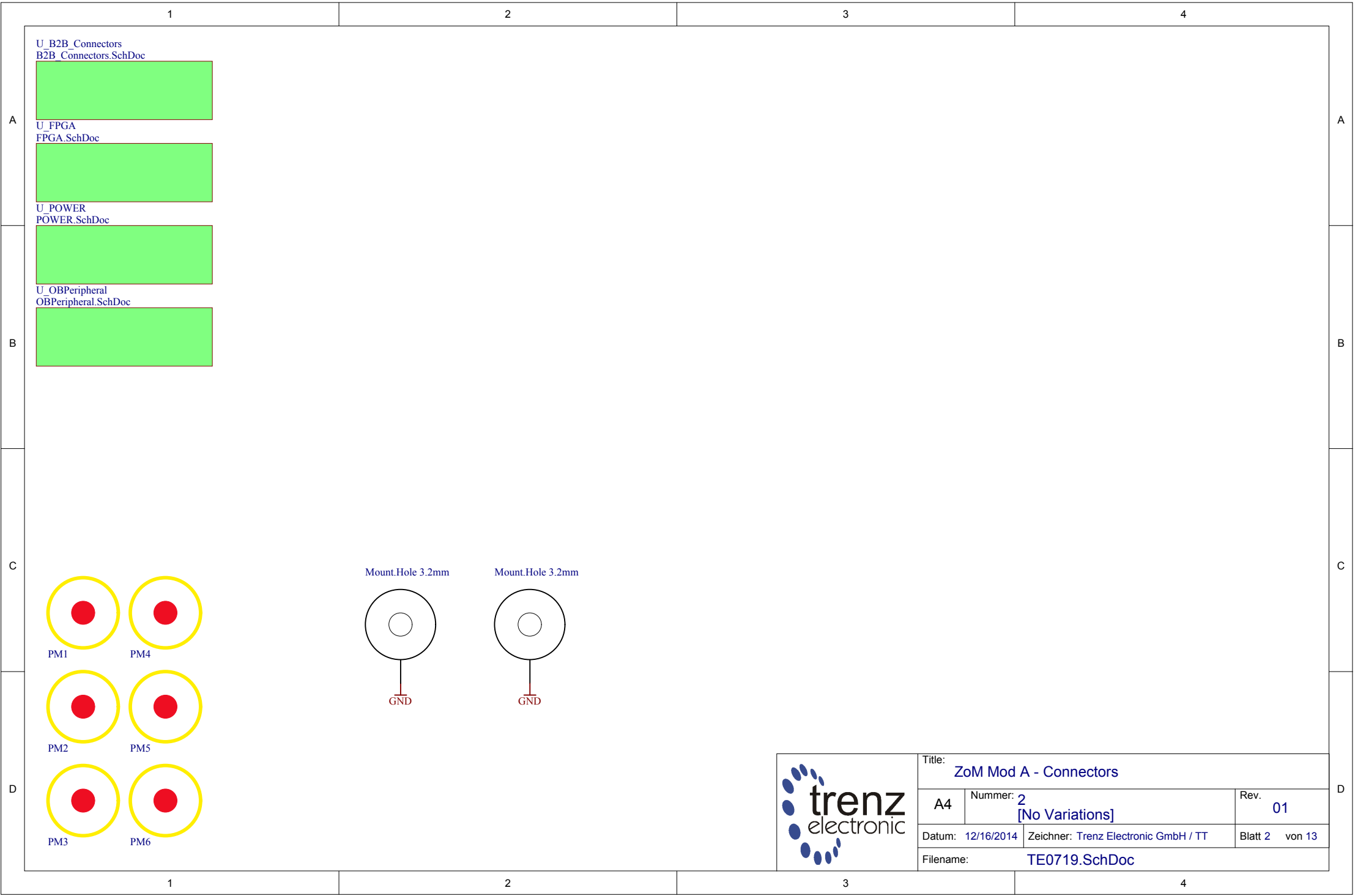


<http://igg.me/at/zynq>

<https://wiki.trenz-electronic.de/display/OSHW/OZOM-A>

U_TE0719
TE0719.SchDoc

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|  | Title: ZoM Mod A | | |
| | A4 | Nummer: 1 [No Variations] | Rev. 01 |
| | Datum: 12/16/2014 | Zeichner: Trenz Electronic GmbH / TT | Blatt 1 von 13 |
| | Filename: ZoM-Model-A.SchDoc | | |



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A

A

B

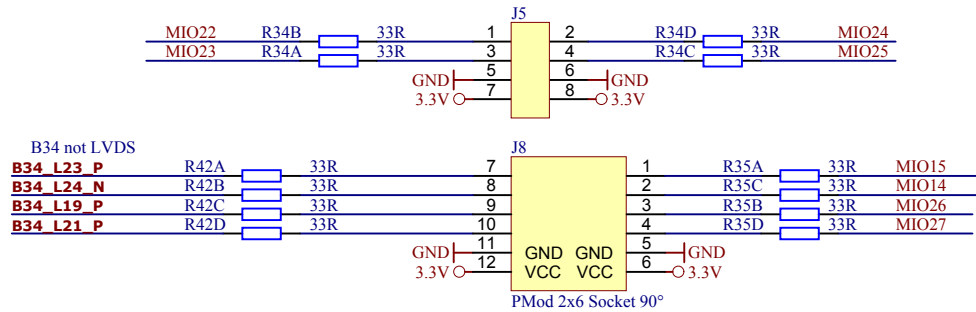
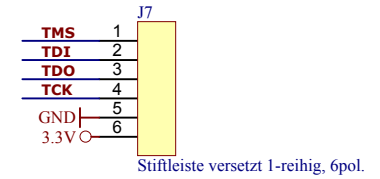
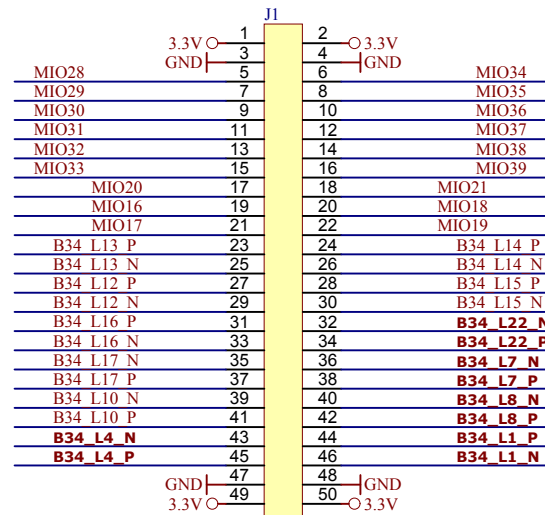
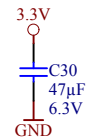
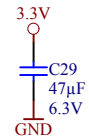
B

C

C

D

D



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|---------------------------------|--------------------------------------|----------------|--|
| Title: ZoM Mod A - Connectors | | | |
| A4 | Nummer: 3 [No Variations] | Rev. 01 | |
| Datum: 12/16/2014 | Zeichner: Trenz Electronic GmbH / TT | Blatt 3 von 13 | |
| Filename: B2B_Connectors.SchDoc | | | |

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A

U_FPGA-Power
FPGA-Power.SchDoc

U_FPGA-Misc
FPGA-Misc.SchDoc

U_B13
B13.SchDoc

U_B34
B34.SchDoc

U_B35
B35.SchDoc

U_PS_MIO
PS_MIO.SchDoc

U_PS-DDR
PS-DDR.SchDoc

A

B

C

D

C


D

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|---|--|----------------------------------|------------------------------|
|  | | Title: ZoM Mod A - FPGA | |
| | | A4 | Number: 5 [No Variations] |
| | | Date: 12/16/2014 | Rev. 01 |
| | | Copyright: Trenz Electronic GmbH | Page5 of 13 |
| | | Filename: FPGA.SchDoc | |

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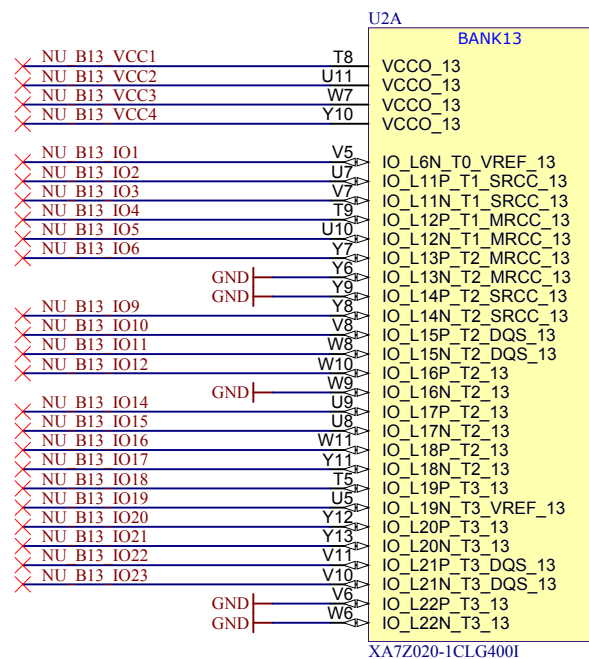
A


B

C

D

Bank 13 not used



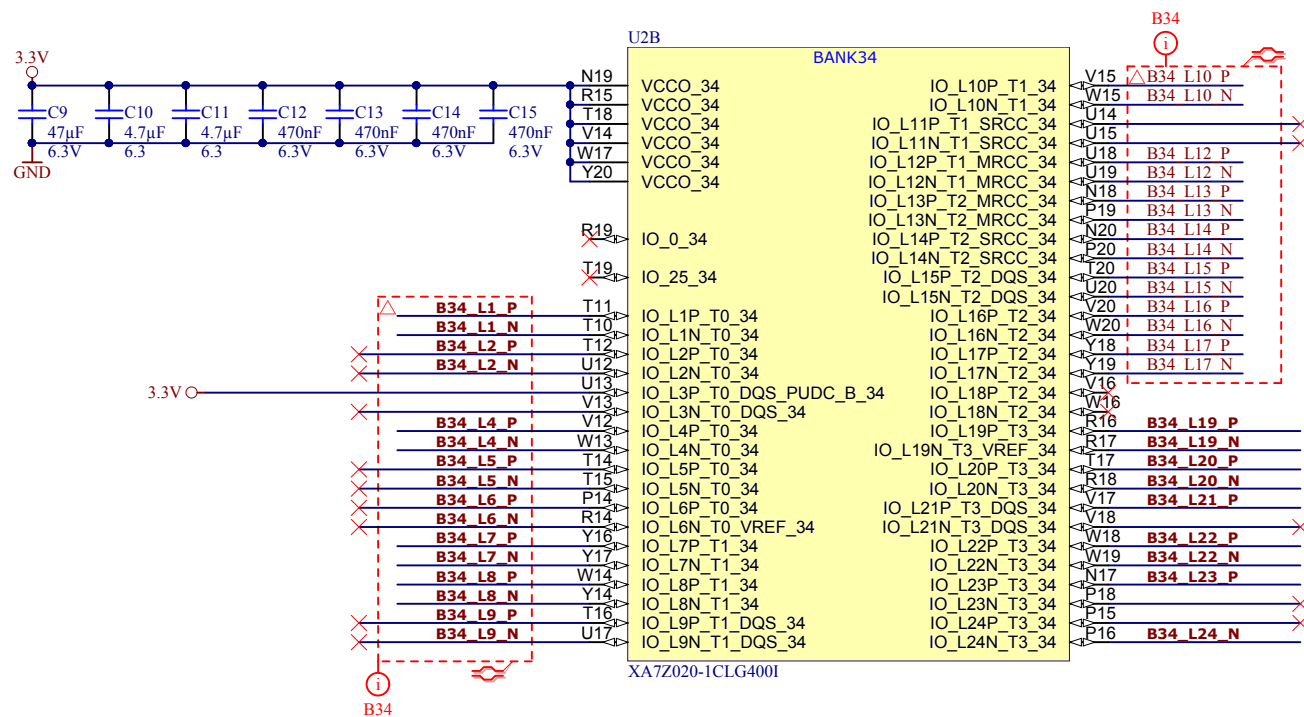
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|  | | Title: ZoM Mod A - FPGA Bank 13 | | |
| | | A4 | Nummer: 6 [No Variations] | Rev. 01 |
| | | Datum: 12/16/2014 | Zeichner: Trenz Electronic GmbH / TT | Blatt 6 von 13 |
| | | Filename: B13.SchDoc | | |

1

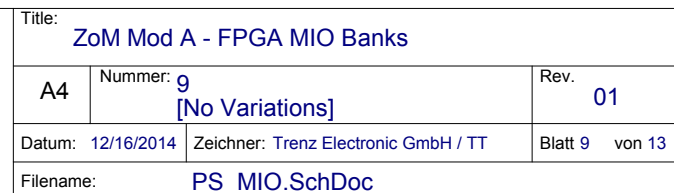
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3

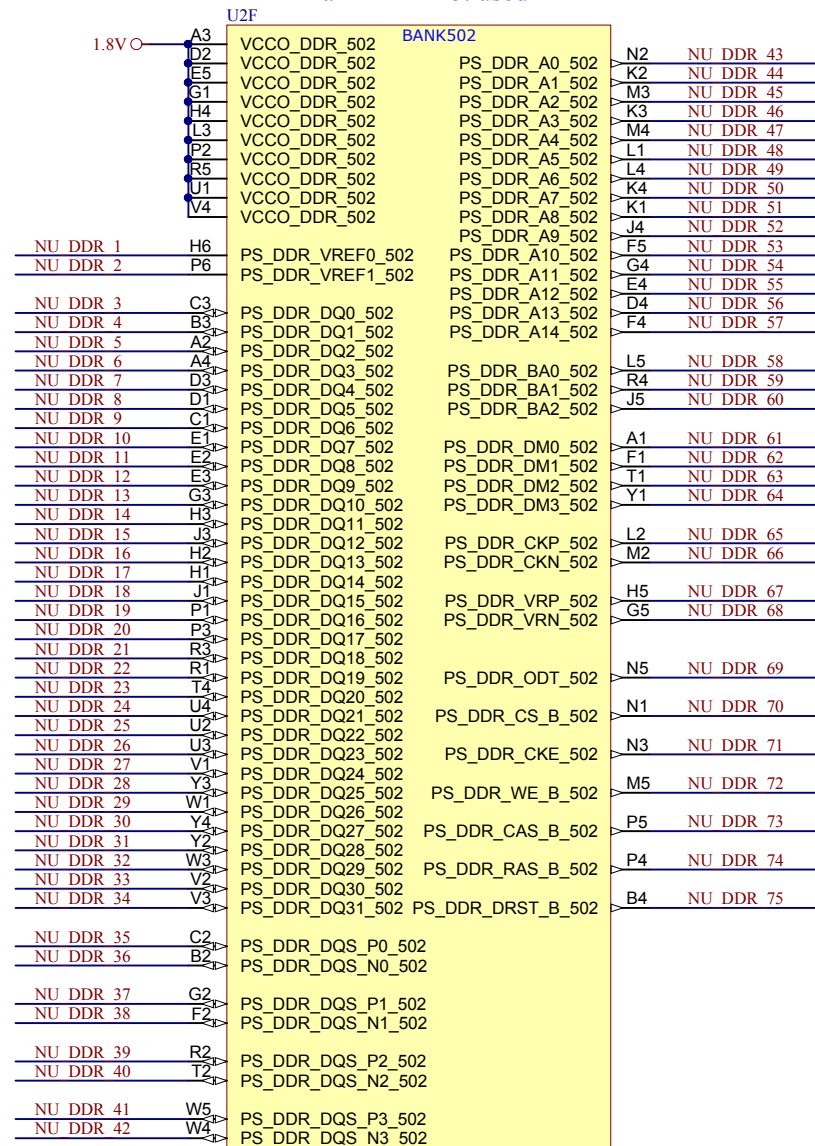
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| Title: ZoM Mod A - FPGA Bank 34 | | |
| A4 | Nummer: 7 [No Variations] | Rev. 01 |
| Datum: 12/16/2014 | Zeichner: Trenz Electronic GmbH / TT | Blatt 7 von 13 |
| Filename: B34.SchDoc | | |



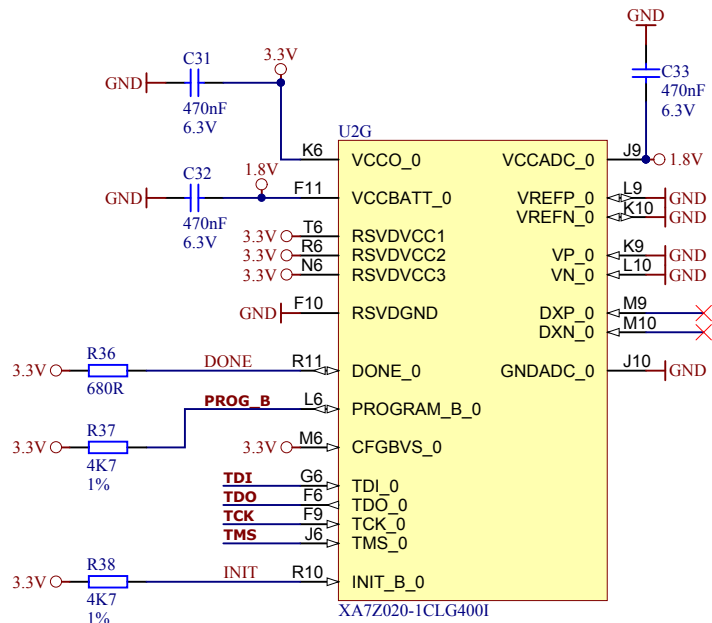
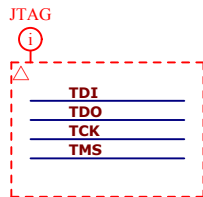
Bank DDR not used



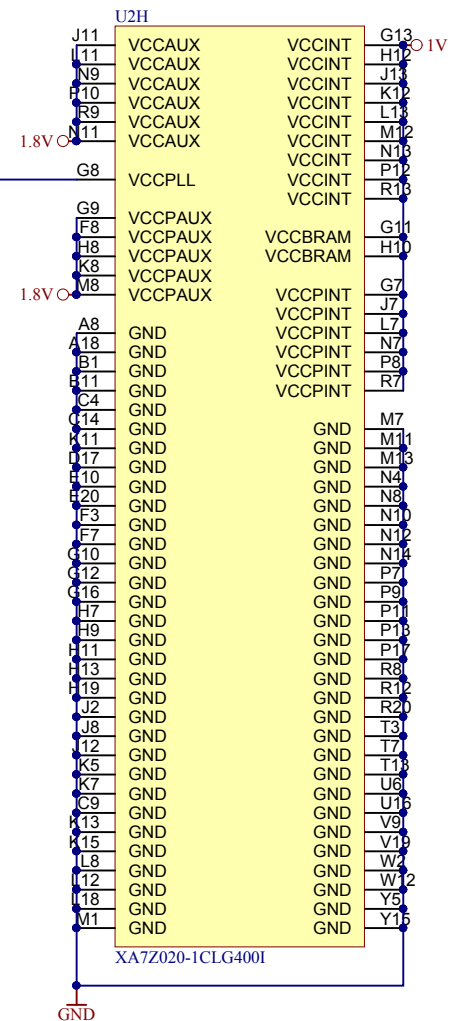
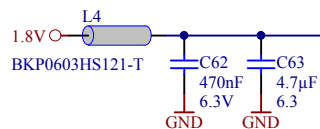
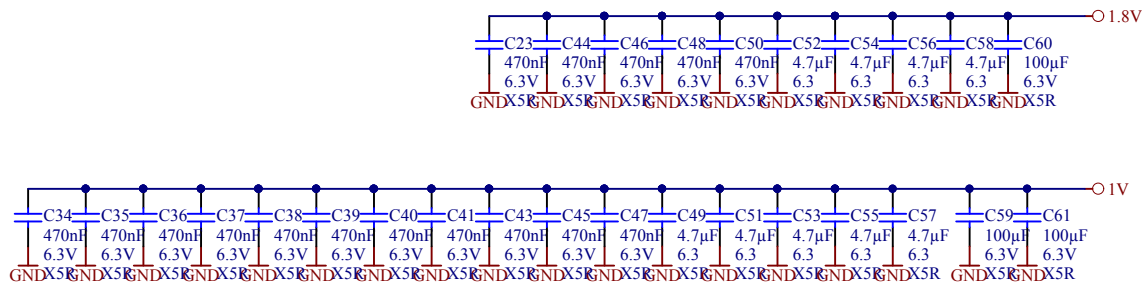
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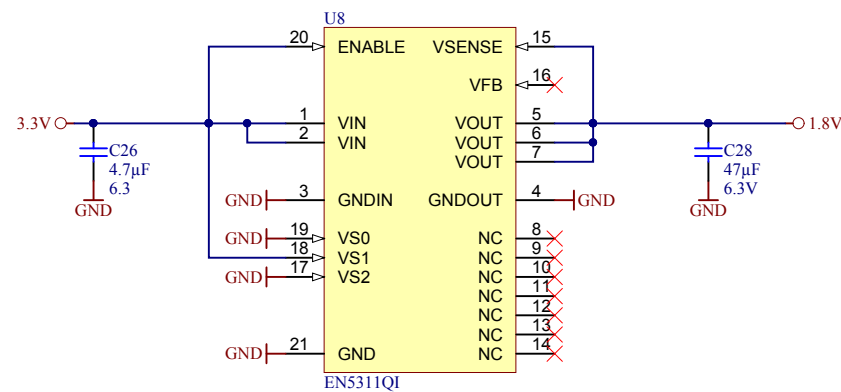
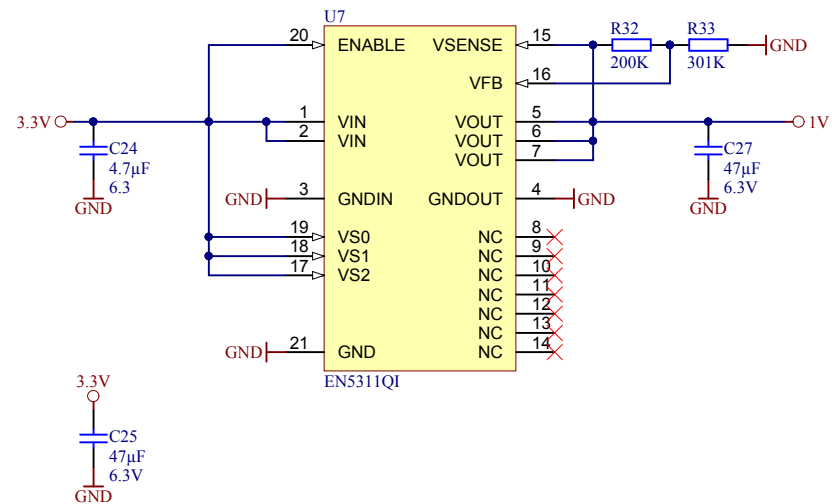
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| Title: ZoM Mod A - FPGA DDR Bank | | |
| A4 | Numer: 10 [No Variations] | Rev. 01 |
| Datum: 12/16/2014 | Zeichner: Trenz Electronic GmbH / TT | Blatt 10 von 13 |
| Filename: PS-DDR.SchDoc | | |



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|------------------------------|--------------------------------------|--|-----------------|
| Title: ZoM Mod A - FPGA Misc | | | |
| A4 | Nummer: 11 [No Variations] | | Rev. 01 |
| Datum: 12/16/2014 | Zeichner: Trenz Electronic GmbH / TT | | Blatt 11 von 13 |
| Filename: FPGA-Misc.SchDoc | | | |



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|---|-------------------------------|--------------------------------------|-----------------|
|  | Title: ZoM Mod A - FPGA Power | | |
| | A4 | Number: 12 [No Variations] | Rev. 01 |
| | Datum: 12/16/2014 | Zeichner: Trenz Electronic GmbH / TT | Blatt 12 von 13 |
| | Filename: FPGA-Power.SchDoc | | |



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|--------------------------|----------------------------------|---------|---------------|
| Title: ZoM Mod A - Power | | | |
| A4 | Number: 13 [No Variations] | Rev. 01 | |
| Date: 12/16/2014 | Copyright: Trenz Electronic GmbH | | Page 13 of 13 |
| Filename: POWER.SchDoc | | | |