

# Metal Capacity check/get

## metal capacity check

```
metal capacity check -m ny,da,sg -P c3.medium.x86 -q 1
```

METRO	PLAN	QUANTITY	AVAILABILITY
ny	c3.medium.x86	1	true
da	c3.medium.x86	1	true
sg	c3.medium.x86	1	true

```
APMBLY64MGWK GK:~ anusoni$ metal capacity check
```

```
Error: required flag(s) "plans", "quantity" not set
```

```
Usage:
```

```
metal capacity check (-m <metro> | -f <facility>) -P <plan> -q <quantity>
[flags]
```

```
Examples:
```

```
# Checks if 10 c3.medium.x86 servers are available in NY or Dallas:
```

```
metal capacity check -m ny,da -P c3.medium.x86 -q 10
```

```
# Checks if Silicon Valley or Dallas has either 4 c3.medium.x86 or m3.large.x86
```

```
metal capacity check -m sv,da -P c3.medium.x86,m3.large.x86 -q 4
```

```
Flags:
```

-f, --facilities strings	A facility or list of facilities.
-h, --help	help for check
-m, --metros strings	A metro or list of metros.
-P, --plans strings	A plan or list of plans.
-q, --quantity int	The number of devices wanted.

```
Global Flags:
```

--config string	Path to JSON or YAML configuration file
--exclude strings	Comma separated Href references to collapse in results, may be dotted three levels deep
--filter stringArray	Filter 'get' actions with name value pairs. Filter is not supported by all resources and is implemented as request query parameters.
--include strings	Comma separated Href references to expand in results, may be dotted three levels deep
-o, --output string	Output format (*table, json, yaml)
--search string	Search keyword for use in 'get' actions. Search is not supported by all resources.

--sort-by string	Sort fields for use in 'get' actions. Sort is not supported by all resources.
--sort-dir string	Sort field direction for use in 'get' actions. Sort is not supported by all resources.
--token string	Metal API Token (METAL_AUTH_TOKEN)

## Output As JSON

```
APMBLY64MGWK GK:~ anusoni$ metal capacity check -m ny,da,sg -P c3.medium.x86 -q 1
--output json
{
  "servers": [
    {
      "metro": "ny",
      "plan": "c3.medium.x86",
      "quantity": 1,
      "available": true
    },
    {
      "metro": "da",
      "plan": "c3.medium.x86",
      "quantity": 1,
      "available": true
    },
    {
      "metro": "sg",
      "plan": "c3.medium.x86",
      "quantity": 1,
      "available": true
    }
  ]
}
```

```
APMBLY64MGWK GK:~ anusoni$ metal capacity check -m ny,da,sg -P c3.medium.x86 -q 1
--output yaml
servers:
- available: true
  metro: ny
  plan: c3.medium.x86
  quantity: 1
- available: true
  metro: da
  plan: c3.medium.x86
  quantity: 1
- available: true
  metro: sg
  plan: c3.medium.x86
```

quantity: 1

## metal capacity check

```
APMBLY64MGWK GK:~ anusoni$ metal capacity get
```

	PLAN	LEVEL	
am6	c3.medium.x86	normal	
am6	c3.small.x86	limited	
am6	m3.large.x86	normal	
am6	m3.small.x86	normal	
am6	n2.xlarge.x86	normal	
am6	t3.small.x86	unavailable	
am6	a3.large.x86	normal	
am6	m2.xlarge.x86	unavailable	
am6	n3.xlarge.x86	normal	
am6	s3.xlarge.x86	limited	
am6	c2.medium.x86	unavailable	
dc13	c2.medium.x86	unavailable	
dc13	c3.large.arm64	unavailable	
dc13	c3.medium.x86	normal	
dc13	m2.xlarge.x86	unavailable	
dc13	m3.small.x86	normal	
dc13	n2.xlarge.x86	normal	
dc13	a3.large.x86	unavailable	
dc13	m3.large.x86	normal	
dc13	n3.xlarge.x86	normal	
dc13	s3.xlarge.x86	normal	
dc13	t3.small.x86	unavailable	
dc13	c3.small.x86	limited	
md2	s3.xlarge.x86	unavailable	
md2	a3.large.x86	unavailable	
md2	c3.medium.x86	normal	
md2	c3.small.x86	limited	
md2	m3.large.x86	normal	
md2	n3.xlarge.x86	normal	
md2	c2.medium.x86	normal	
md2	m3.small.x86	normal	
md2	n2.xlarge.x86	unavailable	
sg4	c3.small.x86	normal	
sg4	m3.large.x86	normal	
sg4	n2.xlarge.x86	unavailable	
sg4	a3.large.x86	unavailable	
sg4	c2.medium.x86	normal	

sg4	c3.medium.x86	normal	
sg4	m3.small.x86	normal	
sg4	n3.xlarge.x86	normal	
sg4	s3.xlarge.x86	normal	
da6	t3.small.x86	normal	
da6	c3.medium.x86	normal	
da6	c3.small.x86	normal	
da6	m3.small.x86	unavailable	
da6	n2.xlarge.x86	normal	
da6	n3.xlarge.x86	unavailable	
da6	a3.large.x86	unavailable	
da6	c2.medium.x86	normal	
da6	m3.large.x86	unavailable	
da6	s3.xlarge.x86	normal	
dc10	c3.small.x86	normal	
dc10	a3.large.x86	unavailable	
dc10	c3.large.arm64	normal	
dc10	c3.medium.x86	normal	
dc10	n2.xlarge.x86	normal	
dc10	n3.xlarge.x86	normal	
dc10	s3.xlarge.x86	normal	
dc10	c2.medium.x86	normal	
dc10	m3.large.x86	normal	
dc10	m3.small.x86	unavailable	
fr2	s3.xlarge.x86	limited	
fr2	a3.large.x86	limited	
fr2	c3.medium.x86	normal	
fr2	m3.large.x86	normal	
fr2	m3.small.x86	normal	
fr2	n2.xlarge.x86	limited	
fr2	c2.medium.x86	unavailable	
fr2	c3.small.x86	limited	
fr2	n3.xlarge.x86	normal	
ny7	c3.medium.x86	normal	
ny7	c3.small.x86	limited	
ny7	m3.large.x86	limited	
ny7	s3.xlarge.x86	unavailable	
ny7	a3.large.x86	unavailable	
ny7	m3.small.x86	limited	
ny7	n2.xlarge.x86	normal	
ny7	n3.xlarge.x86	normal	
ny7	c2.medium.x86	normal	
sl1	s3.xlarge.x86	unavailable	
sl1	a3.large.x86	unavailable	
sl1	c2.medium.x86	normal	
sl1	c3.medium.x86	normal	
sl1	m3.small.x86	normal	
sl1	c3.small.x86	limited	
sl1	m3.large.x86	normal	
sl1	n2.xlarge.x86	unavailable	
sl1	n3.xlarge.x86	normal	

dfw2	t3.small.x86	unavailable	
la4	n3.xlarge.x86	normal	
la4	a3.large.x86	unavailable	
la4	c2.medium.x86	normal	
la4	c3.small.x86	unavailable	
la4	m3.large.x86	limited	
la4	m3.small.x86	normal	
la4	n2.xlarge.x86	normal	
pa4	a3.large.x86	unavailable	
pa4	c3.medium.x86	normal	
pa4	m3.large.x86	limited	
pa4	n2.xlarge.x86	unavailable	
pa4	s3.xlarge.x86	unavailable	
pa4	c2.medium.x86	normal	
pa4	c3.small.x86	limited	
pa4	m3.small.x86	normal	
pa4	n3.xlarge.x86	normal	
sp4	c3.medium.x86	normal	
sp4	c3.small.x86	unavailable	
sp4	m3.large.x86	normal	
sp4	n2.xlarge.x86	unavailable	
sp4	s3.xlarge.x86	normal	
sp4	c2.medium.x86	unavailable	
sp4	m3.small.x86	unavailable	
sp4	n3.xlarge.x86	unavailable	
sp4	a3.large.x86	unavailable	
sv15	c2.medium.x86	normal	
sv15	c3.small.x86	normal	
sv15	m2.xlarge.x86	normal	
sv15	n2.xlarge.x86	limited	
sv15	n3.xlarge.x86	normal	
sv15	x2.xlarge.x86	unavailable	
sv15	a3.large.x86	normal	
sv15	c3.medium.x86	normal	
sv15	m3.large.x86	normal	
sv15	m3.small.x86	normal	
sv15	s3.xlarge.x86	normal	
sv15	t3.small.x86	unavailable	
sg5	c3.small.x86	unavailable	
sg5	n2.xlarge.x86	unavailable	
sg5	n3.xlarge.x86	normal	
sg5	s3.xlarge.x86	unavailable	
sg5	a3.large.x86	unavailable	
sg5	c2.medium.x86	unavailable	
sg5	c3.medium.x86	unavailable	
sg5	m3.large.x86	normal	
sg5	m3.small.x86	normal	
sin3	s3.xlarge.x86	unavailable	
sin3	t3.small.x86	unavailable	
sin3	c2.medium.x86	unavailable	
sin3	c3.medium.x86	unavailable	

sin3	c3.small.x86	unavailable	
sin3	m3.large.x86	unavailable	
at4	m3.large.x86	normal	
at4	m3.small.x86	normal	
at4	n2.xlarge.x86	unavailable	
at4	n3.xlarge.x86	normal	
at4	a3.large.x86	unavailable	
at4	c3.medium.x86	unavailable	
at4	c3.small.x86	unavailable	
at4	c2.medium.x86	unavailable	
at4	s3.xlarge.x86	unavailable	
ch3	n2.xlarge.x86	normal	
ch3	n3.xlarge.x86	normal	
ch3	a3.large.x86	unavailable	
ch3	c2.medium.x86	normal	
ch3	c3.medium.x86	normal	
ch3	c3.small.x86	unavailable	
ch3	m3.large.x86	normal	
ch3	m3.small.x86	normal	
fr8	n3.xlarge.x86	unavailable	
fr8	c2.medium.x86	unavailable	
fr8	m3.large.x86	normal	
fr8	m3.small.x86	unavailable	
fr8	n2.xlarge.x86	unavailable	
fr8	s3.xlarge.x86	unavailable	
fr8	a3.large.x86	unavailable	
fr8	c3.medium.x86	normal	
fr8	c3.small.x86	unavailable	
hk2	c3.medium.x86	normal	
hk2	m3.large.x86	limited	
hk2	m3.small.x86	normal	
hk2	n2.xlarge.x86	unavailable	
hk2	s3.xlarge.x86	unavailable	
hk2	c2.medium.x86	normal	
hk2	c3.small.x86	unavailable	
hk2	n3.xlarge.x86	normal	
hk2	a3.large.x86	unavailable	
ny5	m3.small.x86	unavailable	
ny5	s3.xlarge.x86	normal	
ny5	a3.large.x86	unavailable	
ny5	c3.medium.x86	limited	
ny5	c3.small.x86	limited	
ny5	m3.large.x86	limited	
ny5	x2.xlarge.x86	unavailable	
ny5	c2.medium.x86	normal	
ny5	m2.xlarge.x86	unavailable	
ny5	n2.xlarge.x86	normal	
ny5	n3.xlarge.x86	unavailable	
sg1	m3.small.x86	unavailable	
sg1	n2.xlarge.x86	normal	
sg1	n3.xlarge.x86	unavailable	

sg1	a3.large.x86	unavailable	
sg1	c3.medium.x86	limited	
sg1	c3.small.x86	limited	
sg1	t3.small.x86	unavailable	
sg1	c2.medium.x86	unavailable	
sg1	m3.large.x86	limited	
sg1	s3.xlarge.x86	limited	
sy4	a3.large.x86	unavailable	
sy4	c3.small.x86	normal	
sy4	n2.xlarge.x86	unavailable	
sy4	c2.medium.x86	unavailable	
sy4	c3.medium.x86	normal	
sy4	m3.large.x86	normal	
sy4	m3.small.x86	normal	
sy4	n3.xlarge.x86	normal	
sy4	s3.xlarge.x86	normal	
sy5	c3.small.x86	unavailable	
sy5	m3.large.x86	unavailable	
sy5	m3.small.x86	unavailable	
sy5	n2.xlarge.x86	unavailable	
sy5	a3.large.x86	unavailable	
sy5	c2.medium.x86	unavailable	
sy5	c3.medium.x86	normal	
sy5	n3.xlarge.x86	unavailable	
sy5	s3.xlarge.x86	unavailable	
ty11	a3.large.x86	unavailable	
ty11	c2.medium.x86	normal	
ty11	c3.small.x86	limited	
ty11	m3.small.x86	limited	
ty11	n3.xlarge.x86	normal	
ty11	s3.xlarge.x86	unavailable	
ty11	c3.medium.x86	limited	
ty11	m2.xlarge.x86	unavailable	
ty11	m3.large.x86	limited	
ty11	n2.xlarge.x86	limited	
da11	c3.medium.x86	normal	
da11	m2.xlarge.x86	unavailable	
da11	m3.large.x86	normal	
da11	t3.small.x86	unavailable	
da11	x2.xlarge.x86	unavailable	
da11	a3.large.x86	unavailable	
da11	n2.xlarge.x86	normal	
da11	x.large.arm	unavailable	
da11	c3.large.arm64	normal	
da11	m3.small.x86	normal	
da11	n3.xlarge.x86	normal	
da11	s3.xlarge.x86	limited	
da11	c2.medium.x86	normal	
da11	c3.small.x86	normal	
da11	g2.large.x86	normal	
se4	a3.large.x86	unavailable	

se4	c3.small.x86	unavailable	
se4	s3.xlarge.x86	unavailable	
se4	c2.medium.x86	unavailable	
se4	c3.medium.x86	normal	
se4	m3.large.x86	limited	
se4	m3.small.x86	normal	
se4	n2.xlarge.x86	normal	
se4	n3.xlarge.x86	normal	
sv16	s3.xlarge.x86	unavailable	
sv16	c3.medium.x86	normal	
sv16	m3.large.x86	normal	
sv16	n2.xlarge.x86	unavailable	
sv16	n3.xlarge.x86	unavailable	
sv16	a3.large.x86	unavailable	
sv16	c2.medium.x86	unavailable	
sv16	c3.small.x86	normal	
sv16	m3.small.x86	unavailable	
ld7	c3.medium.x86	normal	
ld7	c3.small.x86	limited	
ld7	m3.large.x86	limited	
ld7	n3.xlarge.x86	limited	
ld7	s3.xlarge.x86	unavailable	
ld7	a3.large.x86	unavailable	
ld7	c2.medium.x86	limited	
ld7	m3.small.x86	normal	
ld7	n2.xlarge.x86	unavailable	
sjc1	c3.small.x86	unavailable	
sjc1	m2.xlarge.x86	normal	
sjc1	n2.xlarge.x86	unavailable	
sjc1	s3.xlarge.x86	unavailable	
sjc1	c2.medium.x86	normal	
sjc1	c3.medium.x86	unavailable	
sjc1	m3.large.x86	unavailable	
sjc1	t3.small.x86	normal	
sjc1	x2.xlarge.x86	unavailable	
mt1	m3.small.x86	normal	
mt1	n2.xlarge.x86	unavailable	
mt1	n3.xlarge.x86	normal	
mt1	s3.xlarge.x86	unavailable	
mt1	c2.medium.x86	unavailable	
mt1	c3.small.x86	unavailable	
mt1	m3.large.x86	unavailable	
mt1	a3.large.x86	unavailable	
mt1	c3.medium.x86	unavailable	
ny6	c3.medium.x86	normal	
ny6	m3.large.x86	unavailable	
ny6	m3.small.x86	unavailable	
ny6	n2.xlarge.x86	unavailable	
ny6	n3.xlarge.x86	unavailable	
ny6	s3.xlarge.x86	unavailable	
ny6	a3.large.x86	unavailable	



ny6	c2.medium.x86	normal	
ny6	c3.small.x86	unavailable	
os3	a3.large.x86	unavailable	
os3	c3.medium.x86	unavailable	
os3	c3.small.x86	unavailable	
os3	s3.xlarge.x86	unavailable	
os3	c2.medium.x86	unavailable	
os3	m3.large.x86	unavailable	
os3	m3.small.x86	normal	
os3	n2.xlarge.x86	unavailable	
os3	n3.xlarge.x86	normal	
sk2	s3.xlarge.x86	unavailable	
sk2	a3.large.x86	unavailable	
sk2	c3.medium.x86	unavailable	
sk2	c3.small.x86	unavailable	
sk2	m3.large.x86	unavailable	
sk2	c2.medium.x86	unavailable	
sk2	m3.small.x86	normal	
sk2	n2.xlarge.x86	unavailable	
sk2	n3.xlarge.x86	normal	
he7	a3.large.x86	unavailable	
he7	c3.medium.x86	unavailable	
he7	m3.large.x86	unavailable	
he7	m3.small.x86	normal	
he7	c2.medium.x86	unavailable	
he7	c3.small.x86	unavailable	
he7	n2.xlarge.x86	unavailable	
he7	n3.xlarge.x86	normal	
he7	s3.xlarge.x86	unavailable	
me2	a3.large.x86	unavailable	
me2	c3.medium.x86	unavailable	
me2	m3.large.x86	unavailable	
me2	m3.small.x86	normal	
me2	s3.xlarge.x86	unavailable	
me2	c2.medium.x86	unavailable	
me2	c3.small.x86	unavailable	
me2	n2.xlarge.x86	unavailable	
me2	n3.xlarge.x86	limited	
tr2	n2.xlarge.x86	unavailable	
tr2	c2.medium.x86	normal	
tr2	c3.medium.x86	unavailable	
tr2	m3.large.x86	unavailable	
tr2	n3.xlarge.x86	normal	
tr2	s3.xlarge.x86	unavailable	
tr2	a3.large.x86	unavailable	
tr2	c3.small.x86	limited	
tr2	m3.small.x86	normal	

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```
APMBLY64MGWKGK:~ anusoni$ metal capacity get -m -P c3.small.x86
```

METRO	PLAN	LEVEL
ch	c3.small.x86	unavailable
dc	c3.small.x86	limited
ty	c3.small.x86	limited
md	c3.small.x86	limited
me	c3.small.x86	unavailable
hk	c3.small.x86	unavailable
sl	c3.small.x86	limited
ld	c3.small.x86	limited
tr	c3.small.x86	limited
sg	c3.small.x86	limited
sk	c3.small.x86	unavailable
pa	c3.small.x86	limited
os	c3.small.x86	unavailable
se	c3.small.x86	unavailable
am	c3.small.x86	limited
sp	c3.small.x86	unavailable
fr	c3.small.x86	limited
sv	c3.small.x86	normal
da	c3.small.x86	normal
sy	c3.small.x86	normal
la	c3.small.x86	unavailable
mt	c3.small.x86	unavailable
he	c3.small.x86	unavailable
ny	c3.small.x86	limited
at	c3.small.x86	unavailable

```
APMBLY64MGWKGK:~ anusoni$ metal capacity get --metros sv,ny,da -P  
c3.large.arm64,c3.medium.x86
```

METRO	PLAN	LEVEL
sv	c3.medium.x86	normal
da	c3.large.arm64	normal
da	c3.medium.x86	normal
ny	c3.medium.x86	normal