OR

10. a) Explain about data-parallel model.

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b) Explain about parallel programming environment and tools.

Roll No . C

CS-605

B.E. VI Semester

Examination, December 2014

Advance Computer Architecture

Time: Three Hours

Maximum Marks: 70

Note: i) Attempt one question from each unit. Each unit have equal marks.

ii) Assume data/value if required.

Unit - I

a) Describe at least four characteristics of MIMD.
 iltiprocessors that distinguish them from multiple mputer systems.

o, Distinguish between static and dynamic connection networks.

OR

a) Distinguish between omega and cross bar networks.

b) Write short note on multistage connection networks.

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Unit - II

- Explain the difference between superscalar and VLIW architectures in terms of hardware and software requirements.
 - Compare the instruction-set architecture in RISC and CISC processors in terms of instruction formats addressing modes, and cycles per instruction.

OR

- Explain about Arbitration, transaction and interrupt.
 - Explain the following terms associated with cache design.
 - Write through versus write back caches
 - ii) Factors affecting cache hit ratios

Unit - III

Consider the following pipeline reservation table:-14

	1	2	3	4
S_1	×			×
S_2		×		
S.			×	

- What are the forbidden latencies?
- Draw the state transition diagram.

- c) Determine the optimal constant latency cycle and the minimal average latency.
- d) Let the pipeline clock period be Z = 2 ns. Determine the throughput of this pipeline.

OR

Write Tomasulo's algorithm.

Describe about branch handling techniques.

Unit-IV

- Explain the following terms related to vector processing.
 - Vector and scalar balance point.
 - ii) Vectorization compiler.
 - Explain about directory based protocols.

OR

- Describe message routing schemes in multi computer network.
 - Discuss the design space for granularity and connectivity of SIMD systems.

Unit - V

- Describe about function and logic model.
- 7

Discuss features of parallelism.