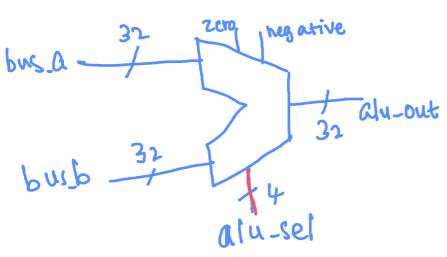
ALU



ALU instruction	Meaning	ALU control signal 0000	
add	add		
sub	subtract	0001	
sll	shift left logical	0010	
srl	shift right logical	0011	
sra	shift right arithmetic	0100	
and	bitwise and	0101	
or	bitwise or	0110	
xor	bitwise xor	0111	
slt	1 if a <b (signed)<="" td=""><td>1000</td>	1000	
sltu	1 if a <b (unsigned)<="" td=""><td>1001</td>	1001	

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Read reg1

Read reg2

Read reg2

reg file

Write reg

x0 is 0

Write data

each one is 32 bit

Reading 1 data

Write enable only it regurik is one