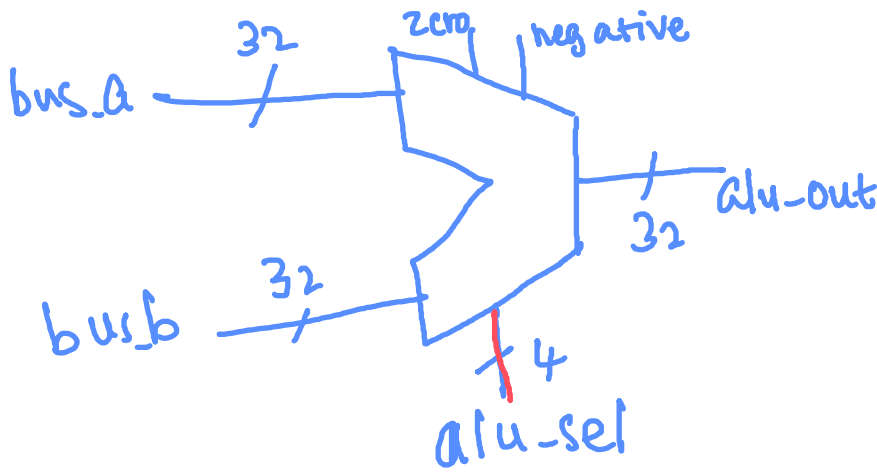
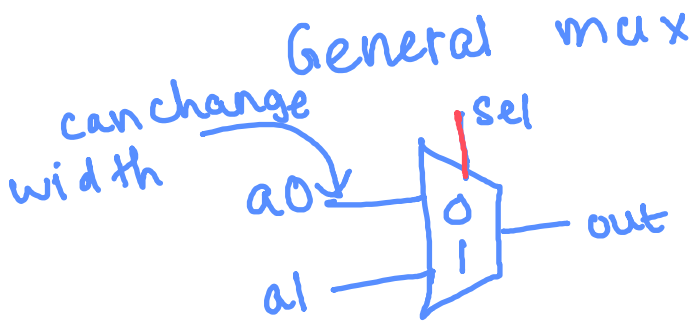


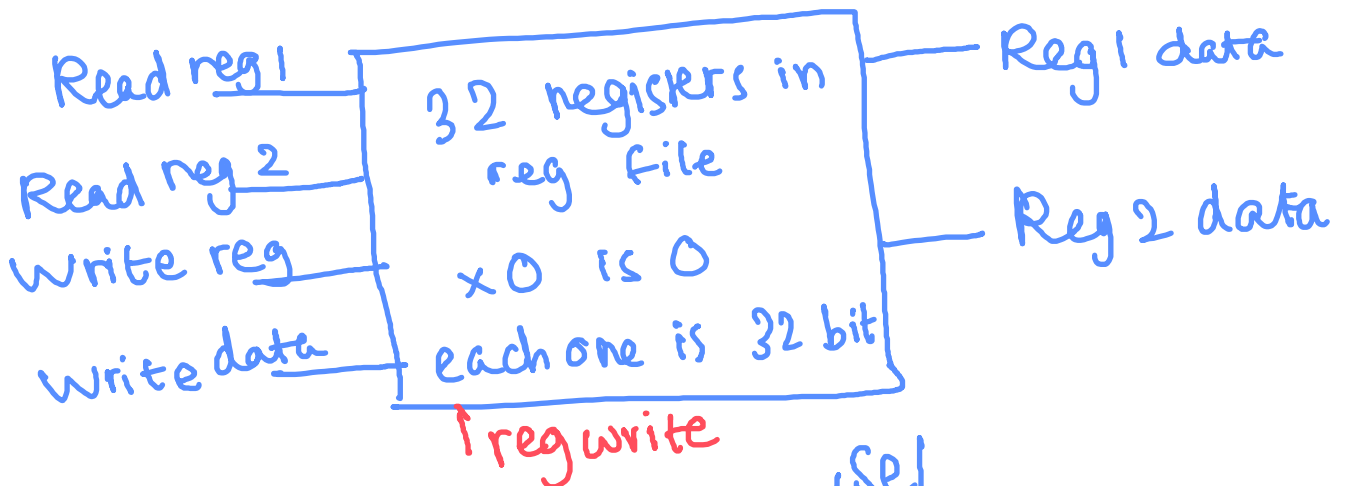
ALU



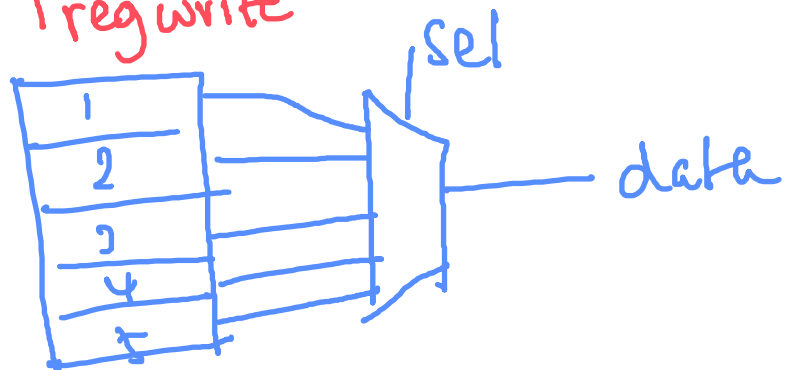
ALU instruction	Meaning	ALU control signal
add	add	0000
sub	subtract	0001
sll	shift left logical	0010
srl	shift right logical	0011
sra	shift right arithmetic	0100
and	bitwise and	0101
or	bitwise or	0110
xor	bitwise xor	0111
slt	1 if a < b (signed)	1000
sltu	1 if a < b (unsigned)	1001



Register file



Reading



Writing - Similar
write enable only if reg write is one