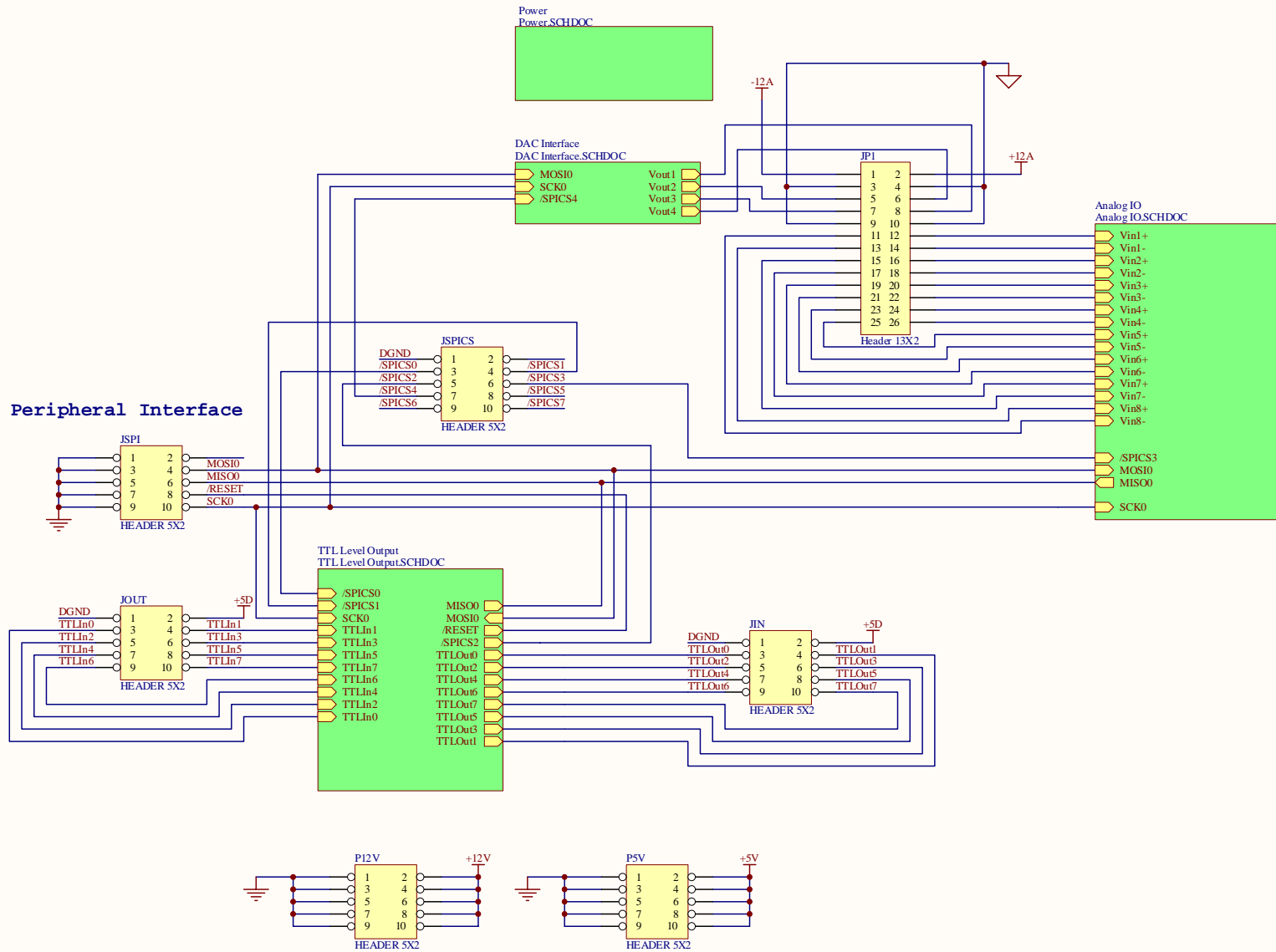
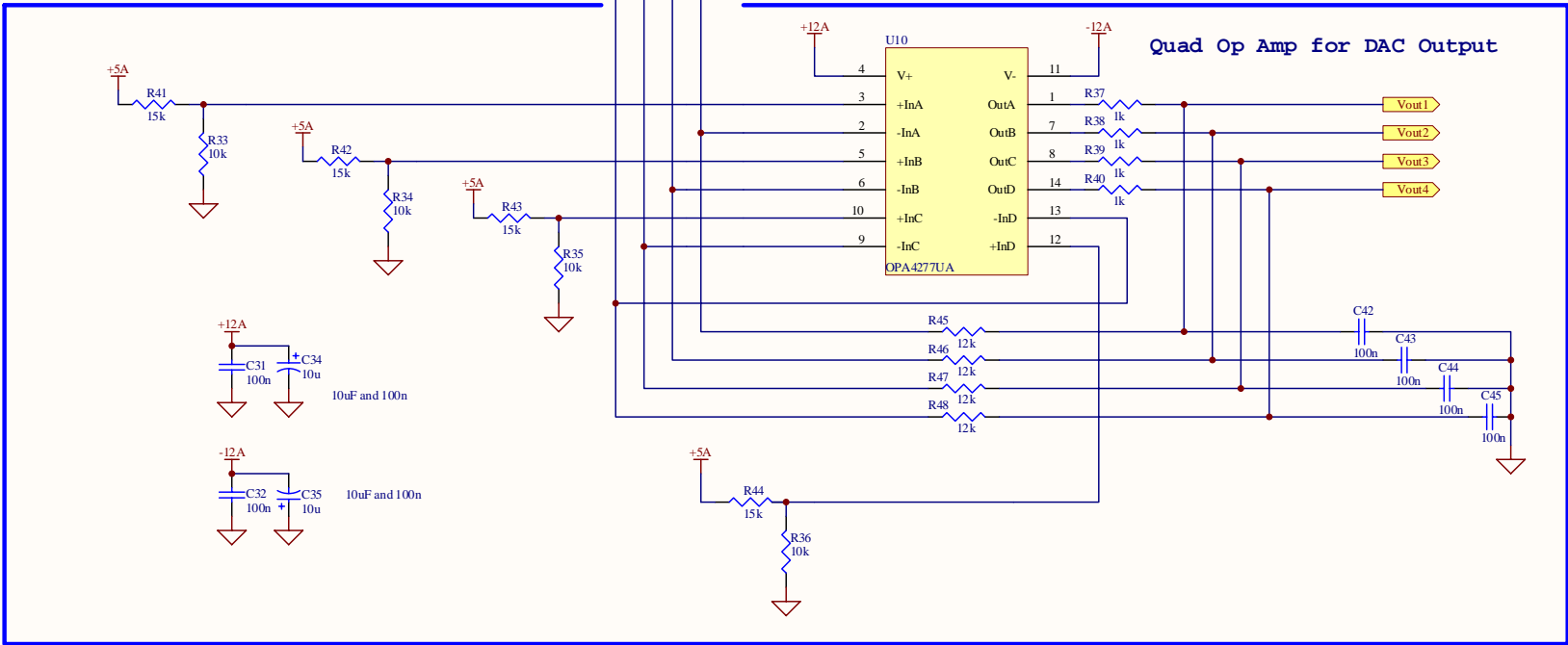
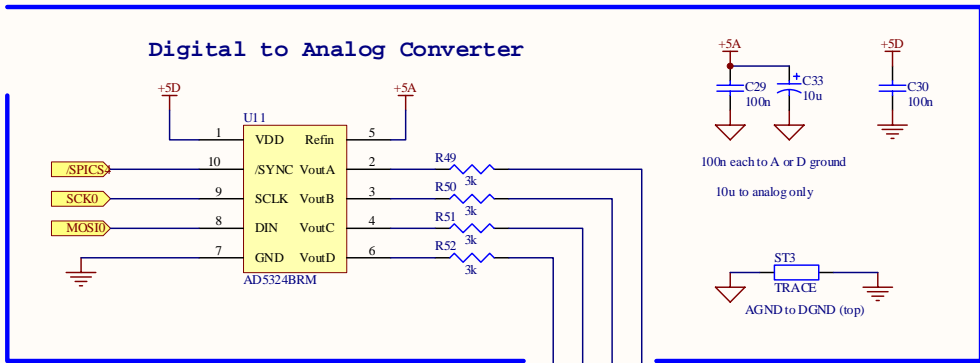


Serial Peripheral Interface



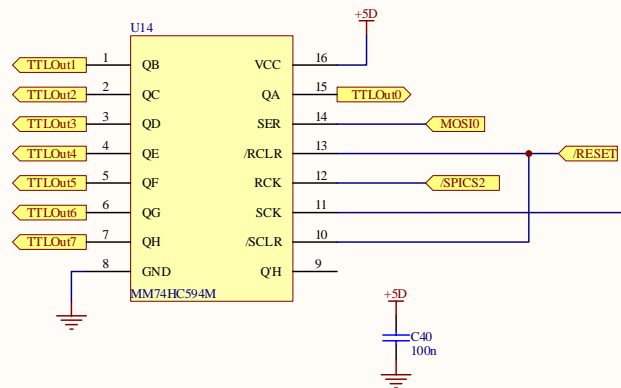
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File:	C:\Project\ADC DAC Board.SCHDOC	Drawn By:



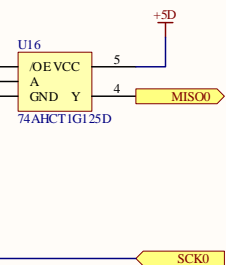
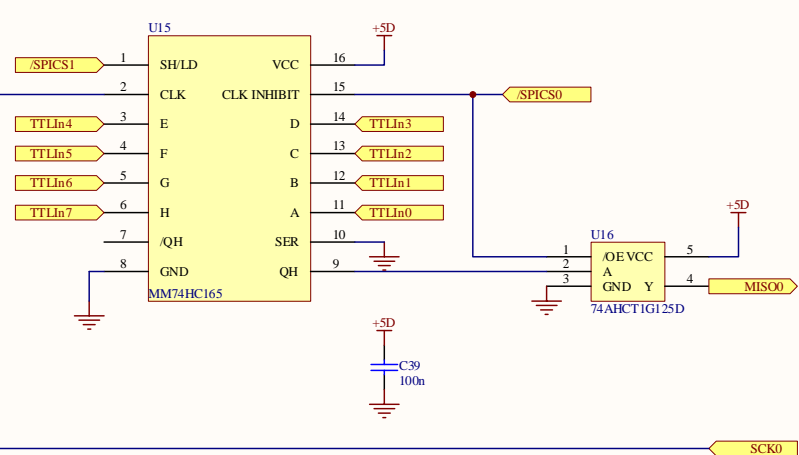


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### Serial In Parallel Out Shift Register

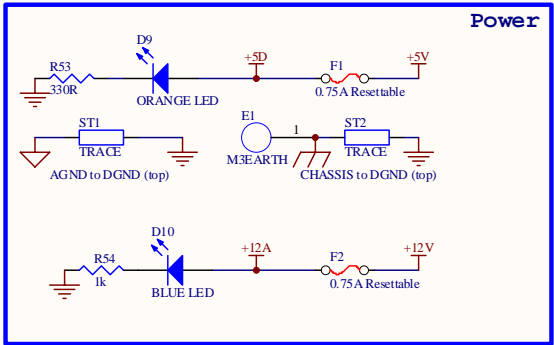
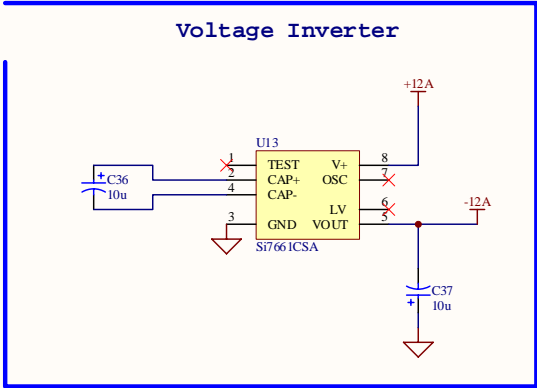
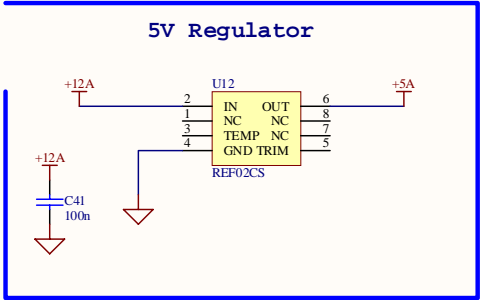


### Parallel In Serial Out Shift Register



SCK0

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Date:	7/05/2009	Sheet of
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