INDIAN INSTITUTE OF INFORMATION TECHNOLOGY TIRUCHIRAPPALLI

SETHURAPATTI, TRICHY-MADURAI HIGHWAY, TIRUCHIRAPPALLI, TAMIL NADU - 620012,

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIME TABLE FOR ACADEMIC YEAR 2025-26 – ODD SEMESTER

DEGREE: B. TECH   **BATCH: 2024-2028**   **SEMESTER: III**

CLASS COORDINATOR: Dr. RAVI BANOTH   **ROOM NO: 119**

Time/Day	09.20-10.10	10.10-11.00	11.00 - 11.20	11.20-12.10	12.10-13.00	13.00-14.00	14.00-14.50	14.50-15.40	15.40-16.00	16.00-16.50
Monday		A1	BREAK	E1	E2	LUNCH	F1		BREAK	
Tuesday	E3	D1		C1	A2					
Wednesday	C2	A3		B1	D2		G1	G2		G3
Thursday		F2		C3	B2		H1	H2		H3
Friday	B3	D3		F3						

Sl.No.	Course Code	Course Name	Faculty	Credit	Slot
1	24MA201	Mathematics-III	Dr Jagadeeshwar P	3	A
2	24EC201	Electronic Circuits	Dr. Ravi Banoth	3	B
3	24EC202	Digital Principles and System Design	Dr. M. Harish Adhithya	3	C
4	24EC203	Signals and Systems	Dr. S. Padmathilagam	3	D
5	24HM251	Economics for Engineers	Dr. Sumathi	3	E
6	24EC204	Data structures and Algorithms	Dr. J. Kokila	3	F
7	24EC205	Electronic Circuits Laboratory	Dr. Ravi Banoth	2	G
8	24EC206	Digital Principles & System Design Laboratory	Dr. M. Harish Adhithya	2	H