BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE	H: 71-B.E. (2012 PAT.)(ELECTRON : 04 JUL 2018 CENTRE: PU	NE INSTITUTE	OF CC	MPUTER	TECHNOL	OGY, PUNE.	PAGE NO.	1				
NOTE: F OTHER L	DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 1 NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER											
B120053	001 AAROHI VAIDYA		s	NEHAL		,715		53001		•		
404181 404182 404183 404184B 404185D 404186 404186 404187 404187 404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 PR 50 20 OR 50 20	17 26 26 22 21	37 41 56 54 47	54 P C 67 P C 82 P C 76 P C 68 P C 36 P C 36 P C 36 P C 33 P C	404189 404190 404191C 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 21 PP 100 40 23 PP 100 40 25 PP 100 40 27 TW 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	33 54 P 57 80 P 56 81 P 51 78 P 33 P 41 P 39 P 39 P 94 P 45 P			
	L = 1116/1500 , RESULT:FIRST C											
	002 AARYAN KUMAR		A				00679D ,PICT ,B1200					
404181 404182 404183 404184A 404185C 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 PR 50 20 OR 50 20 OR 50 20	AA 18 05 24 06	36 29 19 39 38	F 47 P F 63 P C 44 P 37 P C 22 P C 30 P C 35 P C 43 P C	404189 404191 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 15 PP 100 40 12 PP 100 40 18 PP 100 40 22 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	28 43 P 33 45 P 45 63 P 43 65 P 35 P 32 P 24 P 36 P 94 P 45 P			
GRAND TOTA	$I_{i} =/1500$ RESULT: FATIS											
B120053	003 ABHISHEK KUMAR SINGH		s	 HUKWAN'	 TI	,715	27083E , PICT , B1200	53003				
	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I								34 55 P 50 76 P 51 75 P 56 84 P AA F AA F AA F AA F 90 P 42 P			
GRAND TOTA	L =/1500 , RESULT: FAILS											

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PUNI	E INSTITUTE	OF COMPUTER	TECHNOLOG	OGY, PUNE. PAGE NO. 2									
NOTE: FIRST LINE : SEAT NO., NAME OF TOTHER LINES: HEAD OF PASSING, MAX. MAY	DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 2 NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER												
B120053004 AITISKHEM SYNGKON	• • • • • • • • • • • • • • • • • • • •	PROSSIL <i>A</i>		,71400700F ,PICT ,B120053004									
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	12 40 15 38 02 39 10 36 05 44	52 P 53 P 41 P 46 P 49 P 32 P 22 P 25 P 22 P 35 P C	SEM.:2 404189 MOBILE COMMUNICATION PP 100 40 12 29 41 P 404190 BROADBAND COMMUNICATION SYS. PP 100 40 15 35 50 P 404191C AUDIO VIDEO ENGINEERING PP 100 40 14 41 55 P 404192D WIRELESS NETWORKS PP 100 40 18 36 54 P 404193 LAB PRACTICE III(MC & BCS) TW 50 20 21 P 404193 LAB PRACTICE III(MC & BCS) OR 50 20 15# P 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 24 P 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 30 P 404195 PROJECT PHASE II TW 100 40 41 P 404195 PROJECT PHASE II PR 50 20 21 P									
GRAND TOTAL = 729/1500 , RESULT: PASS CLASS	S [# 0.4]		R	RESULT RESERVED FOR BACKLOGS.									
B120053005 AKSHAY SHRIKANT DESHMUKH	• • • • • • • • • • • • • • • • • • • •	SEEMA		,71527103C ,PICT ,B120053005									
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I GRAND TOTAL = 969/1500 , RESULT:FIRST CLAS	PP 100 40 TW 50 20 OR 50 20 TW 50 20 OR 50 20 OR 50 20	09 35 25 41 17 47 16 46 08 45	44 P C 66 P C 64 P C 62 P C 53 P C 31 P C 28 P C 40 P C 36 P C 30 P C	SEM.:2 404189 MOBILE COMMUNICATION PP 100 40 15 32 47 P 404190 BROADBAND COMMUNICATION SYS. PP 100 40 28 32 60 P 404191C AUDIO VIDEO ENGINEERING PP 100 40 16 49 65 P 404192D WIRELESS NETWORKS PP 100 40 24 49 73 P 404193 LAB PRACTICE III(MC & BCS) TW 50 20 36 P 404193 LAB PRACTICE III(MC & BCS) OR 50 20 22 P 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 36 P 404195 PROJECT PHASE II TW 100 40 95 P 404195 PROJECT PHASE II PR 50 20 45 P									
R120053006 AMALE ADVA PATENDRA		DOITPNITMZ		,71300769к ,PICT ,B120053006									
			45 P 45 P C F F 27 P C 32 P C 26 P 25 P 36 P C	SEM.:2 404189 MOBILE COMMUNICATION PP 100 40 19 AA F 404190 BROADBAND COMMUNICATION SYS. PP 100 40 17 AA F 404191D SOFT COMPUTING PP 100 40 15 AA F 404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 24 34 58 P 404193 LAB PRACTICE III(MC & BCS) TW 50 20 28 P 404193 LAB PRACTICE III(MC & BCS) OR 50 20 26 P 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 34 P 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 25 P 404195 PROJECT PHASE II TW 100 40 70 P 404195 PROJECT PHASE II PR 50 20 35 P RESULT RESERVED FOR BACKLOGS.									

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH DATE :	BRANCH: 71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 3														
NOTE: FI	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER														
B1200530	007 ANDURKAR SHUBHAM VYANKAT	ESH		V	AISHNA	AVI		,7152	7110F ,PICT ,B1200	53007					
	SEM.:1								SEM.:2						
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100	40	14	29	43	P C	404189	MOBILE COMMUNICATION	PP 100	40	07	28	35#	P
404182 404183	MICPOWAVE ENGINEERING	PP 100	40 40	∠3 20	44 38	6 / 5 8	P C	404190 404191	AUDIO VIDEO ENGINEERING	PP 100	40 40	⊿3 21	28 46	51 67	P D
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	15	39	54	PC	404192D	WIRELESS NETWORKS	PP 100	40	24	35	59	P
404185C	PLCS AND AUTOMATION	PP 100	40	12	43	55	РC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			36	РC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			30	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			24	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	J.M 20	20			38	PC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20 40			39	P
404187	PROJECT PHASE I	OR 50	20			44	PC	404195	PROJECT PHASE II	PR 50	20			45	P
101200	11100201 1111.52 1	011 00					- 0	101170	1100001 111100 11	211 00					-
GRAND TOTAL	L = 941/1500 , RESULT:FIRST CL	ASS [# (0.4]												
D1200530	008 ANIKET ASHOK BHALERAO	• • • • • • • • •			יייי דעודן גידי דעודן	• • • •		7153	מוליים מול	53008	• • • •			• • • • •	• • • •
	SEM.:1								SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	15	36	51	РC	404189	MOBILE COMMUNICATION	PP 100	40	22	41	63	P
404182	COMPUTER NETWORKS	PP 100	40	27	47	74	P C	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	25	36	61	P
404183 404194D	MICROWAVE ENGINEERING	PP 100	40	23	50 40	73	PC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	25	49	/4 72	D D
404184B	ARTIFICIAL INTELLIGENCE	PP 100	40	⊿3 14	40	7 Z 5 4	PC	4041920	IAB PRACTICE III (MC & BCS)	TW 50	20	24	43	7.5 4.5	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			39	РC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			40	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			41	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			42	P C	404195	PROJECT PHASE II	TW 100	40			95	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			45	P C	404195	PROJECT PHASE II	PR 50	20			44	Р
GRAND TOTAL	$r_{i} = 1106/1500$ RESULT: FIRST C	ASS WITH	DTS'	TINCT	TON										
	009 ANUSHKA NARAYAN							· · · · · · <u>· · · · · · · · · · · · · </u>							
B1200530	JU9 ANUSHKA NARAYAN			P	USHPA			,7152	7118M , PICT , B1200	53009					
	SEM.:1								SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	13	32	45	РC	404189	MOBILE COMMUNICATION				42	60	P
404182	COMPUTER NETWORKS	PP 100	40	27	49	76	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	21		67	
404183	MICROWAVE ENGINEERING	PP 100	40	17	50	67	P C	404191C	AUDIO VIDEO ENGINEERING WIRELESS NETWORKS	PP 100	40	20	51	71	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	20	50	70	PC	404192D	WIRELESS NETWORKS	PP 100	40	29	46	75	P
404185D 404186	ARILFICIAL INIELLIGENCE	PP 100	20	24	31	22	P C	404193	TAB PRACTICE III (MC & BCS)	1W 50	20			29 15	P D
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			44	PC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			33	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			35	P C	404195	PROJECT PHASE II	TW 100	40			93	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			38	P C	404195	WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50	20			44	P
GRAND TOTAL	L = 1058/1500 , RESULT:FIRST C	LASS WITH	DIS'	TINCT	ION										

GRAND TOTAL = 1058/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH DATE :	1:71-B.E. (2012 PAT.)(ELECTRON: 04 JUL 2018 CENTRE: PU	ICS &TELE NE INSTIT	COMU UTE	NICA: OF C	TION) OMPUTEF	R TEC	HNOLOG	Y, PUNE.	PAGE NO. 4					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	IDAT	E, I	MOTHER,	PER	MANENT	REG. NO.	, PREVIOUS SEAT NO., COLLEGE, SEAT NO.					
OTHER LI	NES: HEAD OF PASSING, MAX. M	ARKS, MIN	.PAS	SS MAI	RKS, IN	JT. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OV	/ER				
									7121M ,PICT ,B120053010	• •				
	SEM.:1								SEM.:2					
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	12	28	40	P C	404189	MOBILE COMMUNICATION PP 100 40 22 37 59 P					
404182 404183	MICROWAVE ENGINEERING	PP 100	40	24 16	39 38	63 54	P C	404190 404191C	BROADBAND COMMUNICATION SYS. PP 100 40 18 47 65 P					
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	22	52	74	РC	404192D	WIRELESS NETWORKS PP 100 40 22 50 72 P					
404185C	PLCS AND AUTOMATION	PP 100	40	18	29	47	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20 46 P					
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			44	P C	404193	LAB PRACTICE III(MC & BCS) OR 50 20 43 P					
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			34 11	PC	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 41 P					
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			28	PC	404194	PROJECT PHASE IT TW 100 40 70 P					
404188	PROJECT PHASE I	OR 50	20			31	P C	404195	MOBILE COMMUNICATION PP 100 40 22 37 59 P BROADBAND COMMUNICATION SYS. PP 100 40 18 47 65 P AUDIO VIDEO ENGINEERING PP 100 40 16 41 57 P WIRELESS NETWORKS PP 100 40 22 50 72 P LAB PRACTICE III(MC & BCS) TW 50 20 46 P LAB PRACTICE III(MC & BCS) OR 50 20 43 P LAB PRAC. IV(ELECTIVE III) TW 50 20 41 P LAB PRAC. IV(ELECTIVE III) PR 50 20 38 P PROJECT PHASE II TW 100 40 70 P PROJECT PHASE II PR 50 20 30 P					
GRAND TOTAL	_ = 9///1500 , RESULT:FIRST CL	ASS												
D1000F30	0.1. ADVA & GUATT GUATT KOLOM			,				7150	71.04E DIGE D1.000E2.011	• •				
B1200530	JII ARYA K SHAJI SHAJI KOLOII	1		J	KUMARI			,/152	/124F ,PIC1 ,B120053011					
SEM.:1 SEM.:2 S														
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	15	32	47	P C	404189	MOBILE COMMUNICATION PP 100 40 15 29 44 P					
404182	COMPUTER NETWORKS	PP 100	40	26	52	.78	PC	404190	BROADBAND COMMUNICATION SYS. PP 100 40 23 47 70 P					
404183 404184A	DIGITAL IMAGE PROCESSING	PP 100 PP 100	40	24	42	65	PC	404191C 404192D	WIRELESS NETWORKS PP 100 40 22 48 70 P					
404185C	PLCS AND AUTOMATION	PP 100	40	20	34	54	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20 40 P					
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			32	РC	404193	LAB PRACTICE III(MC & BCS) OR 50 20 46 P					
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			40	P C	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 42 P					
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			39	PC	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20 38 P					
404187	PROJECT PHASE I	OR 50	20			44	PC	404195	PROJECT PHASE II PR 50 20 45 P					
	_ = 1053/1500 , RESULT:FIRST C													
B1200530	012 AVHAD TEJAL VIPIN			1	ANAYANA			,7152	7129G ,PICT ,B120053012					
	SEM.:1								SEM.:2					
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	07	36	43	P C	404189	MOBILE COMMUNICATION PP 100 40 11 31 42 P					
404182 404183	COMPUTER NETWORKS MICROWAVE FNGINFERING	PP 100	40	∠4 13	4 Z 4 A	66 57	P C	404190 404191n	BROADBAND COMMUNICATION SYS. PP 100 40 16 42 58 P					
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	22	36	58	PC	404191D	WIRELESS NETWORKS PP 100 40 18 35 53 P					
404185D	ARTIFICIAL INTELLIGENCE	PP 100	40	12	43	55	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20 29 P					
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			35	РC	404193	LAB PRACTICE III(MC & BCS) OR 50 20 32 P					
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			38	PC	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 28 P					
404187 404187	LAB PRAC. II(VLSI &ELEC. I)	IW 50	⊿∪ 2.0			33	P C	404194	PROJECT PHASE IT TW 100 40 90 D					
404188	PROJECT PHASE I	OR 50	20			40	PC	404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 11 31 42 P BROADBAND COMMUNICATION SYS. PP 100 40 16 42 58 P SOFT COMPUTING PP 100 40 04 36\$ 40 P WIRELESS NETWORKS PP 100 40 18 35 53 P LAB PRACTICE III(MC & BCS) TW 50 20 29 P LAB PRACTICE III(MC & BCS) OR 50 20 32 P LAB PRAC. IV(ELECTIVE III) TW 50 20 34 P PROJECT PHASE II TW 100 40 90 P PROJECT PHASE II PR 50 20 45 P					
	= 908/1500 , RESULT:FIRST CL													

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 5														
NOTE: FIRST LINE : SEAT NO., NAME OF	F THE CANDIDATE,	MOTHER, PERMA	ANENT REG. NO.,	, PREVIOUS SEAT NO., COLLEGE	E, SEAT NO.									
OTHER LINES: HEAD OF PASSING, MAX. I														
B120053013 AWATE NETRA NANDKUMAR		MEENAKSHI	,71527	7131J ,PICT ,B12005	33013									
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY	PP 100 40 0)6 28 34# F	P 404189	SEM.:2 MOBILE COMMUNICATION	PP 100 40 19 35 54 P									
404182 COMPUTER NETWORKS	PP 100 40 1	5 28 43 E	P C 404190	BROADBAND COMMUNICATION SYS.	PP 100 40 23 41 64 P									
404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS	PP 100 40 1	.0 41 51 E 3 31 44 E	P 404191C P C 404192D	AUDIO VIDEO ENGINEERING WIRELESS NETWORKS	DD 100 40 1E 41 EC D									
404185D ARTIFICIAL INTELLIGENCE	PP 100 40 1	.4 38 52 E	P C 404193	LAB PRACTICE III(MC & BCS)	TW 50 20 35 P									
404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE)	TW 50 20	32 E	P C 404193	LAB PRACTICE III(MC & BCS)	OR 50 20 32 P									
404187 LAB PRAC. II(VLSI & ELEC. I)	TW 50 20	35 E	P C 404194	LAB PRAC. IV(ELECTIVE III)	PR 50 20 40 P									
404187 LAB PRAC. II(VLSI &ELEC. I) 404188 PROJECT PHASE I	PR 50 20	30 E	P C 404195	PROJECT PHASE II	TW 100 40 92 P									
404188 PROJECT PHASE I	OR 50 20	44 E	P C 404195	PROJECT PHASE II	TW 50 20 35 P OR 50 20 32 P TW 50 20 40 P PR 50 20 40 P TW 100 40 92 P PR 50 20 45 P									
GRAND TOTAL = 911/1500 . RESULT:FIRST C.	LASS I# U.41													
B120053014 AYUSH BHADAURIA SUMAN ,71527132G ,PICT ,B120053014														
SEM.:1														
404181 VLSI DESIGN & TECHNOLOGY	PP 100 40 1	.6 34 50 E	P C 404189	MOBILE COMMUNICATION										
404182 COMPUTER NETWORKS	PP 100 40 2	26 51 77 E	P C 404190	BROADBAND COMMUNICATION SYS.	PP 100 40 19 38 57 P PP 100 40 20 48 68 P									
404184A DIGITAL IMAGE PROCESSING	PP 100 40 2	.8 43 61 E	P C 404191C	AUDIO VIDEO ENGINEERING WIRELESS NETWORKS	PP 100 40 20 48 68 P PP 100 40 27 45 72 P									
404185C PLCS AND AUTOMATION	PP 100 40 1	.2 40 52 E	P C 404193	LAB PRACTICE III(MC & BCS)	TW 50 20 27 P									
404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE)	OR 50 20	43 E 33 E	P C 404193 P C 404194	LAB PRACTICE III (MC & BCS) LAB PRAC. TV(ELECTIVE III)	OR 50 20 42 P TW 50 20 27 P									
404187 LAB PRAC. II(VLSI &ELEC. I)	TW 50 20	33 E	P C 404194	LAB PRAC. IV(ELECTIVE III)	PR 50 20 36 P									
404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PR 50 20	34 E	P C 404195	PROJECT PHASE II	TW 50 20 27 P OR 50 20 42 P TW 50 20 27 P PR 50 20 27 P PR 50 20 36 P TW 100 40 70 P PR 50 20 30 P									
101100 PROUECT FRASE I	OK 30 20	30 E	F C 404193	FROUECT FIRSE II	FR 30 20 50 F									
GRAND TOTAL = 959/1500 , RESULT:FIRST C														
B120053015 BAGDARE ABHISHEK PRASHA	NT	SHILPA	,71527	7134C ,PICT ,B12005	33015									
SEM.:1				SEM.:2										
404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185D ARTIFICIAL INTELLIGENCE	PP 100 40 1	.4 29 43 E 26 41 67 E	P C 404189 P C 404190	MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS.										
404183 MICROWAVE ENGINEERING	PP 100 40 0	9 35 44 E	P C 404191C	AUDIO VIDEO ENGINEERING	PP 100 40 20 42 62 P									
404184B EMBEDDED SYSTEMS & RTOS 404185D ARTIFICIAL INTELLIGENCE	PP 100 40 1	.9 39 58 E	P C 404192D	WIRELESS NETWORKS	PP 100 40 25 39 64 P TW 50 20 38 P									
404186 LAB PRACTICE I (CN & MWE)	TW 50 20	./ 34 51 E	P C 404193	LAB PRACTICE III(MC & BCS)	OR 50 20 45 P									
404186 LAB PRACTICE I (CN & MWE)	OR 50 20	32 E	P C 404194	LAB PRAC. IV(ELECTIVE III)	TW 50 20 39 P									
404187 LAB PRAC. II(VLSI &ELEC. I) 404187 LAB PRAC. II(VLSI &ELEC. I)	TW 50 20 PR 50 20	35 E 38 E	P C 404194 P C 404195	LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II	PR 50 20 40 P TW 100 40 93 P									
404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	OR 50 20	44 1	P C 404195	LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50 20 45 P									
GRAND TOTAL = 978/1500 , RESULT:FIRST C														

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE	BRANCH: 71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 6												
NOTE: FI	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER												
B1200530	016 BAGUL OMKAR SANJAY				 /AISHAI	JI		,7152	7135M ,PICT ,B120053016	• • • • • • • • • • • • • • • • • • • •			
404186 404187 404187 404188	LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50 TW 50 PR 50 OR 50	20 20 20 20 20		29 40 36 44 40	42 58 47 68 58 26 30 28 38 40		404189 4041910 4041910 4041920 404193 404193 404194 404195 404195	BROADBAND COMMUNICATION SYS. PP 100 40 21 42 AUDIO VIDEO ENGINEERING PP 100 40 13 45	48 P 63 P 58 P 67 P 35 P 32 P 37 P 35 P 60 P 22 P			
GRAND TOTAL	_ = 892 + U8/1500 , RESULT:FIR:	ST CLASS	ĮΟ).2]									
B120053017 BAHETI BALAJI NAVALKISHOR HEMA ,71527137H ,PICT ,B120053017 SEM.:1													
404181 404182 404183 404184A 404185B 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20 20	12 24 28 26 20	54 42 39 48 45	66 67 74 65 42 32 41 32 43	P C C C C C C C C C C C C C C C C C C C	404189 404191C 404192A 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 23 36 BROADBAND COMMUNICATION SYS. PP 100 40 23 47 AUDIO VIDEO ENGINEERING PP 100 40 19 52 BIOMEDICAL SIGNAL PROCESSING PP 100 40 19 45 LAB PRACTICE III(MC & BCS) TW 50 20 LAB PRACTICE III(MC & BCS) OR 50 20 LAB PRAC. IV(ELECTIVE III) TW 50 20 LAB PRAC. IV(ELECTIVE III) PR 50 20 PROJECT PHASE II TW 100 40 PROJECT PHASE II PR 50 20	71 P			
GRAND TOTAL	L = 1038/1500 , RESULT:FIRST CI	LASS WITH	DIS	TINCT	ΓΙΟΝ ·····								
404181 404182 404183 404184A 404185C 404186 404186 404187 404187 404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20 20	16 23 22 26 21	56 48 48 50 43	72 71 70 76 64 43 36 37 32 44	P C C C P C C C C P C C C C P C C C C P C C C C P C C C C P C C C C P C C C C P C C C C C P C C C C C P C	404189 404191C 404192A 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION	55 P 64 P 72 P 72 P 41 P 44 P 30 P 44 P 85 P 42 P			
GRAND TOTAL	L = 1094 + 10/1500 , RESULT:FIR	RST CLASS	WIT	H DIS	STINCTI	ION	[& 0	0.163]					

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH DATE :	::71-B.E. (2012 PAT.)(ELECTRON 04 JUL 2018 CENTRE: PU	ICS &TELE NE INSTIT	COMU UTE	NICA OF C	TION) OMPUTER	TEC	HNOLOG	Y, PUNE.	PAGE NO. 7						
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	 TACI	 E, 1	 MOTHER,	PER	 MANENT	REG. NO.	, PREVIOUS SEAT NO., COLLEGE, SEAT NO.	• •					
OTHER LI	NES: HEAD OF PASSING, MAX. M.	ARKS, MIN	.PAS	SS MA	RKS, IN	IT. M	ARKS,	TH. MARKS	, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVE	ΞR					
B1200530	19 BAND VIPIN DILIP				CHANDA			,7152	7141F ,PICT ,B120053019						
	SEM.:1								SEM.:2 MOBILE COMMUNICATION						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	09	38	47	P C	404189	MOBILE COMMUNICATION PP 100 40 12 31 43 P						
404183	MICROWAVE ENGINEERING	PP 100 PP 100	40	16	36	52	PC	404190 404191C	AUDIO VIDEO ENGINEERING PP 100 40 09 33 42 P						
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	15	34	49	РC	404192D	WIRELESS NETWORKS PP 100 40 22 41 63 P						
404185C	PLCS AND AUTOMATION	PP 100	40	10	39	49	P	404193	LAB PRACTICE III(MC & BCS) TW 50 20 34 P						
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			24	P C	404193	LAB PRACTICE III(MC & BCS) OR 50 20 42 P						
404187	LAB PRACTICE I (CN & MWE)	TW 50	20			21	PC	404194	IAB PRAC. IV(ELECTIVE III) IW 50 20 27 P						
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			25	P	404195	PROJECT PHASE II TW 100 40 80 P						
404188	PROJECT PHASE I	OR 50	20			34	РC	404195	PROJECT PHASE II PR 50 20 35 P						
D1200530	20 DANDAI NEUAI DOUTDAG			• • • •	CIIDEKUN		• • • • • •	7152	7142D ,PICT ,B120053020	• •					
B1200550	20 BANDAL NEHAL KOHIDAS				BUKEKIIA	1		, / 1 3 2							
	SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 16 41 57 P C 404189 MOBILE COMMUNICATION PP 100 40 18 44 62 P 404182 COMPUTER NETWORKS PP 100 40 25 34 59 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 22 49 71 P 404183 MICROWAVE ENGINEERING PP 100 40 17 49 66 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 19 51 70 P 404184A DIGITAL IMAGE PROCESSING PP 100 40 15 51 66 P C 404192D WIRELESS NETWORKS PP 100 40 28 44 72 P 404185 PLCS AND AUTOMATION PP 100 40 13 33 46 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 44 P 404186 LAB PRACTICE I (CN & MWE) OR 50 20 42 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 40 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 32 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 42 P 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 42 P 404188 PROJECT PHASE II TW 100 40 94 P 404188 PROJECT PHASE II OR 50 20 42 P C 404195 PROJECT PHASE II PR 50 20 45 P														
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	16	41	57	P C	404189	MOBILE COMMUNICATION PP 100 40 18 44 62 P						
404182	MICROWAVE ENGINEERING	PP 100 PP 100	40	⊿5 17	34 49	59 66	PC	404190 404191C	AUDIO VIDEO ENGINEERING PP 100 40 22 49 71 P						
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	15	51	66	PC	404192D	WIRELESS NETWORKS PP 100 40 28 44 72 P						
404185C	PLCS AND AUTOMATION	PP 100	40	13	33	46	P C	404193	LAB PRACTICE III(MC & BCS) TW 50 20 44 P						
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			43	P C	404193	LAB PRACTICE III(MC & BCS) OR 50 20 42 P						
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			42	P C	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 40 P						
404187	LAB PRAC. II(VLSI & ELEC. I)	PR 50	20			34	PC	404194	PROJECT PHASE II TW 100 40 94 P						
404188	PROJECT PHASE I	OR 50	20			42	РC	404195	PROJECT PHASE II PR 50 20 45 P						
GRAND TOTAL	= 1069/1500 , RESULT:FIRST C	LASS WITH	DIS	TINC'	TION										
B1200530	21 BANDE NAYAN SHRIRAM				 SINDHU				0143E ,PICT ,B120053021	• •					
								,							
404101	SEM.:1	DD 100	40	11	4.0	EO	D C	404100	SEM.:2 MOBILE COMMUNICATION PP 100 40 08 21 F						
404181	COMPITTER NETWORKS	PP 100 PP 100	40	16	42 35	53 51	PC	404189	MOBILE COMMUNICATION PP 100 40 08 21 F BROADBAND COMMUNICATION SYS. PP 100 40 15 33 48 P AUDIO VIDEO ENGINEERING PP 100 40 13 31 44 P WIRELESS NETWORKS PP 100 40 18 42 60 P LAB PRACTICE III(MC & BCS) TW 50 20 36 P						
404183	MICROWAVE ENGINEERING	PP 100	40	07	33	40	РC	404191C	AUDIO VIDEO ENGINEERING PP 100 40 13 31 44 P						
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	17	34	51	РC	404192D	WIRELESS NETWORKS PP 100 40 18 42 60 P						
404185C	PLCS AND AUTOMATION	PP 100	40	16	28	44	P P G	404193	LAB PRACTICE III(MC & BCS) TW 50 20 36 P						
404186 404186	LAB PRACTICE I (CN & MWE)	UR 50	∠U 20			39 25	ъс ъс	404193 404194	LAB PRACTICE III (MC & BCS) OR 50 Z0 40 P IAR DRAC IV/FIECTIVE III) TW 50 20 30 D						
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			35	PC	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20 39 P						
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			25	РC	404195	PROJECT PHASE II TW 100 40 42 P						
404188	PROJECT PHASE I	OR 50	20			38	РC	404195	AUDIO VIDEO ENGINEERING PP 100 40 13 31 44 P WIRELESS NETWORKS PP 100 40 18 42 60 P LAB PRACTICE III (MC & BCS) TW 50 20 40 P LAB PRACTICE III (MC & BCS) OR 50 20 40 P LAB PRAC. IV(ELECTIVE III) TW 50 20 30 P LAB PRAC. IV(ELECTIVE III) PR 50 20 39 P PROJECT PHASE II TW 100 40 42 P PROJECT PHASE II PR 50 20 22 P						
GRAND TOTAL	=/1500 , RESULT:FAILS														

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH: 71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 8													
NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, MAX. M	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER												
B120053022 BARAPATRE VRUNAL NARENDR	 A	VAISHA	 LI	,7152		53022							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 PP 50 20 TW 50 20 TW 50 20 PR 50 20 OR 50 20 OR 50 20	21 59 28 50 25 43 22 57 18 35	80 P C 78 P C 68 P C 79 P C 53 P C 39 P C 42 P C 40 P C 32 P C 30 P C	404189 404190 404191C 4041920 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 18 PP 100 40 21 PP 100 40 25 PP 100 40 27 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	43 61 P 49 70 P 58 83 P 53 80 P 40 P 42 P 32 P 36 P 95 P 45 P						
GRAND TOTAL = 1125/1500 , RESULT:FIRST C	LASS WITH DIS	TINCTION											
B120053023 BHADGAONKAR SARANG SUBHASH SHAMALA ,71527149M ,PICT ,B120053023													
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20 OR 50 20	11 49 26 45 18 54 22 47 16 45	60 P C 71 P C 72 P C 69 P C 61 P C 37 P C 41 P C 39 P C 42 P C 43 P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 20 PP 100 40 23 PP 100 40 20 PP 100 40 23 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20 PR 50 20	37 57 P 46 69 P 53 73 P 51 74 P 43 P 46 P 40 P 39 P 92 P 43 P						
GRAND TOTAL = 1111/1500 , RESULT:FIRST C	LASS WITH DIS	TINCTION											
B120053024 BHIMANI JINKAL MAHENDRAK	UMAR	REKHA		,7163	3605H ,PICT ,B1200	53024							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	OR 50 20 TW 50 20 PR 50 20 OR 50 20		69 P C 77 P C 70 P C 68 P C 64 P C 40 P C 30 P C 39 P C 38 P C 42 P C	404189 404190 404191C 404192D 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	DD 100 40 04	40 58 P 47 71 P 53 73 P 54 81 P 41 P 44 P 34 P 40 P 85 P 40 P						
GRAND TOTAL = 1104/1500 , RESULT:FIRST C	LASS WITH DIS	TINCTION											

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCI DATE	: 71-B.E. (2012 PAT.)(ELECTRON : 04 JUL 2018	ICS &TELEC NE INSTITU	JTE (OF CON	LON) MPUTER	TEC	HNOLOG	Y, PUNE.	PAGE NO. 9	
NOTE: FI	IRST LINE : SEAT NO., NAME OF INES: HEAD OF PASSING, MAX. M	THE CANDI ARKS, MIN.	[DAT] . PAS:	E, MO S MARI	OTHER, KS, IN	PER T. M	MANENT IARKS,	REG. NO. TH. MARKS	., PREVIOUS SEAT NO., COLLEGE, SEAT NO. S, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVE	· :R
B120053	D25 BHIRUD AKSHAY PRADIP			AI	NITA			,7152	27155F ,PICT ,B120053025	•
404181 404182 404183 404184A 404185C 404186 404187 404187 404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I L = 987/1500 , RESULT:FIRST CL	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20	12 18 16 16 11	43 35 33 43 35	55 53 49 59 46 39 45 25 40	P C C C C C C P C C C C P C C C P P C C C C P P C C C C P P C C C C P P C C C P P C C C P P C C C P P C C C P P C C C C P P C C C C C P P C C C C C P P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION	
D120052	- 907/ISOU , RESULLIFIRSI CL.							71.50		
404181 404182 404183 404184A 404185C 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20 20	12 25 29 22 20	48 40 48 50 40	60 65 77 72 60 31 32 35 29	P C C P C C P C C P C C P P P C C P P P C C P P P C C P P P C C P P P C C P	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 15 39 54 P BROADBAND COMMUNICATION SYS. PP 100 40 29 49 78 P AUDIO VIDEO ENGINEERING PP 100 40 14 48 62 P WIRELESS NETWORKS PP 100 40 27 45 72 P LAB PRACTICE III(MC & BCS) TW 50 20 35 P LAB PRACTICE III(MC & BCS) OR 50 20 40 P LAB PRAC. IV(ELECTIVE III) TW 50 20 33 P LAB PRAC. IV(ELECTIVE III) PR 50 20 33 P PROJECT PHASE II TW 100 40 70 P PROJECT PHASE II PR 50 20 30 P	
GRAND TOTAL	L = 1002/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	ION					
404181 404182 404183 404184A 404185C 404186 404186 404187 404187 404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20 20	10 27 26 21 23	51 43 47 45 51	61 70 73 66 74 43 38 42 36 40	P C P C C P C C P C C P C C	404189 404190 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 20 28 48 P BROADBAND COMMUNICATION SYS. PP 100 40 18 53 71 P AUDIO VIDEO ENGINEERING PP 100 40 25 50 75 P WIRELESS NETWORKS PP 100 40 29 50 79 P LAB PRACTICE III(MC & BCS) TW 50 20 43 P LAB PRACTICE III(MC & BCS) OR 50 20 39 P LAB PRAC. IV(ELECTIVE III) TW 50 20 38 P LAB PRAC. IV(ELECTIVE III) PR 50 20 38 P PROJECT PHASE II TW 100 40 90 P PROJECT PHASE II PR 50 20 45 P	
GRAND TOTAL	L = 1114/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT:	ION					

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCI DATE :	BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 10														
NOTE: FI	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER														
B1200530	 128 BHUJBAL AJINKYA RAVINDRA			VIJAYA		• • • • •	,7152		,B1200!	 53028	• • • •			• • • •	
	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I										40 40 40 20 20 20 20	20 14 16 15	17 33 30 29	 47 46 44 42 41 36 36	F P P P P P P
404188	PROJECT PHASE I	OR 50 Z	U		45 E	P C	404195	PROJECT PHASE II		PK 50	20			42	Р
	L =/1500 , RESULT:FAILS														
B1200530)29 BIDKAR SHUBHAM MANOJ			MANASI			,7152	7162J ,PICT	,B1200	 53029	• • • •			• • • •	
404181 404182 404183 404184B 404185D 404186 404187 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 4 TW 50 2 OR 50 2 TW 50 2 OR 50 2 OR 50 2	0 11 0 20 0 22 0 20 0 17 0 0	51 45 53 47 46	62 F 65 F 75 F 67 F 63 F 40 F 45 F 40 F 36 F 44 F		404189 404190 404191D 404192A 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SOFT COMPUTING BIOMEDICAL SIGNAL FOR LAB PRACTICE III(MOTE LAB PRACTICE III(MOTE LAB PRACTICE III(MOTE LAB PRACTICE PROJECT PHASE II PROJECT PHASE II	PROCESSING C & BCS) C & BCS) UVE III)	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20 20	17 26 21 21	33 45 41 41	50 71 62 62 40 46 37 38 97 48	P P P P P P P P P
GRAND TOTAL	L = 1088/1500 . RESULT: FIRST CI	LASS WITH D	ISTINC	TION											
B1200530	030 BIRAJDAR RUSHABH UMAKANT			RANJANA			,7152	7163G ,PICT	,B1200	53030					
404181 404182 404183 404184A 404185C 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & WE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 4 TW 50 2 OR 50 2 TW 50 2 PR 50 2 OR 50 2	0 12 0 20 0 21 0 22 0 13 0 0	47 42 35 34 30	59 F 62 F 56 F 56 F 43 F 47 F 28 F 37 F 23 F 42 F		404189 404190 404191C 404192A 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION AUDIO VIDEO ENGINER BIOMEDICAL SIGNAL R LAB PRACTICE III(MO LAB PRACTICE III(MO LAB PRAC. IV(ELECTI LAB PRAC. IV(ELECTI PROJECT PHASE II PROJECT PHASE II	ON ATION SYS. ERING PROCESSING C & BCS) C & BCS) IVE III)	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20 20	11 23 10 16	40 36 40 38	51 59 50 54 37 37 39 40 84 42	P P P P P P P P
GRAND TOTAL	L = 946 + 10/1500 , RESULT:FIRS	ST CLASS	[& 0.	163]											

GRAND TOTAL = 946 + 10/1500 , RESULT:FIRST CLASS [& O.163]

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH DATE :	BRANCH: 71-B.E. (2012 PAT.) (ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 11														
NOTE: FI	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER														
OTHER LI	NES: HEAD OF PASSING, MAX. M	ARKS, MIN	.PAS	S MAF	RKS, IN	JT. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED, P/F:	PASS/FAII	, C	:PREV	LOUS (CARRY	OVER
B1200530	31 BOBADE SRUSHTI PRAKASH			Ι	DARSHAN	JA		,7152	7164E ,PICT ,B1200	53031				• • • •	
	CDM • 1								GEM . O						
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100	40	15	55	70	РC	404189	SEM.:2 MOBILE COMMUNICATION	PP 100	40	17	38	55	P
404182	COMPUTER NETWORKS	PP 100	40	27	33	60	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	27	42	69	P
404183	MICROWAVE ENGINEERING	PP 100	40	22	45	67	РC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	23	50	73	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	21	46	67	РC	404192D	WIRELESS NETWORKS	PP 100	40	29	38	67	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	18	45	63	P C	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	J.M 20	20			29	P C	404193	LAB PRACTICE III (MC & BCS)	OR 50	20			44	Ъ
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			35 40	PC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	DR 50	20			44	P C	404194	DROTECT DHASE IT	TW 100	40			95	P D
404188	PROJECT PHASE I	OR 50	20			43	PC	404195	PROJECT PHASE II	PR 50	20			45	P
CDAND TOTAL	= 1084/1500 , RESULT:FIRST C	. УСС ИТШП	DIG	יייד אז ייי	TON.										
GRAND TOTAL	- 1004/1500 , RESULITERSI CI														
B1200530	32 BOMALE ADITYA SANJAY			7	'ASHODA	A		,7152	7166M ,PICT ,B1200	53032					
SEM.:1 SEM.:2 S															
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	16	53	69	РC	404189	MOBILE COMMUNICATION	PP 100	40	23	38	61	P
404182	COMPUTER NETWORKS	PP 100	40	26	41	67	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	26	37	63	P
404183	MICROWAVE ENGINEERING	PP 100	40	14	36	50	РC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	21	48	69	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	21	40	61	РC	404192D	WIRELESS NETWORKS	PP 100	40	26	47	73	P
404185C	PLCS AND AUTOMATION	PP 100	40	13	38	51	PC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			38	PC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			38 41	P C	404194	LAB PRAC. IV(ELECTIVE III)	J.M 20	20			20	Ъ
404107	IAB PRAC. II(VLSI &ELEC. I)	TW 50	20			33 41	P C	404194	DECITE DUNCE II	PR 50	40			3 <i>9</i>	P D
404188	PROJECT PHASE I	OR 50	20			42	PC	404195	PROJECT PHASE II	PR 50	20			43	P
GRAND TOTAL	= 1061/1500 , RESULT:FIRST C	LASS WITH	DIS	. T. T. N.C.1	.TON										
B1200530	33 BORA HARSHAL MAHESH			7	/AISHAI	Ί		,7152	7168H ,PICT ,B1200	53033					
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	14	39	53	РC	404189	MOBILE COMMUNICATION	PP 100	40	24	37	61	P
404182	COMPUTER NETWORKS	PP 100	40	23	32	55	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	25	32	57	P
404183	MICROWAVE ENGINEERING	PP 100	40	20	35	55	РC	404191C	AUDIO VIDEO ENGINEERING WIRELESS NETWORKS	PP 100	40	13	51	64	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	18	43	61	РC	404192D	WIRELESS NETWORKS	PP 100	40	27	34	61	P
404185C	PLCS AND AUTOMATION	PP 100	40	13	44	57	РC	404193	LAB PRACTICE III (MC & BCS)	TW 50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			34	P C	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OK 50	20			42	ь с	404194	LAB PRAC. IV(ELECTIVE III)	.T.M 20	20			40	D F
4U418/ 404197	LAB PRAC. II(VLSI &ELEC. I)	DB EU TM 20	20			4±3 ⊿1	D C	404194 404105	DROTECT DHAGE IT	PK 50	⊿∪ ⊿∩			39 06	D D
404188	PROJECT PHASE I	OR 50	20			47	PC	404195	LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50	20			46	P
101100	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	310 30	20			-,	- 0	101173	11.00 201 1111.00 11	110 30	20			10	-
GRAND TOTAL	= 1031/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	CION										

GRAND TOTAL = 1031/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	: 04 JUL 2018 CENTRE: PU	NE INSTIT	UTE	OF CO	MPUTER	TEC	HNOLOG	GY, PUNE.	PAC	GE NO. 1	.2					
NOTE: F		THE CAND	 IDAT	 ъ. М	OTHER,	PER	 MANENT	REG. NO.	, PREVIOUS SEAT NO.,	 COLLEGE	 E, SEAT	NO.	• • • • • •		• • • •	• • • • •
OTHER LI	INES: HEAD OF PASSING, MAX. M	ARKS, MIN	.PAS	S MAR	KS, IN	T. M	ARKS,	TH. MARKS	, TOTAL MARKS OBTAINED,	, P/F:P	ASS/FAIL	, (:PREVI	OUS C	'ARRY	OVER
B1200530	34 BOROLE NEHAL RAJENDRA			v	ANDANA			,7152	7169F ,PICT	,B12005	3034					
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	16	54	70	P C	404189	MOBILE COMMUNICATION	277	PP 100	40	26	51	77	P
404182 404183	COMPUTER NETWORKS	PP 100	40 40	25 25	3 / 45	62 70	PC	404190 404191n	SOFT COMPUTING	ON SYS.	PP 100	40 40	26	43 50	69 73	D P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	21	45	66	PC	404191D	WIRELESS NETWORKS		PP 100	40	28	42	70	P
404185D	ARTIFICIAL INTELLIGENCE	PP 100	40	13	45	58	РC	404193	LAB PRACTICE III(MC &	BCS)	TW 50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			44	P C	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			45	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			46	P C	404194	LAB PRAC. IV(ELECTIVE	III)	TW 50	20			40	P
404187 404187	LAB PRAC. II(VLSI &ELEC. I)	1W 5U	20 20			39	PC	404194 404195	DROTECT DHASE IT	TTT)	PK 50	∠0 4∩			40 97	P D
404188	COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			39	PC	404195	PROJECT PHASE II		PR 50	20			48	P
GRAND TOTAL	L = 1142/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	ION											
B1200530	35 BOTRE AKSHAYRAJ GANPAT			P	RATIBH	A		,7152	7170K ,PICT	,B12005	3035					
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	14	55	69	РC	404189	MOBILE COMMUNICATION		PP 100	40	26	36	62	P
404182	COMPUTER NETWORKS	PP 100	40	26	35	61	РC	404190	BROADBAND COMMUNICATIO	ON SYS.	PP 100	40	25	44	69	P
404183	MICROWAVE ENGINEERING	PP 100	40	20	40	60	P C	404191C	AUDIO VIDEO ENGINEERIN	NG	PP 100	40	19	55	74	P
404164A 404185C	PICS AND AUTOMATION	PP 100 PP 100	40	19 17	30	47	PC	404192A 404193	IAR PRACTICE TIT(MC &	BCS)	TW 50	20	24	40	32	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20	Ξ,	30	44	РC	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			45	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			34	РC	404194	LAB PRAC. IV(ELECTIVE	III)	TW 50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			38	P C	404194	LAB PRAC. IV(ELECTIVE	III)	PR 50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			38	PC	404195	PROJECT PHASE II		TW 100	40			96 47	P
404100	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OK 30	20			33	PC	404193	PROUECI PHASE II		PK 50	20			4/	P
GRAND TOTAL	L = 1069 + 10/1500 , RESULT:FI	RST CLASS	WIT	H DIS	TINCTI	ON	[& C	0.163]								
B1200530)36 CHANDOLE PALLAVI BHARAT			Α	RATI			,7163	3607D ,PICT	,B12005	3036					
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	19	61	80	P C	404189	MOBILE COMMUNICATION					41		
404182	COMPUTER NETWORKS	PP 100	40	29	44	73	P C	404190	BROADBAND COMMUNICATION AUDIO VIDEO ENGINEERIN					44 43		
404163 404184A	DIGITAL IMAGE PROCESSING	PP 100 PP 100	40	26	42	68	PC	404191C 404192A	BIOMEDICAL SIGNAL PROC	TECCTNC	חח 1 חח	40	2.2	20	60	D
404185C	PLCS AND AUTOMATION	PP 100	40	27	40	67	РC	404193	LAB PRACTICE III(MC &	BCS)	TW 50	20	22	30	40	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			47	РC	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			45	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			40	P C	404194	LAB PRAC. IV(ELECTIVE	III)	TW 50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			40	PC	404194	LAB PRAC. IV(ELECTIVE	III)	PR 50	20			41	Ъ
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			40	P C	404195	LAB PRACTICE III(MC & LAB PRACTICE III(MC & LAB PRAC. IV(ELECTIVE LAB PRAC. IV(ELECTIVE PROJECT PHASE II PROJECT PHASE II		PR 50	20			44	P
						- 0	- 0	-01170			_1. 50					=
GRAND TOTAL	L = 1131/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	ION											

GRAND TOTAL = 1131/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCI DATE :	BRANCH: 71-B.E. (2012 PAT.) (ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 13														
NOTE: F	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER														
	O37 CHANGEDE SHRENIK SANJAY														
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	DD 100	40	13	5.0	63	DС	404189	SEM.:2	DD 100	40	15	42	57	D
404182	COMPUTER NETWORKS	PP 100	40	21	42	63	PC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	20	38	58	P
404183	MICROWAVE ENGINEERING	PP 100	40	17	39	56	РC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	16	50	66	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	20	39	59	РC	404192D	WIRELESS NETWORKS	PP 100	40	27	41	68	P
404185C	PLCS AND AUTOMATION	DD TOO	40	12	40	52	P C	404193	LAB PRACTICE III (MC & BCS)	J.M 20	20			41 4E	Ъ
404186	TAB PRACTICE I (CN & MWE)	1W 5U	20			3 Q	P C	404193	TAB PRACTICE III (MC & BCS)	UK 50	20			45 2Ω	P D
404187	LAB PRAC. II(VIST & ELEC. I)	TW 50	20			42	PC	404194	IAB PRAC. IV(ELECTIVE III)	PR 50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			32	РC	404195	PROJECT PHASE II	TW 100	40			94	P
404188	PROJECT PHASE I	OR 50	20			42	P C	404195	PROJECT PHASE II	PR 50	20			47	P
GRAND TOTAL	L = 1038/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	ION										
B1200530)38 CHARVI AJAY GUPTA				 ACHMT		• • • • • •	7152	71.76.T DTCT B1200	53038	• • • •				• • • • •
	SEM.:1								SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	17	55	72	РC	404189	MOBILE COMMUNICATION	PP 100	40	17	47	64	P
404182	COMPUTER NETWORKS	PP 100	40	26	42	68	PC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	19	46	65	P
404183	MICROWAVE ENGINEERING	PP 100	40	22	36 47	58	P C	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	7.0 T.0	46	62 7E	Р
404184B	ARTIFICIAL INTELLICENCE	PP 100 PP 100	40	20	45	7 U	PC	4041920	IAR DRACTICE TIT(MC & RCG)	TW 50	20	22	55	40	D
404186	LAB PRACTICE I (CN & MWE)	TW 50	20	20	13	38	PC	404193	LAB PRACTICE III (MC & BCS)	OR 50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			44	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			37	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			34	РC	404195	PROJECT PHASE II	TW 100	40			92	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			44	РC	404195	PROJECT PHASE II	PR 50	20			44	P
GRAND TOTAL	= 1092/1500 . RESULT:FIRST C	ASS WITH	DTS	TINCT	TON										
B1200530)39 CHAUDHARI DARSHAN DURGADA			 Д	AYASHR	 T	• • • • • •	.7152	7177G PICT B1200	53039				• • • •	• • • • •
	SEM.:1								SEM.:2						_
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	14	59	73	PC	404189	MOBILE COMMUNICATION	PP 100	40	22	42		
404182	COMPUTER NETWORKS	PP 100	40	29	43	72	P C	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	10	46 51		
404103 404104	MICKOMAVE ENGINEEKING	PP 100	40	22	50	74	P C	4041910	AUDIO VIDEO ENGINEERING WIRELESS NETWORKS	PP 100	40	20	5.1 5.1	7.0	P
4041850	DI.CS AND AUTOMATION	PP 100	40	21	45	66	P C	4041920	IAR PRACTICE III (MC & RCS)	TW 50	20	49	JI	40	D D
404186	LAB PRACTICE I (CN & MWE)	TW 50	20	21	13	36	PC	404193	LAB PRACTICE III (MC & BCS)	OR 50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			43	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			41	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			40	РC	404195	PROJECT PHASE II	TW 100	40			95	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			46	РC	404195	LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50	20			45	P
	L = 1161/1500 , RESULT:FIRST C														

GRAND TOTAL = 1161/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCI DATE	BRANCH: 71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 14													
NOTE: F	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER													
	040 CHAUDHARI MITAL DURGADAS													
404181 404182 404183 404184A 404185C 404186	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACT. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 40 TW 50 20	13 29 27 24 16	57 34 47 39 37	70 63 74 63 53 27	P C P C P C P C P C	404189 404190 404191C 404192D 404193 404193	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS)	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50	40 40 40 40 20 20	23 28 24 29	42 43 45 58	65 71 69 87 40 42	P P P P P
404186 404187 404187 404188	LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50 20 TW 50 20 PR 50 20 OR 50 20			38 34 41 42	P C P C P C	404194 404194 404195 404195	LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	TW 50 PR 50 TW 100 PR 50	20 20 40 20			40 39 93 44	P P P
GRAND TOTAI	L = 1095/1500 , RESULT:FIRST C	LASS WITH DI	STINCT	'ION										
B1200530	041 CHAUDHARI SAMEER SATISH		S	SAVT.I.A			, /152	7180G ,PICT ,BI200	53041					
404181 404182 404183 404184B 404185D 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 40 TW 50 20 TW 50 20 PR 50 20 OR 50 20	16 21 23 20 21	51 48 43 54 44	67 69 66 74 65 35 30 39 32 38	P C C P C C P C C P C C P C C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 40 20 20 20 20 20	14 23 16 26	39 47 57 48	53 70 73 74 43 43 40 38 90 45	P P P P P P P
GRAND TOTAL	$T_{\rm c} = 1084/1500$ RESULT: FIRST C	LASS WITH DI	STINCT	'TON										
B1200530	042 CHAVAN MONIKA RADHAKISAN		s	ANGITA	· · · ·		,7163	3608B ,PICT ,B1200	53042	• • • •				
	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRACTICE I (VLSI & ELEC. I) PROJECT PHASE I L = 1220/1500 , RESULT:FIRST COMPANY AND				76 73 71 78 75 38 35 42 40 43	P C C C C C C C C C C C P P P P P P P P	404189 404190 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20	21 27 23 29	63 57 55 60	84 78	P P

GRAND TOTAL = 1220/1500 , RESULT:FIRST CLASS WITH DISTINCTION

SAVITRIBAI PHULE PUNE UNIVERSITY, B.E.(2012 COURSE) EXAMINATION, MAY 2018 BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04	4 JUL 2018 CENTRE: PUN	E INSTIT	UTE	OF CO	MPUTER	TEC	HNOLOG	Y, PUNE.	P.F	GE NO. 1	15					
NOTE: FIRST	T LINE : SEAT NO., NAME OF	THE CAND	IDAT	E, N	OTHER,	PER	MANENT	REG. NO.	, PREVIOUS SEAT NO.,	COLLEGE	E, SEAT	NO.				
OTHER LINES	S: HEAD OF PASSING, MAX. MA	RKS, MIN	.PAS	S MAF	RKS, IN	T. M	ARKS,	TH. MARKS	, TOTAL MARKS OBTAINED), P/F:F	PASS/FAIL	, C	PREVI	OUS C	'ARRY	OVER
B120053043	CHHAJED KSHITIJ SUBHASH				SANDHYA			,7152	7182C ,PICT	,B12005	53043				• • • •	
	SEM.:1								SEM.:2							
404181 VI	LSI DESIGN & TECHNOLOGY	PP 100	40	12	47	59	P C	404189	MOBILE COMMUNICATION	OM GMG	PP 100	40	19	44	63	P
404182 CC 404183 Mi	OMPUTER NETWORKS TOROWAVE ENGINEERING	PP 100 PP 100	40	∠8 19	3⊥ 37	59 56	P C	404190 404191D	SOFT COMPUTING	ON SYS.	PP 100 PP 100	40	2.4	39 42	66	P
404184B EN	MBEDDED SYSTEMS & RTOS	PP 100	40	23	29	52	PC	404192A	BIOMEDICAL SIGNAL PRO	CESSING	PP 100	40	21	43	64	P
404185D AF	RTIFICIAL INTELLIGENCE	PP 100	40	14	35	49	P C	404193	LAB PRACTICE III(MC &	BCS)	TW 50	20			36	P
404186 LA 404186 T.7	AB PRACTICE I (CN & MWE) AB DDACTICE I (CN & MWE)	TW 50	20			37	P C	404193 404194	LAB PRACTICE III (MC &	BCS)	OR 50	20			25 46	Ъ
404187 LA	AB PRAC. II(VLSI &ELEC. I)	TW 50	20			42	PC	404194	LAB PRAC. IV(ELECTIVE	: III)	PR 50	20			32	P
404187 LA	AB PRAC. II(VLSI &ELEC. I)	PR 50	20			34	P C	404195	PROJECT PHASE II		TW 100	40			85	P
404188 PF	SEM.:1 LSI DESIGN & TECHNOLOGY OMPUTER NETWORKS ICROWAVE ENGINEERING MBEDDED SYSTEMS & RTOS RTIFICIAL INTELLIGENCE AB PRACTICE I (CN & MWE) AB PRACTICE I (CN & MWE) AB PRAC. II(VLSI & ELEC. I) AB PRAC. II(VLSI & ELEC. I) ROJECT PHASE I	OR 50	20			30	РC	404195	PROJECT PHASE II		PR 50	20			30	P
GRAND TOTAL =	967/1500 , RESULT:FIRST CLA	SS														
B120053044	CHIKHALE TEJASWINI SAYAJI	:RAO		S	SUMAN			,7163	3609L ,PICT	,B12005	53044					
	SEM.:1								SEM.:2							
404181 VI	LSI DESIGN & TECHNOLOGY	PP 100	40	11	58	69	P C	404189	MOBILE COMMUNICATION		PP 100	40	26	56	82	P
404182 CC	OMPUTER NETWORKS	PP 100	40	29	42	71	P C	404190	BROADBAND COMMUNICATI	ON SYS.	PP 100	40	25	51	76	P
404183 MI 404184A DI	ICROWAVE ENGINEERING IGITAL IMAGE PROCESSING	PP 100 PP 100	40	21	55 51	72	PC	404191C 404192D	WIRELESS NETWORKS	.NG	PP 100 PP 100	40	24	53 57	84	P
404185C PI	LCS AND AUTOMATION	PP 100	40	22	48	70	РC	404193	LAB PRACTICE III(MC &	BCS)	TW 50	20			42	P
404186 LA	AB PRACTICE I (CN & MWE)	TW 50	20			40	P C	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			31	P
404186 LA 404187 T.7	AB PRACTICE I (CN & MWE) AB DDAC II(VISI SFIEC I)	OR 50	20			42	P C	404194	LAB PRAC. IV(ELECTIVE	: <u> </u>	TW 50	20			36 40	Ъ
404187 LA	AB PRAC. II(VLSI &ELEC. I)	PR 50	20			44	PC	404195	PROJECT PHASE II	1 111/	TW 100	40			85	P
404188 PF	SEM.:1 LSI DESIGN & TECHNOLOGY OMPUTER NETWORKS ICROWAVE ENGINEERING IGITAL IMAGE PROCESSING LCS AND AUTOMATION AB PRACTICE I (CN & MWE) AB PRACTICE I (CN & MWE) AB PRAC. II(VLSI & ELEC. I) AB PRAC. II(VLSI & ELEC. I) ROJECT PHASE I	OR 50	20			42	РC	404195	PROJECT PHASE II		PR 50	20			40	P
GRAND TOTAL =	1164/1500 PESULT: FIRST CL	HTTW 224.	DIS	TINCT	MOT											
R120053045	CHINMAY NITIN INDURKAR	• • • • • • • •			 FWD		• • • • • •	7152	7184K PTCT	R12005					• • • •	
										, D12000	3013					
404101 777	SEM.:1	DD 100	4.0	0.0	Г.С	60	D C	404100	SEM.: 2		DD 100	4.0	0.4	1.1	60	D
404181 VI 404182 CC	USI DESIGN & TECHNOLOGY	PP 100 PP 100	40	24	56 37	6⊿ 61	P C	404189 404190	MOBILE COMMUNICATION BROADBAND COMMUNICATI						68 73	
404183 MJ	ICROWAVE ENGINEERING	PP 100	40	24	48	72	РC	404191D	SOFT COMPUTING WIRELESS NETWORKS	.01. 515.	PP 100	40	26	49	75	P
404184A DI	IGITAL IMAGE PROCESSING	PP 100	40	13	42	55	ΡC	404192D	WIRELESS NETWORKS		PP 100	40	26	53	79	P
404185C PI	LCS AND AUTOMATION	PP 100	40 20	20	51	7.1	PC	404193	LAB PRACTICE III(MC &	BCS)	TW 50	20			44	P D
404186 LA	AB PRACTICE I (CN & MWE)	OR 50	20			45	P C	404194	LAB PRACTICE TIT(MC &	: III)	TW 50	20			43	P
404187 LA	AB PRAC. II(VLSI &ELEC. I)	TW 50	20			43	РC	404194	LAB PRAC. IV(ELECTIVE	: III)	PR 50	20			45	P
404187 LA	AB PRAC. II(VLSI &ELEC. I)	PR 50	20			46	P C	404195	LAB PRACTICE III(MC & LAB PRACTICE III(MC & LAB PRAC. IV(ELECTIVE LAB PRAC. IV(ELECTIVE PROJECT PHASE II PROJECT PHASE II		TW 100	40			92	P
404188 PF	SEM.:1 LSI DESIGN & TECHNOLOGY OMPUTER NETWORKS ICROWAVE ENGINEERING IGITAL IMAGE PROCESSING LCS AND AUTOMATION AB PRACTICE I (CN & MWE) AB PRACTICE I (CN & WE) AB PRAC. II(VLSI & ELEC. I) AB PRAC. II(VLSI & ELEC. I) ROJECT PHASE I	OK 50	∠U			43	P C	404195	PROJECT PHASE II		PK 50	۷۷			43	P
	1143/1500 , RESULT:FIRST CL															

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH DATE :	BRANCH: 71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 16													
NOTE: FI	NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.													
OTHER LI	NES: HEAD OF PASSING, MAX. M	ARKS, MIN	I.PAS	S MAF	RKS, IN	JT. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY	OVER				
	46 CHIRAG			5	SUDESH			,7152	7185H ,PICT ,B120053046					
	SEM.:1								SEM.:2					
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	16	53	69	РC	404189	MOBILE COMMUNICATION PP 100 40 22 45 67 BROADBAND COMMUNICATION SYS. PP 100 40 26 48 74 SPEECH & AUDIO SIGNAL PROC. PP 100 40 AA 44 44 BIOMEDICAL SIGNAL PROCESSING PP 100 40 22 45 67 LAB PRACTICE III(MC & BCS) TW 50 20 38 LAB PRACTICE III(MC & BCS) OR 50 20 44 LAB PRAC. IV(ELECTIVE III) TW 50 20 34 LAB PRAC. IV(ELECTIVE III) PR 50 20 38 PROJECT PHASE II TW 100 40 90 PROJECT PHASE II PR 50 20 44	P				
404182	COMPUTER NETWORKS	PP 100	40	27	38	65	PC	404190	BROADBAND COMMUNICATION SYS. PP 100 40 26 48 74	P				
404183 404184a	MICROWAVE ENGINEERING DICTTAL IMACE DEOCESSING	PP 100	40 40	∠5 24	40	7 N	PC	404191A 404192A	BIOMEDICAL SIGNAL PROCESSING DD 100 40 AA 44 44	P D				
404185C	PLCS AND AUTOMATION	PP 100	40	13	42	55	PC	404192A	LAB PRACTICE III (MC & BCS) TW 50 20 38	P				
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			47	РC	404193	LAB PRACTICE III(MC & BCS) OR 50 20 44	P				
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			39	РC	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 34	P				
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			35	РC	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20 38	P				
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			28	P C	404195	PROJECT PHASE II TW 100 40 90	P				
404188	PROJECT PHASE I	OR 50	20			40	ЬС	404195	PROJECT PHASE II PR 50 20 44	Ь				
GRAND TOTAL	. = 1053/1500 , RESULT:FIRST C	LASS WITH	DIS	2.T.TNG.1	I.TON									
B1200530	47 CHOUKSEY SONAL NARENDRA				 SHARDA			.7163		• • • •				
404101	SEM.:1	DD 100	4.0	1.0	F 0	- 1	D 0	404100	SEM.: 2	Б				
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	13 27	58 40	/ <u>1</u>	PC	404189	MOBILE COMMUNICATION PP 100 40 18 47 65 .	D P				
404182	MICROWAVE ENGINEERING	PP 100 PP 100	40	23	40	65	PC	404190 404191C	AUDIO VIDEO ENGINEERING DD 100 40 21 40 61	P D				
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	22	44	66	PC	404192D	WIRELESS NETWORKS PP 100 40 29 53 82	P				
404185C	PLCS AND AUTOMATION	PP 100	40	17	57	74	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20 40	P				
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			37	РC	404193	LAB PRACTICE III(MC & BCS) OR 50 20 42	P				
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			40	РC	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 24	P				
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			37	P C	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20 39	P				
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			3 L	P C	404195	PROJECT PHASE II TW 100 40 95	Ь				
404188	PROJECT PHASE I	OR 50	20			40	PC	404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 18 47 65 BROADBAND COMMUNICATION SYS. PP 100 40 27 49 76 AUDIO VIDEO ENGINEERING PP 100 40 21 40 61 WIRELESS NETWORKS PP 100 40 29 53 82 LAB PRACTICE III(MC & BCS) TW 50 20 40 LAB PRACTICE III(MC & BCS) OR 50 20 42 LAB PRACT. IV(ELECTIVE III) TW 50 20 24 LAB PRAC. IV(ELECTIVE III) PR 50 20 39 PROJECT PHASE II TW 100 40 95 PROJECT PHASE II PR 50 20 45	Р				
GRAND TOTAL	= 1097 + 10/1500 RESULT:FI	RST CLASS	ידע :	אר די	STINCTI	ON	0.31	1631						
B1200530	48 DABADGAONKAR SATYAM SANJA	AY		5	SUDHA			,7152						
	SEM.:1								SEM.:2					
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	15	53	68	РC	404189	MOBILE COMMUNICATION PP 100 40 20 45 65	P				
404182	COMPUTER NETWORKS	PP 100	40	29	40	69	РC	404190	BROADBAND COMMUNICATION SYS. PP 100 40 21 39 60	P				
404183	MICROWAVE ENGINEERING	PP 100	40	19	37	56	P C	404191C	AUDIO VIDEO ENGINEERING PP 100 40 20 40 60	P				
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	24	4.7	71	PC	404192A	BIOMEDICAL SIGNAL PROCESSING PP 100 40 25 45 70	P P				
4041850	PLCS AND AUTOMATION	PP 100	20	Ι/	53	/ 0	P C	404193	LAB PRACTICE III(MC & BCS) IW 50 20 46 LAB PRACTICE III(MC & BCS) OR 50 20 45	D D				
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			42	P C	404194	LAB PRACTICE TITING & BCS/ OK SO 20 45	P P				
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			44	PC	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20 40	P				
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			48	РĈ	404195	PROJECT PHASE II TW 100 40 97	P				
404188	PROJECT PHASE I	OR 50	20			37	P C	404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 20 45 65 BROADBAND COMMUNICATION SYS. PP 100 40 21 39 60 AUDIO VIDEO ENGINEERING PP 100 40 20 40 60 BIOMEDICAL SIGNAL PROCESSING PP 100 40 25 45 70 LAB PRACTICE III(MC & BCS) TW 50 20 46 LAB PRACTICE III(MC & BCS) OR 50 20 45 LAB PRAC. IV(ELECTIVE III) TW 50 20 40 LAB PRAC. IV(ELECTIVE III) PR 50 20 40 PROJECT PHASE II TW 100 40 97 PROJECT PHASE II PR 50 20 48	P				
GRAND TOTAL	= 1119 + 10/1500 , RESULT:FI	RST CLASS	WIT	H DIS	STINCTI	ON	[& C).163]						

GRAND TOTAL = 1119 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION [& O.163]

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH: 71-B.E. (2012 PAT.) (ELECTRONICS & TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 17													
NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, MAX. I	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER												
B120053049 DAHIPHALE VIJAY DATTU		ANT	IKA	,7152	7190D ,PICT ,B1200	53049							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 PP 50 20 PR 50 20 PR 50 20 OR 50 20	10 5 27 3 25 3 19 4 17 4	64 64 P 0 87 64 P 0 87 62 P 0 19 68 P 0 19 66 P 0 45 P 0 43 P 0 41 P 0	C 404189 C 404191 C 404191C C 404192D C 404193 C 404193 C 404194 C 404194 C 404195 C 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACT IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 25 41 66 PP 100 40 26 47 73 PP 100 40 16 37 53 PP 100 40 23 52 75 PT 50 20 35 PT 50 20 41 PR 50 20 41 PR 50 20 TW 100 40 95 PR 50 20 45 PR 50 20 PR 50 20 45 PR 50 20 PR 50 PR 50 20 PR 50 PR 5	5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5						
GRAND TOTAL = 1098/1500 , RESULT:FIRST CLASS WITH DISTINCTION													
B120053050 DEOSALE JINENDRA ANJANKUMAR SMITA ,71527194G ,PICT ,B120053050													
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	15 5 24 3 21 4 20 5 19 4	52 67 P 0 33 57 P 0 41 62 P 0 50 70 P 0 43 P 0 43 P 0 41 P 0 46 P 0 35 P 0	C 404189 C 404190 C 404191C C 404192A C 404193 C 404193 C 404194 C 404194 C 404195 C 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING BIOMEDICAL SIGNAL PROCESSING LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 20 42 62 PP 100 40 25 48 73 PP 100 40 17 36 53 PP 100 40 18 39 57 PT 50 20 30 PR 50 20 39 PT 100 40 40 PR 50 20 47 P							
GRAND TOTAL = 1062/1500 , RESULT:FIRST (CLASS WITH DI	STINCTION	1										
B120053051 DESHMUKH RUCHA UMESH		VAIS	SHALI	,7152	7198K ,PICT ,B1200	53051							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185B ELECTRONIC PRODUCT DESIGN 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I				C 404189 C 404190 C 404191C C 404192D C 404193 C 404193 C 404194 C 404194 C 404195 C 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS)	ם כל או פני או 100 מת	P P P P P P P						
GRAND TOTAL = 1100/1500 , RESULT:FIRST (CLASS WITH DI	STINCTION	J										

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BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH DATE :	BRANCH: 71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 18														
NOTE: F1	IRST LINE : SEAT NO., NAME OF	THE CAND	IDAT	Έ, Μ	OTHER,	PER	MANENT	REG. NO.	, PREVIOUS SEAT NO., COLLEG	E, SEAT	NO.				
	INES: HEAD OF PASSING, MAX. M.										,			ARRY	OVER
B1200530	052 DESHMUKH TANMAYI VINOD			R	ASHMI			,7152	7199H ,PICT ,B1200	53052					
	SEM.:1								SEM.:2						
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100	40	17	54	71	РC	404189	MOBILE COMMUNICATION	PP 100	40	20	40	60	P
404182	COMPUTER NETWORKS	PP 100	40	24	40	64	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	18	51	69	P
404183	MICROWAVE ENGINEERING	PP 100	40	21	43	64	PC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	17	43	60	P
404184A 404185C	DIGITAL IMAGE PROCESSING DICS AND AUTOMATION	PP 100 PP 100	40 40	22 15	49	7 N	PC	404192D 404193	MIRELESS NEIWORKS	TW 50	20	∠8	55	83 42	P D
404186	LAB PRACTICE I (CN & MWE)	TW 50	20	22	10	34	PC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			24	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			28	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			43	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			36	P C	404195	PROJECT PHASE II	TW 100	40			85	P
404188	PROJECT PHASE I	OR 50	20			41	ЬC	404195	PROJECT PHASE II	PR 50	20			39	Р
GRAND TOTAL	L = 1051/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	ION										
						<u>.</u>		7150	77001G DTGE D1000						• • • • •
B1200530	DESHPANDE MANSI VASANT			V	AISHAL	ıΤ		, /152	7/201C , PICT , B1200	53053					
	SEM.:1								SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	10	50	60	РC	404189	MOBILE COMMUNICATION	PP 100	40	20	37	57	P
404182	COMPUTER NETWORKS	PP 100	40	24	36	60	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	23	44	67	P
404183 404194B	MICROWAVE ENGINEERING	PP 100	40	20	39 40	59	PC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	17	34 40	51 67	Ъ
404184B	PICS AND AUTOMATION	PP 100 PP 100	40	22	47	69	PC	4041920	LAB PRACTICE III (MC & BCS)	TW 50	20	۷ /	40	36	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20		- '	38	РC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			32	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			36	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			38	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			39	PC	404195	PROJECT PHASE II	TW 100	40			88	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			39	PC	404195	PROJECT PHASE II	PR 50	20			40	Р
GRAND TOTAL	L = 1023/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	ION										
D1000F30	054 DESHPANDE SUKHADA SUNIL							7150	7700CD DTGE D1000		• • • •				• • • • •
B1200530	J54 DESHPANDE SUKHADA SUNIL			А	PARNA			, /152	,PICI ,BI200	53054					
	SEM.:1								SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	18	53	71	РC	404189	MOBILE COMMUNICATION	PP 100	40	22		64	
404182	COMPUTER NETWORKS	PP 100	40	27	40	67	PC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	23		73	
404183 4041841	MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING	PP 100 PP 100	40 40	26 24	48 51	7 4 75	PC	4041910	AUDIO VIDEO ENGINEERING WIRELESS NETWORKS	PP 100	40	28	40	0∠ 71	P D
404185C	PLCS AND AUTOMATION	PP 100	40	20	55	75	PC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20	20	13	36	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			27	РC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			33	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			32	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			38	P
404187 404189	DROIFOT DHASE T	PK 50	20			44 ⊿1	ь с БС	404195 404195	LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	DB EU J.M T00	40 20			90 41	P D
404100	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	UC AU	20			41	PC	404133	FROUECT FRASE II	PK 30	20			41	r
	L = 1090/1500 , RESULT:FIRST C														

GRAND TOTAL = 1090/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PUN	NE INSTIT	'UTE	OF CC	MPUTER	TEC	HNOLOG	Y, PUNE.	PA	GE NO. 1	L9					
NOTE: FIF		THE CAND	 IDAT	 Έ, м	OTHER,	PER	 MANENT	REG. NO.	, PREVIOUS SEAT NO.,	COLLEGE	 E, SEAT	NO.	• • • • •		• • • •	
OTHER LIN	NES: HEAD OF PASSING, MAX. MA	ARKS, MIN	.PAS	S MAR	RKS, IN	T. M	ARKS,	TH. MARKS	, TOTAL MARKS OBTAINED	, P/F:F	PASS/FAIL	, C	:PREV	COUS C	CARRY	OVER
B12005305	55 DESHPANDE UMANG SATISH	• • • • • • • •	• • • •		SNEHAL			,7152	7207B ,PICT	,B12005	53055				• • • •	
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	18	56	74	P C	404189	MOBILE COMMUNICATION	OM GMG	PP 100	40	21	53	74	P
404182	COMPUTER NETWORKS	PP 100	40	10	48 43	/5 62	PC	404190 404191C	BROADBAND COMMUNICATI	ON SYS.	PP 100	40	27	50 45	// 65	D P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	21	50	71	PC	404191C	WIRELESS NETWORKS	NG	PP 100	40	29	46	75	P
404185D	ARTIFICIAL INTELLIGENCE	PP 100	40	21	44	65	РC	404193	LAB PRACTICE III(MC &	BCS)	TW 50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			37	РC	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			41	P C	404194	LAB PRAC. IV(ELECTIVE	III)	TW 50	20			44	P
404187 404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			42	PC	404194	LAB PRAC. IV(ELECTIVE	111)	PR 50	20 40			39 95	D P
404187	PROJECT PHASE I	OR 50	20			47	PC	404195	PROJECT PHASE II		PR 50	20			95 45	P
101200	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & WE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	010 00					- 0	10110			211 00					-
GRAND TOTAL	= 1157/1500 , RESULT:FIRST CI	LASS WITH	DIS	TINCT	CION											
D12005205		• • • • • • •	• • • •		יייי א	• • • •	• • • • • •	7162		D1200E		• • • •			• • • •	• • • • •
B12005305	DEVADE SAIALI KALIAN				OTIA			, /103	JOILD , PICI	,612005	33030					
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	16	56	72	РC	404189	MOBILE COMMUNICATION		PP 100	40	19	50	69	P
404182	COMPUTER NETWORKS	PP 100	40	29	45	74	P C	404190	BROADBAND COMMUNICATI	ON SYS.	PP 100	40	28	51	79 50	P
404183 404184Δ	DIGITAL IMAGE PROCESSING	PP 100 PP 100	40	∠4 18	50 48	74 66	PC	404191C 404192D	MIRELESS NETWORKS	NG	PP 100	40	20 28	39 48	59 76	P D
404185C	PLCS AND AUTOMATION	PP 100	40	20	45	65	PC	404193	LAB PRACTICE III(MC &	BCS)	TW 50	20	20	10	42	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			39	РC	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			32	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			30	РC	404194	LAB PRAC. IV(ELECTIVE	III)	TW 50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			39	РC	404194	LAB PRAC. IV(ELECTIVE	III)	PR 50	20			39	P
404187 404188	DROTECT DUNCE T	PR 50	20			36 38	PC	404195	PROJECT PHASE II		JM T00	20			86 40	D P
404100	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OK 30	20			30	PC	404193	PROJECT PHASE II		PR 50	20			40	P
GRAND TOTAL	= 1091/1500 , RESULT:FIRST CI	LASS WITH	DIS	TINCT	CION											
B12005305	57 DEVANG VINAY DAVE	•••••		N	IEETA			,7152	7209J ,PICT	,B12005	53057					
	SEM.:1								SEM.:2							
404181		PP 100	40	17	57	74	РC	404189	MOBILE COMMUNICATION		PP 100	40	25	47	72	P
404182	COMPUTER NETWORKS	PP 100	40	24	48	72	РC	404190	BROADBAND COMMUNICATI	ON SYS.	PP 100	40	27	43	70	P
404183	MICROWAVE ENGINEERING	PP 100	40	17	47	64	P C	404191C	AUDIO VIDEO ENGINEERI	NG	PP 100	40	16	44	60	P
404184A 404185C	DIGITAL IMAGE PROCESSING	PP 100	40 40	22 16	5∠ 42	74 58	PC	404192A 404193	IAR DRACTICE III/MC S	CESSING	TW 50	20	∠0	41	0 / 27	P D
404186	LAB PRACTICE I (CN & MWE)	TW 50	20	10	12	43	PC	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			38	РC	404194	LAB PRAC. IV(ELECTIVE	III)	TW 50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			38	РC	404194	LAB PRAC. IV(ELECTIVE	III)	PR 50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			42	P C	404195	PROJECT PHASE II		TW 100	40			95	P
404188	VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OK 50	∠U			58	P C	404195	PROJECT PHASE II		PK 50	۷۷			45	Р
GRAND TOTAL	= 1086/1500 , RESULT:FIRST CI	LASS WITH	DIS	TINCT	CION											

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH: 71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 20														
NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, MAX. M	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER													
B120053058 DHAKANE KIRANKUMAR SHAHA	ADEO	SHOBH	 A	,7163	33612L ,PICT ,B1200	 53058	• • • • • • • • • • • • • • • • • • • •							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185B ELECTRONIC PRODUCT DESIGN 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 PP 100 40 PP 100 40 PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	23 43 23 39 19 50 19 42	68 P C 66 P C 62 P C 69 P C 61 P C 43 P C 34 P C 39 P C 44 P C 43 P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS)	PP 100 40 16 PP 100 40 23 PP 100 40 19 PP 100 40 23 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	54 70 P 40 63 P 45 64 P 49 72 P 40 P 42 P 34 P 38 P 95 P 40 P							
GRAND TOTAL = 1087/1500 , RESULT:FIRST CLASS WITH DISTINCTION														
B120053059 DHONDGE KANCHAN VIJAY NANDINI ,71633613J ,PICT ,B120053059														
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185B ELECTRONIC PRODUCT DESIGN 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I							50 73 P 45 65 P 44 59 P 54 83 P 42 P 44 P 40 P 36 P 95 P 41 P							
GRAND TOTAL = 1150/1500 , RESULT:FIRST C	LASS WITH DI	STINCTION												
B120053060 DHUMALE AJINKYA DNYANESH		SUNIT	A	,7163	33614G ,PICT ,B1200	53060								
B120053060 DHUMALE AJINKYA DNYANESHWAR SUNITA ,71633614G ,PICT ,B120053060 SEM.:1 SEM.:1 SEM.:2 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 14 49 63 P C 404189 MOBILE COMMUNICATION PP 100 40 17 46 63 P 404182 COMPUTER NETWORKS PP 100 40 26 40 66 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 24 40 64 P 404183 MICROWAVE ENGINEERING PP 100 40 19 40 59 P C 404191 AUDIO VIDEO ENGINEERING PP 100 40 22 45 67 P 404184 DIGITAL IMAGE PROCESSING PP 100 40 20 53 73 P C 404192D WIRELESS NETWORKS PP 100 40 26 41 67 P 404185 PLCS AND AUTOMATION PP 100 40 24 53 77 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 40 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 48 P C 404193 LAB PRACTICE III(MC & BCS) OR 50 20 35 P 404186 LAB PRACTICE I (CN & MWE) OR 50 20 48 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 30 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 40 P 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 40 P 404188 PROJECT PHASE I TW 100 40 90 P 404188 PROJECT PHASE I OR 50 20 44 P														
GRAND TOTAL = 1057/1500 , RESULT:FIRST C	TH HITM COMM.	O T TINC I TOIN												

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 21													
NOTE: FIRST LINE : SEAT NO., NAME C	F THE CANDIDATE,	MOTHER, PERMANEN'	T REG. NO., PREVIOUS SEAT NO., COLLEGI	E, SEAT NO.									
			TH. MARKS, TOTAL MARKS OBTAINED, P/F:										
B120053061 DIKHALE SNEHAL SUBHASH		JAYSHREE	,71527217K ,PICT ,B12009	53061									
SEM.:1			SEM.:2										
404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS	PP 100 40 14	4 28 42 P C	404189 MOBILE COMMUNICATION 404190 BROADBAND COMMUNICATION SYS.										
404182 COMPOTER NETWORKS 404183 MICROWAVE ENGINEERING	PP 100 40 21	1 46 67 P C	404191C AUDIO VIDEO ENGINEERING	PP 100 40 18 37 55 P									
404184B EMBEDDED SYSTEMS & RTOS	PP 100 40 23	3 44 67 P C	404192D WIRELESS NETWORKS 404193 LAB PRACTICE III(MC & BCS)	PP 100 40 25 44 69 P									
404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE)	TW 50 20	39 P C	404193 LAB PRACTICE III (MC & BCS)	TW 50 20 36 P OR 50 20 40 P									
404186 LAB PRACTICE I (CN & MWE)	OR 50 20	29 P C	404194 LAB PRAC. IV(ELECTIVE III)	TW 50 20 39 P									
404187 LAB PRAC. II(VLSI &ELEC. I) 404187 LAB PRAC. II(VLSI &ELEC. I)	TW 50 20 PR 50 20	35 P C 39 P C	404194 LAB PRAC. IV(ELECTIVE III) 404195 PROJECT PHASE II	PR 50 20 40 P TW 100 40 96 P									
404188 PROJECT PHASE I OR 50 20 44 P C 404195 PROJECT PHASE II PR 50 20 46 P													
GRAND TOTAL = 1047/1500 , RESULT:FIRST CLASS WITH DISTINCTION													
B120053062 GADIYA RUSHABH KANTILAL SUNITA ,71633616C ,PICT ,B120053062													
404181 VLSI DESIGN & TECHNOLOGY	PP 100 40 15	5 49 64 P C	404189 MOBILE COMMUNICATION										
404182 COMPUTER NETWORKS	PP 100 40 29	9 44 73 P.C.	404190 BROADBAND COMMUNICATION SYS.	PP 100 40 25 54 79 P PP 100 40 16 44 60 P									
404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING	PP 100 40 24	4 52 76 P C	404191C AUDIO VIDEO ENGINEERING 404192D WIRELESS NETWORKS	PP 100 40 16 44 00 P PP 100 40 27 49 76 P									
404185C PLCS AND AUTOMATION	PP 100 40 25	5 48 73 P C	404193 LAB PRACTICE III (MC & BCS)	TW 50 20 38 P									
404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE)	TW 50 20 OR 50 20	48 P C 40 P C	404194 LAB PRACTICE III (MC & BCS) 404194 LAB PRAC. IV(ELECTIVE III)	OR 50 20 39 P TW 50 20 40 P									
404187 LAB PRAC. II(VLSI &ELEC. I)	TW 50 20	41 P C	404194 LAB PRAC. IV(ELECTIVE III)	PR 50 20 38 P									
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PR 50 20	42 P C	404195 PROJECT PHASE II 404195 PROJECT PHASE II	PP 100 40 27 49 76 P TW 50 20 38 P OR 50 20 39 P TW 50 20 40 P PR 50 20 38 P TW 100 40 87 P PR 50 20 40 P									
			10 1195 TROUBET TIMES II	10 1									
GRAND TOTAL = 1134/1500 , RESULT:FIRST													
B120053063 GAIKWAD SAMRUDDHI AVINA	ASH	NILIMA	,71527224B ,PICT ,B1200!	53063									
SEM.:1			SEM.:2										
404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING	PP 100 40 13	3 44 57 P 6 45 71 P.C	404189 MOBILE COMMUNICATION 404190 BROADBAND COMMUNICATION SYS.										
404183 MICROWAVE ENGINEERING	PP 100 40 22	2 35 57 P	404191C AUDIO VIDEO ENGINEERING	PP 100 40 19 46 65 P									
404184A DIGITAL IMAGE PROCESSING 404185B ELECTRONIC PRODUCT DESIGN	PP 100 40 22	2 34 56 P C	404192D WIRELESS NETWORKS 404193 LAB PRACTICE III(MC & BCS)	PP 100 40 21 55 76 P TW 50 20 35 P									
404186 LAB PRACTICE I (CN & MWE)	TW 50 20	30 P C	404193 LAB PRACTICE III(MC & BCS)	OR 50 20 35 P									
404186 LAB PRACTICE I (CN & MWE)	OR 50 20	22 P C	404194 LAB PRAC. IV(ELECTIVE III)	TW 50 20 35 P									
404187 LAB PRAC. II(VLSI &ELEC. I) 404187 LAB PRAC. II(VLSI &ELEC. I)	1W 5U 2U PR 50 20	25 P C	404192D WIRELESS NETWORKS 404193 LAB PRACTICE III(MC & BCS) 404193 LAB PRACTICE III(MC & BCS) 404194 LAB PRAC. IV(ELECTIVE III) 404194 LAB PRAC. IV(ELECTIVE III) 404195 PROJECT PHASE II 404195 PROJECT PHASE II	PR 50 20 39 P TW 100 40 60 P									
404185B ELECTRONIC PRODUCT DESIGN 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	OR 50 20	40 P C	404195 PROJECT PHASE II	PR 50 20 22 P									
GRAND TOTAL = 915/1500 , RESULT:FIRST C													

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERNANDAY RES. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO., OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. HASS MARKS, INT. MARKS, TH. TRANS., TOTAL MARKS OSTAINED, PFFTPASS/PAIL, C-PREVIOUS CARRY OVER BIJOUSS/COLLEGE TOTAL MARKS OSTAINED, PFTPASS/PAIL, C-PREVIOUS CARRY OVER BIJOUS AND COMMUNICATION PRIOR MARKS OSTAINED, PRIOR	BRANCH: 71-B.E. (2012 PAT.) (ELECTRONICS & TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 22													
SEM .1 SEM .1 SEM .1 SEM .1 SEM .2 S	NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, MAX. MAX.	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER												
B120053065 GANGRAS ANURAG ASHUTOSH B120053065 GANGRAS ANURAG ASHUTOSH SEM.:1 SEM.:2 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 11 30 41 P C 404189 MOBILE COMMUNICATION PP 100 40 23 35 58 P 404182 COMPUTER NETWORKS PP 100 40 25 42 67 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 18 37 55 P 404183 MICROWAVE ENGINEERING PP 100 40 23 40 63 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 15 37 52 P 404184A DIGITAL IMAGE PROCESSING PP 100 40 22 49 71 P C 404192D WIRELESS NETWORKS PP 100 40 26 43 69 P 404185D ARTIFICIAL INTELLIGENCE PP 100 40 15 32 47 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 26 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 46 P C 404194 LAB PRACTICE III(MC & BCS) OR 50 20 42 P 404186 LAB PRACTICE I (CN & MWE) OR 50 20 30 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 28 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 30 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 34 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 30 P C 404195 PROJECT PHASE II TW 100 40 86 P 404188 PROJECT PHASE I OR 50 20 35 P C 404195 PROJECT PHASE II TW 100 40 P GRAND TOTAL = 959/1500 , RESULT:FIRST CLASS B120053066 GARJE PRIYANKA BABASAHEB SANGITA ,71527231E ,PICT ,B120053066	B120053064 GANGISHETTI SRAVAN		PADMA		,715272	26J ,PICT ,B12005	3064	• • • • • • • • • • • • • • • • • • • •						
B120053065 GANGRAS ANURAG ASHUTOSH B120053065 GANGRAS ANURAG ASHUTOSH SEM.:1 SEM.:2 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 11 30 41 P C 404189 MOBILE COMMUNICATION PP 100 40 23 35 58 P 404182 COMPUTER NETWORKS PP 100 40 25 42 67 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 18 37 55 P 404183 MICROWAVE ENGINEERING PP 100 40 23 40 63 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 15 37 52 P 404184A DIGITAL IMAGE PROCESSING PP 100 40 22 49 71 P C 404192D WIRELESS NETWORKS PP 100 40 26 43 69 P 404185D ARTIFICIAL INTELLIGENCE PP 100 40 15 32 47 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 26 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 46 P C 404194 LAB PRACTICE III(MC & BCS) OR 50 20 42 P 404186 LAB PRACTICE I (CN & MWE) OR 50 20 30 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 28 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 30 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 34 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 30 P C 404195 PROJECT PHASE II TW 100 40 86 P 404188 PROJECT PHASE I OR 50 20 35 P C 404195 PROJECT PHASE II TW 100 40 P GRAND TOTAL = 959/1500 , RESULT:FIRST CLASS B120053066 GARJE PRIYANKA BABASAHEB SANGITA ,71527231E ,PICT ,B120053066	SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	18 48 25 55 27 55 27 42 26 46	66 P C 80 P C 82 P C 69 P C 72 P C 38 P C 40 P C 38 P C 42 P C 46 P C	404189 Mc 404190 Bi 404191C Ai 404192D Wi 404193 Li 404193 Li 404194 Li 404194 Li 404195 Pi 404195 Pi	SEM.:2 OBILE COMMUNICATION ROADBAND COMMUNICATION SYS. UDIO VIDEO ENGINEERING IRELESS NETWORKS AB PRACTICE III(MC & BCS) AB PRACTICE III(MC & BCS) AB PRAC. IV(ELECTIVE III) AB PRAC. IV(ELECTIVE III) ROJECT PHASE II ROJECT PHASE II	PP 100 40 23 PP 100 40 17 PP 100 40 17 PP 100 40 29 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	40 63 P 50 67 P 47 64 P 53 82 P 35 P 39 P 35 P 39 P 95 P 45 P						
B120053065 GANGRAS ANURAG ASHUTOSH SEM.:1 SEM.:2 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 11 30 41 P C 404189 MOBILE COMMUNICATION PP 100 40 23 35 58 P 404182 COMPUTER NETWORKS PP 100 40 25 42 67 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 15 37 55 P 404183 MICROWAVE ENGINEERING PP 100 40 23 40 63 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 15 37 52 P 404184A DIGITAL IMAGE PROCESSING PP 100 40 15 32 47 P C 404192D WIRELESS NETWORKS PP 100 40 26 43 69 P 404185D ARTIFICIAL INTELLIGENCE PP 100 40 15 32 47 P C 404193 LAB PRACTICE II(MC & BCS) TW 50 20 26 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 39 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 42 P 404187 LAB PRAC. IV(VISI & ELEC. I) TW 50 20 30 P C 404197 LAB PRAC. IV(ELECTIVE III) PR 50 20 34 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 35 P C 404195 PROJECT PHASE II TH 100 40 86 P 404188 PROJECT PH	GRAND TOTAL = 1137/1500 , RESULT: FIRST C	LASS WITH DIS	TINGITON											
GRAND TOTAL = 959/1500 , RESULT:FIRST CLASS B120053066 GARJE PRIYANKA BABASAHEB SANGITA ,71527231E ,PICT ,B120053066	B120053065 GANGRAS ANURAG ASHUTOSH		APARNA		,715272	27G ,PICT ,B12005	3065							
B120053066 GARJE PRIYANKA BABASAHEB SANGITA ,71527231E ,PICT ,B120053066			11 30 25 42 23 40 22 49 15 32	41 P C 67 P C 63 P C 71 P C 47 P C 46 P C 39 P C 30 P C 30 P C 35 P C	404189 MM 404190 Bi 404191C Ai 404192D Wi 404193 Li 404193 Li 404194 Li 404195 Pi	SEM.:2 OBILE COMMUNICATION ROADBAND COMMUNICATION SYS. UDIO VIDEO ENGINEERING IRELESS NETWORKS AB PRACTICE III(MC & BCS) AB PRACTICE III(MC & BCS) AB PRAC. IV(ELECTIVE III) AB PRAC. IV(ELECTIVE III) ROJECT PHASE II	PP 100 40 23 PP 100 40 18 PP 100 40 15 PP 100 40 26 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	37 55 P 37 52 P						
B120053066 GARJE PRIYANKA BABASAHEB SANGITA ,71527231E ,PICT ,B120053066														
404182 COMPUTER NETWORKS PP 100 40 19 36 55 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 18 37 55 P 404183 MICROWAVE ENGINEERING PP 100 40 16 46 62 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 17 49 66 P 404184A DIGITAL IMAGE PROCESSING PP 100 40 26 48 74 P C 404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 25 38 63 P 404185C PLCS AND AUTOMATION PP 100 40 21 35 56 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 32 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 45 P C 404194 LAB PRACTICE III(MC & BCS) OR 50 20 38 P 404186 LAB PRACTICE I (CN & MWE) OR 50 20 35 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 36 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 35 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 37 P 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 42 P C 404195 PROJECT PHASE II TW 100 40 91 P 404188 PROJECT PHASE I OR 50 20 42 P C 404195 PROJECT PHASE II PR 50 20 45 P	B120053066 GARJE PRIYANKA BABASAHEB		SANGIT	A	,715272	31E ,PICT ,B12005	33066							
GRAND TOTAL = 1019 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION [& 0.163]	404181 VLSI DESIGN & IECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I GRAND TOTAL = 1019 + 10/1500 , RESULT:FIR	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20 RST CLASS WIT	19 36 16 46 26 48 21 35	55 P C 62 P C 74 P C 56 P C 45 P C 25 P C 35 P C 30 P C 42 P C	404189 MM 404191C A 404192A B 404193 L 404193 L 404194 L 404194 L 404195 PI 404195 PI 1.163]	OBILE COMMUNICATION ROADBAND COMMUNICATION SYS. UDIO VIDEO ENGINEERING IOMEDICAL SIGNAL PROCESSING AB PRACTICE III(MC & BCS) AB PRACTICE III(MC & BCS) AB PRAC. IV(ELECTIVE III) AB PRAC. IV(ELECTIVE III) ROJECT PHASE II ROJECT PHASE II	PP 100 40 24 PP 100 40 18 PP 100 40 17 PP 100 40 25 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	30 02 P 37 55 P 49 66 P 38 63 P 32 P 38 P 36 P 37 P 91 P 45 P						

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	BRANCH: 71-B.E. (2012 PAT.) (ELECTRONICS & TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 23														
NOTE: FI	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER														
B1200530	067 GAWANDE KARTIK DILIP			SANDHYA	٠		,7152	7236F ,PICT ,B	12005	 3067	• • • •				• • • • •
	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) PROJECT PHASE I		0 03 0 21 0 01 0 09 0 03 0 0	37\$ 32 43 34 32	40 53 44 43 20 35 28 35 34	P C P C P P C	404189 4041910 404191C 4041920 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SAUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS LAB PRACTICE III(MC & BCS LAB PRAC. IV(ELECTIVE III LAB PRAC. IV(ELECTIVE III PROJECT PHASE II PROJECT PHASE II	SYS. S) S) I) I)	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20	15 08 13 20	32 34 32 32	47 42 45 52 37 23 30 34 70 30	P
GRAND TOTAL	L =/1500 , RESULT:FAILS [:	3 0.1]				R	RESULT RES	ERVED FOR BACKLOGS.							
B1 200530	068 GHADLING SNEHAL JAYWANT					• • • • • •	7163		12005		• • • •			• • • •	
404181 404182 404183 404184B 404185C 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (LAB PROJECT PHASE I (LAB PRACTICE I (L	PP 100 4 TW 50 2 OR 50 2 TW 50 2 PR 50 2 OR 50 2	0 15 0 27 0 24 0 18 0 11 0 0	43 37 37 45 34	58 64 61 63 45 36 33 44 33 35	P C P C P C P C P C P C	404189 4041910 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION S AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS LAB PRACTICE III(MC & BCS LAB PRAC. IV(ELECTIVE III LAB PRAC. IV(ELECTIVE III PROJECT PHASE II PROJECT PHASE II	SYS. S) S) I)	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20 20	22 17 23 24	46 36 39 55	68 53 62 79 40 37 32 40 79 36	P P P P P P
GRAND TOTAL	L = 998/1500 , RESULT:FIRST CLA	ASS WITH DI	STINCT	CION											
B1200530	069 GUPTA SANKALP RAJESH			SANDHYA			,7152	7247M ,PICT ,B	 12005	 3069	• • • •				• • • • •
404181 404182 404183 404184A 404185C 404186 404186 404187 404187 404188 GRAND TOTAI	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I L = 950/1500 , RESULT:FIRST CL2	PP 100 4 TW 50 2 OR 50 2 TW 50 2 PR 50 2 OR 50 2 ASS [\$ 0.	0 14 0 20 0 19 0 25 0 18 0 0	30 31 32 51 42	44 51 51 76 60 44 38 38 30 40		404189 4041910 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SAUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS	SYS. S) S) I)	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20	20 22 08 20	30 34 32\$ 32	50 56 40 52 40 32 37 33 92 46	P P P P P P P P P

GRAND TOTAL = 950/1500 , RESULT:FIRST CLASS [\$ 0.1]

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	BRANCH: 71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 24													
NOTE: F	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.													
OTHER L	INES: HEAD OF PASSING, MAX. M	ARKS, MIN	.PAS	S MARK	S, IN	T. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY (OVER				
B1200530	070 GWALHERKAR KRUTIKA MANOJ	• • • • • • • • •		SAI	NGITA			,7152	7248K ,PICT ,B120053070	• • • •				
	SEM.:1								SEM.:2					
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	16	39	55	P C	404189	SEM.:2 MOBILE COMMUNICATION PP 100 40 21 35 56 18 18 18 18 18 18 18 18 18 18 18 18 18	P				
404182 404183	COMPUTER NETWORKS	PP 100	40 40	23	48 52	71 75	P C	404190 404191C	BROADBAND COMMUNICATION SYS. PP 100 40 19 48 67 1	D F				
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	19	47	66	PC	404191C	BIOMEDICAL SIGNAL PROCESSING PP 100 40 23 46 69 1	P				
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	20	42	62	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20 42	P				
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			35	РC	404193	LAB PRACTICE III(MC & BCS) OR 50 20 40 1	P				
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			40	P C	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 30 1	P				
404187 404187	LAB PRAC. II(VLSI &ELEC. I) IAB DRAC II(VLSI &ELEC. I)	TW 50	20 20			3 Z 3 Z	P C	404194 404195	DROTECT DHASE II TW 100 40 78 1	P D				
404188	PROJECT PHASE I	OR 50	20			33	PC	404195	PROJECT PHASE II PR 50 20 33 1	P				
GRAND TOTAI	L = 998/1500 , RESULT:FIRST CLA	ASS WITH I	DIST	INCTIO	N									
D1000F3	771							7150	7040m PTOT P1000F2071					
									7249H ,PICT ,B120053071					
	SEM.:1								SEM.:2					
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	13	37	50	РC	404189	MOBILE COMMUNICATION PP 100 40 17 43 60 1	P				
404182	COMPUTER NETWORKS	PP 100	40	23	41	64	PC	404190	BROADBAND COMMUNICATION SYS. PP 100 40 17 43 60 1	P				
404163 404184A	DIGITAL IMAGE PROCESSING	PP 100 PP 100	40	14	49	55 63	PC	404191D 404192D	WIRELESS NETWORKS PP 100 40 15 41 56 1	P P				
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	16	50	66	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20 35	P				
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			27	P C	404193	LAB PRACTICE III(MC & BCS) OR 50 20 40 1	P				
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			32	P C	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 30 1	P				
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			3.7	PC	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20 32 1	P				
404187	PROJECT PHASE I	OR 50	20			38	PC	404195	SEM.: 2 MOBILE COMMUNICATION PP 100 40 17 43 60 18 BROADBAND COMMUNICATION SYS. PP 100 40 17 43 60 18 SOFT COMPUTING PP 100 40 15 41 56 19 WIRELESS NETWORKS PP 100 40 21 40 61 18 LAB PRACTICE III(MC & BCS) TW 50 20 35 18 LAB PRACTICE III(MC & BCS) OR 50 20 40 18 LAB PRACT. IV(ELECTIVE III) TW 50 20 30 18 LAB PRAC. IV(ELECTIVE III) PR 50 20 32 20 PROJECT PHASE II TW 100 40 94 19 PROJECT PHASE II PR 50 20 44 19	P P				
CPAND TOTAL	L = 988/1500 , RESULT:FIRST CL	100												
B1200530	072 HEMANT KUMAR DUKRE			RE	KHA			,7152	7251K ,PICT ,B120053072					
	SEM.:1								SEM.:2					
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	11	32	43	P C	404189	MOBILE COMMUNICATION PP 100 40 22 43 65 1	P				
404182	MICROWAVE ENGINEERING	PP 100 PP 100	40	⊿3 15	37	7 / 5 2	PC	404190 404191C	AUDIO VIDEO ENGINEERING PP 100 40 21 46 67 1	P P				
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	20	49	69	PC	404192D	WIRELESS NETWORKS PP 100 40 17 41 58	P				
404185C	PLCS AND AUTOMATION	PP 100	40	16	42	58	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20 43 1	P				
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			47	P C	404193	LAB PRACTICE III(MC & BCS) OR 50 20 32 1	P				
404186 404197	LAB PRACTICE I (CN & MWE)	OR 50	20			33 42	ъ С Б С	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 41 1	D F				
404187	LAB PRAC. II(VIST & FIEC T)	PR 50	20			32	PC	404195	PROJECT PHASE II TW 100 40 95 1	F P				
404188	PROJECT PHASE I	OR 50	20			39	PC	404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 22 43 65 18 18 18 19 19 19 19 19 100 40 22 43 65 19 19 19 19 19 19 19 19 19 19 19 19 19	P				
	L = 1026/1500 , RESULT:FIRST C													

GRAND TOTAL = 1026/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH:71-B.1 DATE : 04 JUI	BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 25																
NOTE: FIRST LI	NE : SEAT NO., NAME OF	THE CAND:	IDAT	E, M	OTHER,	PER	MANENT	REG. NO.	, PREV	IOUS SEAT NO.,	COLLEGE	E, SEAT	NO.				
OTHER LINES: H	EAD OF PASSING, MAX. MA	ARKS, MIN	.PAS	S MAR	KS, IN	T. M	ARKS,	TH. MARKS	, TOTAL	MARKS OBTAINED	, P/F:P	PASS/FAIL	, C	:PREV	IOUS (ARRY	OVER
B120053073 II	NGALE RAVEENA RAVINDRA			R	AMA			,7152	7255B	,PICT	,B12005	3073					
SI	EM.:1								SEN	м.:2							
404181 VLSI I	DESIGN & TECHNOLOGY	PP 100	40	17	28	45	P C								37		
404182 COMPUT 404183 MICRO	DESIGN & TECHNOLOGI TTER NETWORKS WAVE ENGINEERING DED SYSTEMS & RTOS TCIAL INTELLIGENCE	PP 100 PP 100	40	21 25	50 39	71 64	P C	404190 404191C		AND COMMUNICATION VIDEO ENGINEERIN					55 47	69 71	_
404184B EMBEDI	DED SYSTEMS & RTOS	PP 100	40	23	47	70	РC	404102D	MIDDIE	CC NETWODEC		חח 1 חח	40	20	EO	0.1	D
404185D ARTIF: 404186 LAB PI	'ICIAL INTELLIGENCE	PP 100	40	21	40	61 40	P C	404193	LAB PRA	ACTICE III(MC &	BCS)	TW 50	20			45	P P
404186 LAB PI	RACTICE I (CN & MWE)	OR 50	20			35	P C	404194	LAB PRA	AC. IV(ELECTIVE	III)	TW 50	20			45	P
404187 LAB PI	RAC. II(VLSI &ELEC. I)	TW 50	20			42	P C	404194	LAB PRA	AC. IV(ELECTIVE	III)	PR 50	20			40	P
404187 LAB PI 404188 PROJEC	PED SISTEMS & RIOS ICIAL INTELLIGENCE RACTICE I (CN & MWE) RACTICE I (CN & ELEC. I) RAC. II(VLSI & ELEC. I) CT PHASE I	OR 50	20			33 44	P C	404195	PROJECT.	Γ PHASE II Γ PHASE II		PR 50	20			95 45	P P
GD337D MOM31 100	6 /1500 PROVED BERGE GE	3.00	DIG		T 0.17												
GRAND TOTAL = 1080	6/1500 , RESULT:FIRST CL	ASS WITH	DIS.	TINCI	ION												
B120053074 ISHAN TRIPATHI UMA ,71527257J ,PICT ,B120053074																	
CFM ·1																	
404181 VLSI	DESIGN & TECHNOLOGY TER NETWORKS WAVE ENGINEERING CAL IMAGE PROCESSING	PP 100	40	09	34	43	P	404189	MOBILE	COMMUNICATION						54	
404182 COMPU	TER NETWORKS	PP 100	40	18	32 45	50 67	P C	404190	BROADBA	AND COMMUNICATION IDEO ENGINEERIN					40 37	53 49	
404184A DIGITA	AL IMAGE PROCESSING	PP 100	40	13	35	48	P C	404191C	BIOMED	ICAL SIGNAL PRO	CESSING	PP 100	40	18	36	54	P
404185C PLCS 2	AND AUTOMATION	PP 100	40	15	29	44	P C	404193	LAB PRA	ACTICE III(MC &	BCS)	TW 50	20			33	P
404186 LAB PI 404186 LAB PI	RACTICE I (CN & MWE)	OR 50	20			3 <i>3</i> 35	P C	404193	LAB PRA	ACTICE III(MC &	BCS)	OR 50 TW 50	20			∠5 34	P P
404187 LAB PI	PRAC. II(VLSI &ELEC. I)	TW 50	20			27	P C	404194	LAB PRA	AC. IV(ELECTIVE	III)	PR 50	20			37	P
404187 LAB PI 404188 PROJEC	AL IMAGE PROCESSING AND AUTOMATION RACTICE I (CN & MWE) RACTICE I (CN & MWE) RAC. II(VLSI & ELEC. I) RAC. II(VLSI & ELEC. I) CT PHASE I	PR 50	20			25 21	P C	404195	PROJECT	F PHASE II		TW 100	40			75 33	P
404100 FROOE	CI FIIASE I	OK 50	20			31	PC	404193	FROUEC.	ICAL SIGNAL PROC ACTICE III(MC & ACTICE III(MC & AC. IV(ELECTIVE AC. IV(ELECTIVE I PHASE II I PHASE II		FK 50	20			33	r
GRAND TOTAL = 850	/1500 , RESULT:HIGHER SE	COND CLAS	SS														
В120053075 Д	ADHAV ASHRU BABAN			Α	SHA			,7152	7258G	,PICT	,B12005	3075	• • • •	• • • • •		• • • •	
SI	EM.:1									м.:2							
404181 VLSI I	DEM.:1 DESIGN & TECHNOLOGY TER NETWORKS WAVE ENGINEERING DED SYSTEMS & RTOS	PP 100	40	17	44	61	P C	404189		COMMUNICATION						72	
404182 COMPUT 404183 MICRO	WAVE ENGINEERING	PP 100 PP 100	40	29 21	5∠ 51	72	P C	404190 404191C		AND COMMUNICATION VIDEO ENGINEERIN						81 64	
404184B EMBEDI	DDED SYSTEMS & RTOS RONIC PRODUCT DESIGN	PP 100	40	22	51	73	РC	404192D	WIDELEG	SS METWORKS		DD 100	40	29	48	77	P
								404193 404193	LAB PRA	ACTICE III(MC &	BCS)	TW 50	20 20			44 32	P P
404186 LAB PI	RACTICE I (CN & MWE)	OR 50	20			32	P C	404194	LAB PRA	AC. IV(ELECTIVE	III)	TW 50	20			40	P
404187 LAB PI 404187 LAB PI	RAC. II(VLSI &ELEC. I)	TW 50	20			46	P C	404194 404195	LAB PRA	AC. IV(ELECTIVE	III)	PR 50	20			36	P P
404187 LAB PROJEC	RONIC PRODUCT DESIGN RACTICE I (CN & MWE) RACTICE I (CN & MWE) RAC. II(VLSI & ELEC. I) RAC. II(VLSI & ELEC. I) CT PHASE I	OR 50	20			44	P C	404195	PROJECT	ACTICE III(MC & ACTICE III(MC & ACTICE III(MC & AC. IV(ELECTIVE AC. IV(ELECTIVE I PHASE II I PHASE II		PR 50	20			40	P P
	4/1500 , RESULT:FIRST CL																

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 26														
NOTE: FI	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER														
B1200530	76 JADHAV PRASANNA YESHWANT			P(MANOC			,7152	7260J ,PICT ,B1200	53076					
	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I										40 40 40 20 20 20 20 20 20	22 26 15 29	50 53 47 45	72 79 62 74 46 43 45 39 92 42	P P P P P P P
GRAND TOTAL	= 11/2/1500 , RESULT:FIRST CI	JASS WITH	DIS	TINCL	LON										
B1200530	77 JADHAV SANJIVANI PRABHAKA	AR		P	HULABA	i		,7163	3618K ,PICT ,B1200	53077					
	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I														P P P P P P P
GRAND TOTAL	= 1134/1500 . RESULT:FIRST CI	LASS WITH	DIS	TINCT	ION										
B1200530	78 JAMBHALIKAR ADITYA RAVINI	DRA		S	NEHA			,7163	3619H ,PICT ,B1200	53078					• • • • •
404181 404182 404183 404184B 404185C 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20 20	15 24 26 21 21	52 59 47 47 53										
GRAND TOTAL	= 1133/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	ION										

GRAND TOTAL = 1133/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH DATE :	04 JUL 2018 CENTRE: PU	ICS &TELE NE INSTIT	COMU	OF CC	TON) MPUTEF	R TEC	CHNOLOG	SY, PUNE.	PAGE NO. 27		
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	IDAT	Έ, Μ	OTHER,	, PEF	RMANENT	REG. NO.	, PREVIOUS SEAT NO., COLLEGE, SEAT NO.		
OTHER LI	NES: HEAD OF PASSING, MAX. M.	ARKS, MIN	.PAS	S MAR	KS, IN	JT. M	MARKS,	TH. MARKS	, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:I	PREVIOUS CA	ARRY OVER
B1200530	79 JAMBHULKAR SAHIL SIDDHAR	гн гн		С	HHAYA		• • • • • •	,7152	,PICT ,B120053079	· • • • • • • • • • • • • • • • • • • •	
	SEM.:1								SEM.:2		
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	12	39	51	P C	404189	MOBILE COMMUNICATION PP 100 40	.9 47	66 P
404182 404183	MICROWAVE ENGINEERING	PP 100	40 40	24 22	3 Z 4 7	50 69	PC	404190 404191	AUDIO VIDEO ENGINEERING DD 100 40 7	11 48 19 39	69 P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	22	47	69	PC	404192D	WIRELESS NETWORKS PP 100 40 2	24 40	64 P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	17	41	58	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20		37 P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			35	РC	404193	LAB PRACTICE III(MC & BCS) OR 50 20		43 P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			39	PC	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20		41 P
404187	IAB PRAC. II(VLSI & ELEC. I)	IW 50	20			3 / 4 O	PC	404194	PROJECT PHASE IT TW 100 40		30 P 85 P
404188	PROJECT PHASE I	OR 50	20			44	PC	404195	MOBILE COMMUNICATION PP 100 40 BROADBAND COMMUNICATION SYS. PP 100 40 AUDIO VIDEO ENGINEERING PP 100 40 WIRELESS NETWORKS PP 100 40 LAB PRACTICE III(MC & BCS) TW 50 20 LAB PRACTICE III(MC & BCS) OR 50 20 LAB PRAC. IV(ELECTIVE III) TW 50 20 LAB PRAC. IV(ELECTIVE III) PR 50 20 PROJECT PHASE II TW 100 40 PROJECT PHASE II PR 50 20		40 P
GRAND TOTAL	$_{\rm I}$ = 1039/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	'ION						
B1200530	80 .TTGTSHA DATTI.		• • • •		Δ Δ. Τ Δ W.Τ.Τ			7152		, 	
	SEM.:1								SEM.:2		
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	20	48	68	P C	404189	MOBILE COMMUNICATION PP 100 40	23 52	75 P
404182	COMPUTER NETWORKS	PP 100	40	28	52 54	80 77	P C	404190 404191C	BROADBAND COMMUNICATION SYS. PP 100 40 2	11 48	69 P
404183 404184A	DIGITAL IMAGE PROCESSING	PP 100	40	23	5 1	80	PC	404191C	WIRELESS NETWORKS PP 100 40 2	28 40	68 P
404185C	PLCS AND AUTOMATION	PP 100	40	20	38	58	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20	.0 10	34 P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			33	РC	404193	LAB PRACTICE III(MC & BCS) OR 50 20		42 P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			37	P C	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20		34 P
404187	LAB PRAC. II(VLSI &ELEC. I)	J.M 20	20			33 45	PC	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20		39 P
404187	PROJECT PHASE T	OR 50	20			41	PC	404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 20 BROADBAND COMMUNICATION SYS. PP 100 40 20 AUDIO VIDEO ENGINEERING PP 100 40 20 WIRELESS NETWORKS PP 100 40 20 LAB PRACTICE III(MC & BCS) TW 50 20 LAB PRACTICE III(MC & BCS) OR 50 20 LAB PRAC. IV(ELECTIVE III) TW 50 20 LAB PRAC. IV(ELECTIVE III) PR 50 20 PROJECT PHASE II TW 100 40 PROJECT PHASE II PR 50 20		95 P 45 P
							- 0	101170	11.00201 1111.02 11		10 1
GRAND TOTAL	= 1117/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	CION						
	SEM.:1								SEM.:2		
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	13	36	49	P C	404189	MOBILE COMMUNICATION PP 100 40 2	22 45	67 P
404182	COMPUTER NETWORKS	PP 100	40	25	44	69 E 6	PC	404190	BROADBAND COMMUNICATION SYS. PP 100 40	19 47 27 42	66 P
404183 404184A	DIGITAL IMAGE PROCESSING	PP 100	40	20	5 / 5 1	71	PC	404191C	WIRELESS NETWORKS PP 100 40 2	19 38	57 P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	22	37	59	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20	., 50	43 P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			41	РC	404193	LAB PRACTICE III(MC & BCS) OR 50 20		37 P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			39	РC	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20		38 P
404187	LAB PRAC. II(VLSI &ELEC. I)	.T.M 20	20			41	P C	404194 40410E	LAB PRAC. IV(ELECTIVE III) PR 50 20		36 P
404188	PROJECT PHASE I	OR 50	20			38	P C	404195	WIRELESS NETWORKS		43 P
						23	- 0	_0110	SEM.:2 MOBILE COMMUNICATION PP 100 40 20 20 20 PROJECT PHASE II PP 100 40 20 20 20 20 20 20 20 20 20 20 20 20 20		-
GRAND TOTAL	$_{\perp}$ = 1045/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	.TON						

GRAND TOTAL = 1045/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 28													
NOTE: FIRST LINE : SEAT NO., NAME OF	THE CANDIDATE,	MOTHER, PERMANENT	reg. No., previous seat no., collec	E, SEAT NO.									
OTHER LINES: HEAD OF PASSING, MAX. N			TH. MARKS, TOTAL MARKS OBTAINED, P/F:										
B120053083 JOSHI BHAVESH DILIP		SHARADA	,71527274J ,PICT ,B1200	53083									
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185C PLCS AND AUTOMATION	PP 100 40 11 PP 100 40 26 PP 100 40 18 PP 100 40 12 PP 100 40 18	1 31 42 P C 6 36 62 P C 8 36 54 P C 2 41 53 P C 8 28 46 P C	SEM.:2 404189 MOBILE COMMUNICATION 404190 BROADBAND COMMUNICATION SYS. 404191C AUDIO VIDEO ENGINEERING 404192D WIRELESS NETWORKS 404193 LAB PRACTICE III (MC & BCS)	PP 100 40 19 43 62 P PP 100 40 23 44 67 P PP 100 40 22 42 64 P PP 100 40 23 37 60 P TW 50 20 28 P									
404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	OR 50 20 TW 50 20 PR 50 20 OR 50 20	34 P C 35 P C 35 P C 34 P C 36 P C	404192 WIRELESS NEIWORKS 404193 LAB PRACTICE III(MC & BCS) 404193 LAB PRACTICE III(MC & BCS) 404194 LAB PRAC. IV(ELECTIVE III) 404194 LAB PRAC. IV(ELECTIVE III) 404195 PROJECT PHASE II 404195 PROJECT PHASE II	TW 50 20 35 P PR 50 20 40 P TW 100 40 88 P PR 50 20 42 P									
GRAND TOTAL = 942/1500 , RESULT:FIRST CI	LASS												
B120053084 JOSHI CHAITANYA SHRIKAN		SUNITA	,71300979K ,PICT ,B1200	53084									
B120053084 JOSHI CHAITANYA SHRIKANT SUNITA ,71300979K ,PICT ,B120053084 SEM.:1													
404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS	PP 100 40 03 PP 100 40 03 PP 100 40 07 PP 100 40 08 PP 100 40 05 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20 OR 50 20	3 44 47 P 39 39 58 P C 7 45 52 P 8 36 44 P C 5 40 45 P 38 P C 28 P C 28 P C 43 P C 37 P C	404189 MOBILE COMMUNICATION 404190 BROADBAND COMMUNICATION SYS. 404191C AUDIO VIDEO ENGINEERING	PP 100 40 20 40 60 P									
GRAND TOTAL = 911/1500 . RESULT:FIRST CI	ASS												
B120053085 JOSHI RADHA VINAY		JYOTI	,71527275G ,PICT ,B1200	53085									
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I GRAND TOTAL = 931/1500 , RESULT:FIRST CI	TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	8 33 41 P C 9 48 67 P C 4 34 48 P C 8 49 57 P C 8 41 49 P C 37 P C 36 P C 37 P C 32 P C 42 P C	SEM.:2 404189 MOBILE COMMUNICATION 404190 BROADBAND COMMUNICATION SYS. 404191C AUDIO VIDEO ENGINEERING 404192D WIRELESS NETWORKS 404193 LAB PRACTICE III(MC & BCS) 404194 LAB PRAC. IV(ELECTIVE III) 404194 LAB PRAC. IV(ELECTIVE III) 404195 PROJECT PHASE II 404195 PROJECT PHASE II	PP 100 40 12 42 54 P PP 100 40 13 28 41 P PD 100 40 25 33 58 P									
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BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY	, PUNE. PAGE NO. 29
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, T	REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. "H. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
B120053086 JOSHI SHISHIR ASHISH PRATIBHA	,71633622н ,РICT ,B120053086
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 12 30 42 P C 404182 COMPUTER NETWORKS PP 100 40 27 45 72 P C 404183 MICROWAVE ENGINEERING PP 100 40 22 45 67 P C 404184A DIGITAL IMAGE PROCESSING PP 100 40 14 53 67 P C 404185D ARTIFICIAL INTELLIGENCE PP 100 40 10 42 52 P C 404186 LAB PRACTICE I (CN & MWE) TW 50 20 44 P C 404186 LAB PRACTICE I (CN & MWE) OR 50 20 43 P C 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 36 P C 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 40 P C 404188 PROJECT PHASE I OR 50 20 43 P C	SEM.:2 404189 MOBILE COMMUNICATION PP 100 40 20 42 62 P 404190 BROADBAND COMMUNICATION SYS. PP 100 40 17 39 56 P 404191D SOFT COMPUTING PP 100 40 15 35 50 P 404192D WIRELESS NETWORKS PP 100 40 27 40 67 P 404193 LAB PRACTICE III(MC & BCS) TW 50 20 42 P 404193 LAB PRACTICE III(MC & BCS) OR 50 20 40 P 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 26 P 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 40 P 404195 PROJECT PHASE II TW 100 40 95 P 404195 PROJECT PHASE II PR 50 20 45 P
GRAND TOTAL = 1029/1500 , RESULT:FIRST CLASS WITH DISTINCTION	
B120053087 JUNAID S PATEL MEHNAZ	,71527278M ,PICT ,B120053087
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 14 57 71 P C 404182 COMPUTER NETWORKS PP 100 40 22 39 61 P C 404183 MICROWAVE ENGINEERING PP 100 40 09 42 51 P C 404184A DIGITAL IMAGE PROCESSING PP 100 40 19 50 69 P C 404185C PLCS AND AUTOMATION PP 100 40 12 40 52 P C 404186 LAB PRACTICE I (CN & MWE) TW 50 20 37 P C 404186 LAB PRACTICE I (CN & MWE) OR 50 20 37 P C 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 35 P C 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 30 P C 404188 PROJECT PHASE I OR 50 20 31 P C	SEM.:2 404189 MOBILE COMMUNICATION PP 100 40 13 38 51 P 404190 BROADBAND COMMUNICATION SYS. PP 100 40 09 44 53 P 404191C AUDIO VIDEO ENGINEERING PP 100 40 24 39 63 P 404192D WIRELESS NETWORKS PP 100 40 20 43 63 P 404193 LAB PRACTICE III(MC & BCS) TW 50 20 38 P 404193 LAB PRACTICE III(MC & BCS) OR 50 20 39 P 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 34 P 404195 PROJECT PHASE II TW 100 40 95 P 404195 PROJECT PHASE II PR 50 20 45 P
GRAND TOTAL = 987/1500 RESULT: FIRST CLASS	
B120053088 KADAM SHIVANJALI RAJENDRA SUNITA	,71633623F ,PICT ,B120053088
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 16 51 67 P C 404182 COMPUTER NETWORKS PP 100 40 27 37 64 P C 404183 MICROWAVE ENGINEERING PP 100 40 20 48 68 P C 404184A DIGITAL IMAGE PROCESSING PP 100 40 24 47 71 P C 404185C PLCS AND AUTOMATION PP 100 40 23 32 55 P C 404186 LAB PRACTICE I (CN & MWE) TW 50 20 41 P C 404186 LAB PRACTICE I (CN & MWE) OR 50 20 45 P C 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 40 P C 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 38 P C 404188 PROJECT PHASE I OR 50 20 45 P C	SEM.:2 404189 MOBILE COMMUNICATION PP 100 40 20 38 58 P 404190 BROADBAND COMMUNICATION SYS. PP 100 40 23 42 65 P 404191C AUDIO VIDEO ENGINEERING PP 100 40 16 42 58 P 404192D WIRELESS NETWORKS PP 100 40 25 38 63 P 404193 LAB PRACTICE III(MC & BCS) TW 50 20 41 P 404194 LAB PRACTICE III(MC & BCS) OR 50 20 40 P 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 39 P 404195 PROJECT PHASE II TW 100 40 96 P 404195 PROJECT PHASE II PR 50 20 47 P
GRAND TOTAL = 1081/1500 , RESULT:FIRST CLASS WITH DISTINCTION	

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH DATE :	04 JUL 2018 CENTRE: PU	ICS &TELE NE INSTIT	COMU UTE	OF CO	MPUTEF	R TEC	HNOLOG	SY, PUNE.	PAGE NO. 30				
NOTE: FI	DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 30 NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER												
						. .				••			
B1200530	89 KADAM SHRADDHA SHALIGRAM			ľ	MANISHA	A		,7163	3624D ,PICT ,B120053089				
	SEM.:1								SEM.:2				
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	13	55	68	P C	404189	MOBILE COMMUNICATION PP 100 40 25 47 72 P				
404182 404183	COMPUTER NETWORKS	PP 100	40 40	26	48 52	74 72	PC	404190 404191	BROADBAND COMMUNICATION SYS. PP 100 40 25 55 80 P				
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	14	54	68	PC	404191C	WIRELESS NETWORKS PP 100 40 24 47 71 P				
404185D	ARTIFICIAL INTELLIGENCE	PP 100	40	18	48	66	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20 44 P				
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			39	P C	404193	LAB PRACTICE III(MC & BCS) OR 50 20 34 P				
404186 404187	LAB PRACTICE I (CN & MWE)	OR 50	20			36 40	PC	404194 404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 40 P				
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			40	PC	404195	PROJECT PHASE II TW 100 40 88 P				
404188	PROJECT PHASE I	OR 50	20			40	РC	404195	MOBILE COMMUNICATION PP 100 40 25 47 72 P BROADBAND COMMUNICATION SYS. PP 100 40 25 55 80 P AUDIO VIDEO ENGINEERING PP 100 40 26 50 76 P WIRELESS NETWORKS PP 100 40 24 47 71 P LAB PRACTICE III(MC & BCS) TW 50 20 44 P LAB PRACTICE III(MC & BCS) OR 50 20 40 P LAB PRAC. IV(ELECTIVE III) TW 50 20 40 P LAB PRAC. IV(ELECTIVE III) PR 50 20 88 P PROJECT PHASE II TW 100 40 88 P PROJECT PHASE II PR 50 20 40 P				
CDAND TOTAL	_ = 1127/1500 , RESULT:FIRST C	אככ אודדם	י חדס	ייידאוכיי	rt ON								
GRAND TOTAL		SEAL											
B1200530	90 KAKADE ROSHANI DATTU			ľ	INAKSH	ΙΙ		,7163	3625B ,PICT ,B120053090				
B120053090 KAKADE ROSHANI DATTU MINAKSHI ,71633625B ,PICT ,B120053090 SEM.:1													
404181	VIST DESIGN & TECHNOLOGY	PP 100	40	17	52	69	РC	404189	SEM.: 2 MORTLE COMMINICATION DD 100 40 23 47 70 D				
404182	COMPUTER NETWORKS	PP 100	40	19	44	63	PC	404190	BROADBAND COMMUNICATION SYS. PP 100 40 26 51 77 P				
404183	MICROWAVE ENGINEERING	PP 100	40	09	55	64	P C	404191C	AUDIO VIDEO ENGINEERING PP 100 40 23 46 69 P				
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	16	49	65	P C	404192D	WIRELESS NETWORKS PP 100 40 28 40 68 P				
404185C 404186	PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE)	PP 100	20	Ι/	45	6∠ 41	P C	404193 404193	LAB PRACTICE III (MC & BCS) TW 50 Z0 40 P				
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			40	PC	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 32 P				
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			35	РC	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20 33 P				
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			23	P C	404195	PROJECT PHASE II TW 100 40 80 P				
404188	PROJECT PHASE I	OR 50	20			38	РC	404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 23 47 70 P BROADBAND COMMUNICATION SYS. PP 100 40 26 51 77 P AUDIO VIDEO ENGINEERING PP 100 40 23 46 69 P WIRELESS NETWORKS PP 100 40 28 40 68 P LAB PRACTICE III(MC & BCS) TW 50 20 40 P LAB PRACTICE III(MC & BCS) OR 50 20 30 P LAB PRAC. IV(ELECTIVE III) TW 50 20 32 P LAB PRAC. IV(ELECTIVE III) PR 50 20 33 P PROJECT PHASE II TW 100 40 80 P PROJECT PHASE II PR 50 20 32 P				
GRAND TOTAL	L = 1031/1500 , RESULT:FIRST C	LASS WITH	DIS	TINC	CION								
-1000500													
B1200530	191 KALE UMA PRAFULLCHANDRA			I	ARCHANA	Ā		,7152	7284F ,PICT ,B120053091				
	SEM.:1								SEM.:2				
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	17	61	78	РC	404189	MOBILE COMMUNICATION PP 100 40 29 52 81 P				
404182	COMPUTER NETWORKS	PP 100	40	27	49	76	P C	404190	BROADBAND COMMUNICATION SYS. PP 100 40 26 53 79 P AUDIO VIDEO ENGINEERING PP 100 40 24 56 80 P				
404183 4041842	MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING	PP 100	40 40	24 28	48 61	/ Z 8 9	P C	404191C 404192D	AUDIO VIDEO ENGINEERING PP 100 40 24 56 80 P WIRELESS NETWORKS PP 100 40 29 47 76 P				
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	24	46	70	PC	404193	LAB PRACTICE III(MC & BCS) TW 50 20 42 P				
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			43	РC	404193	LAB PRACTICE III(MC & BCS) OR 50 20 44 P				
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			40	P C	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 43 P				
404187 404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			33 16	P C	404194 404195	LAB PRAC. IV(ELECTIVE III) PR 50 20 40 P				
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			44	P C	404195	WIRELESS NEIWORKS				
	= 1219/1500 , RESULT:FIRST C												

GRAND TOTAL = 1219/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH: 71-B.E. (2012 PAT.) (ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PINE INSTITUTE OF COMPUTER TECHNOLOGY, PINE.

DATE :	04 JUL 2018 CENTRE: PUI	NE INSTIT	UTE	OF C	OMPUTER	TEC	HNOLOG	Y, PUNE.	PAG	SE NO. 3	31					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	IDAT	E,	MOTHER,	PER	MANENT	REG. NO.	, PREVIOUS SEAT NO.,	COLLEGE	E, SEAT	NO.				
	NES: HEAD OF PASSING, MAX. M.															
B1200530	92 KAMBLE KAJAL VIJAY			• • • •	GEETA	• • • •		,7163	3626L ,PICT	,B12005	53092					
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	11	45	56	РC	404189	MOBILE COMMUNICATION		PP 100	40	22	42	64	P
404182	COMPUTER NETWORKS	PP 100	40	25	36	61	РC	404190	BROADBAND COMMUNICATIO	ON SYS.	PP 100	40	28	43	71	P
404183	MICROWAVE ENGINEERING	PP 100	40	21	45	66	РC	404191C	AUDIO VIDEO ENGINEERIN	1G	PP 100	40	20	43	63	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	12	42	54	РC	404192D	WIRELESS NETWORKS		PP 100	40	25	44	69	P
404185C	PLCS AND AUTOMATION	PP 100	40	18	45	63	РC	404193	LAB PRACTICE III(MC &	BCS)	TW 50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			42	РC	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			24	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			35	РC	404194	LAB PRAC. IV(ELECTIVE	III)	TW 50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			36	PC	404194	LAB PRAC. IV(ELECTIVE	III)	PR 50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			32	P C	404195	PROJECT PHASE II		TW 100	40			84	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			41	PС	404195	PROJECT PHASE II		PR 50	20			38	P
CDAND TOTAL	= 1019/1500 , RESULT:FIRST C	ו אפפ שדיים	י חדכ	ייידאור	'T'T ()NI											
GRAND TOTAL	- 1019/1500 , RESULITERSI C.	LASS WIID	נענו) I IIIC	LITON											
B1200530	93 KAMBLE PRAJAKTA SHRIMANT				SANGEET	· · · · Δ		7152	72871, PTCT	B12005						
D1200330					DIMODEL	21		, , 152	72071 ,1101	, D12000	33073					
	SEM.:1								SEM.:2							
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN	PP 100	40	09	45	54	РC	404189	MOBILE COMMUNICATION		PP 100	40	17	30	47	P
404182	COMPUTER NETWORKS	PP 100	40	20	33	53	РC	404190	BROADBAND COMMUNICATIO	ON SYS.	PP 100	40	22	28	50	P
404183	MICROWAVE ENGINEERING	PP 100	40	16	32	48	РC	404191C	AUDIO VIDEO ENGINEERIN	1G	PP 100	40	13	37	50	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	15	39	54	РC	404192D	WIRELESS NETWORKS		PP 100	40	18	40	58	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	18	31	49	РC	404193	LAB PRACTICE III(MC &	BCS)	TW 50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			37	РC	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			31	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			38	РC	404194	LAB PRAC. IV(ELECTIVE	III)	TW 50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			40	РC	404194	LAB PRAC. IV(ELECTIVE	III)	PR 50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			34	РC	404195	PROJECT PHASE II		TW 100	40			88	P
404188	ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			38	РC	404195	LAB PRACTICE III(MC & LAB PRACTICE III(MC & LAB PRAC. IV(ELECTIVE LAB PRAC. IV(ELECTIVE PROJECT PHASE II PROJECT PHASE II		PR 50	20			42	P
GRAND TOTAL	= 915/1500 , RESULT:FIRST CL	ASS														
B1200530	94 KAMBLE RAJAT JEETENDRA		• • • •	• • • •	RESHMA			,7152		,B12005	 53094					• • • • •
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	12	33	45	РC	404189	MOBILE COMMUNICATION		PP 100	40	22		62	
404182	COMPUTER NETWORKS	PP 100	40	18	35	53	PC	404190	BROADBAND COMMUNICATIO						73	
404183	COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION	PP 100	40	15	41	56	P C	404191C	AUDIO VIDEO ENGINEERIN	NG	PP 100	40	14 15	40	54	Ъ
404184A	DIGITAL IMAGE PROCESSING	PD 100	40	2T	43	64	ъ с	404192D	WIKELESS NETWORKS	Daa)	55 TOO	40	15	38	53	P
404185C 404186	PLCS AND AUTOMATION	LTM EV	40	13	3 L	44	b C	404193	LAB PRACTICE III (MC &	BCS)	T.M 20	20			34 26	P
404186	TAD DDACTICE I (CN & MME)	TM 20	20			44 21	D C	404193	TAD DDAG TW/FT FOTTWE	TTT /	טול אט דישו בח	20			30 22	D F
404186	LAD PRACILCE I (CN & MWE)	טול אָט	20			22	P C	404194	IND DDAC IV (ELECTIVE	TTT)	TM 20	20			33	D D
404187	TAR DRAC II (VIOI GELEC, I)	DB 20	20			3 <i>3</i>	D C	404194	DBULEGA DHVGE 11	TTT /	FIX 30	⊿ 0 4 ∩			9 U	D D
404188	LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			35	D C	404195	AUDIO VIDEO ENGINEERIN WIRELESS NETWORKS LAB PRACTICE III(MC & LAB PRACTICE III(MC & LAB PRAC. IV(ELECTIVE LAB PRAC. IV(ELECTIVE PROJECT PHASE II PROJECT PHASE II		PR 50	20			42	Þ
101100	11.00 201 11111011 1	510 50	20			55	1 0	10110	11.00 201 1111101 11		110 30	20			12	_
GRAND TOTAL	= 957/1500 , RESULT:FIRST CL	ASS														

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: P	NICS &TELECOMUN UNE INSTITUTE O	TCATION) F COMPUTER T	rechnology, pun	E. PAGE NO.	32
NOTE: FIRST LINE : SEAT NO., NAME O	F THE CANDIDATE	, MOTHER, P	PERMANENT REG.	NO., PREVIOUS SEAT NO., COLLEG	E, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX.					
B120053095 KANDGE MANDAR SANJAY		JYOTI	,7	1527291J ,PICT ,B1200	53095
SEM.:1				SEM.:2	
404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS	PP 100 40	08 32 4	10 PC 40418	9 MOBILE COMMUNICATION SYS	PP 100 40 13 33 46 P PP 100 40 23 45 68 P
404183 MICROWAVE ENGINEERING	PP 100 40 PP 100 40	13 33 4	16 PC 40419	1C AUDIO VIDEO ENGINEERING	PP 100 40 23 45 66 P PP 100 40 13 39 52 P
404184A DIGITAL IMAGE PROCESSING	PP 100 40	19 39 5	58 P C 40419	0 BROADBAND COMMUNICATION SYS. 1C AUDIO VIDEO ENGINEERING 2A BIOMEDICAL SIGNAL PROCESSING 3 LAB PRACTICE III(MC & BCS)	PP 100 40 14 37 51 P
404185B ELECTRONIC PRODUCT DESIGN 404186 LAB PRACTICE I (CN & MWE)	TW 50 20	12 33 4	15 PC 40419 20 PC 40419	3 LAB PRACTICE III(MC & BCS) 3 LAB PRACTICE III(MC & BCS)	OR 50 20 37 P
404186 LAB PRACTICE I (CN & MWE)	OR 50 20	2	28 P C 40419	4 LAB PRAC. IV(ELECTIVE III)	TW 50 20 37 P
404187 LAB PRAC. II(VLSI &ELEC. I) 404187 LAB PRAC. II(VLSI &ELEC. I)	TW 50 20 PR 50 20	2	27 P 40419 84 P 40419	4 LAB PRAC. IV(ELECTIVE III) 5 PROJECT PHASE II	PR 50 20 40 P
404188 PROJECT PHASE I	OR 50 20	3	36 P C 40419	ZA BIOMEDICAL SIGNAL PROCESSING LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50 20 44 P
GRAND TOTAL = 870/1500 , RESULT:HIGHER	SECOND CLASS				
B120053096 KANJIA ANERI PARESHKUMA			<u>.</u>		
B120053096 KANJIA ANERI PARESHKUMA	R	SHAILA	, 7	1527293E ,PICT ,B1200	o3096
SEM.:1				SEM.:2	
404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185B ELECTRONIC PRODUCT DESIGN	PP 100 40	12 59 7 26 44 7	71 PC 40418 70 PC 40419	9 MOBILE COMMUNICATION 0 BROADBAND COMMUNICATION SYS.	
404183 MICROWAVE ENGINEERING	PP 100 40	24 48 7	72 P C 40419	1C AUDIO VIDEO ENGINEERING 2D WIRELESS NETWORKS	
404184A DIGITAL IMAGE PROCESSING 404185B ELECTRONIC PRODUCT DESIGN	PP 100 40	18 58 7 20 35 5	76 PC 40419 55 PC 40419	2D WIRELESS NETWORKS 3 IAB DRACTICE III(MC & BCS)	PP 100 40 29 38 67 P
404186 LAB PRACTICE I (CN & MWE)	TW 50 20	4	13 P C 40419	3 LAB PRACTICE III(MC & BCS)	OR 50 20 29 P
404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I)	OR 50 20	4	16 PC 40419	4 LAB PRAC. IV(ELECTIVE III)	TW 50 20 37 P
404187 LAB PRAC. II(VLSI &ELEC. I) 404187 LAB PRAC. II(VLSI &ELEC. I)	PR 50 20	4	13 P C 40419 13 P C 40419	5 PROJECT PHASE II	TW 100 40 92 P
404185B ELECTRONIC PRODUCT DESIGN 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	OR 50 20	4	14 P C 40419	5 PROJECT PHASE II	TW 50 20 42 P OR 50 20 29 P TW 50 20 37 P PR 50 20 40 P TW 100 40 92 P PR 50 20 43 P
GRAND TOTAL = 1118/1500 , RESULT:FIRST	CLASS WITH DIST	INCTION			
B120053097 KARAN JUNEJA		NEELAM			
SEM.:1				SEM.:2	
404181 VLSI DESIGN & TECHNOLOGY	PP 100 40	09 49 5	58 P C 40418	9 MOBILE COMMUNICATION	
404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING	PP 100 40	28 28 5 11 45 5	56 PC 40419 56 PC 40419	0 BROADBAND COMMUNICATION SYS. 1C AUDIO VIDEO ENGINEERING	
404184B EMBEDDED SYSTEMS & RTOS	PP 100 40	16 42 5	58 P C 40419	2D WIRELESS NETWORKS	ם 62 אם 100 מת
404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE)	PP 100 40	13 31 4	14 PC 40419	3 LAB PRACTICE III(MC & BCS)	TW 50 20 34 P OR 50 20 38 P
404186 LAB PRACTICE I (CN & MWE)	OR 50 20	4	10 PC 40419	4 LAB PRACTICE ITI(MC & BCS)	TW 50 20 30 P
404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I)	TW 50 20	3	32 P C 40419	4 LAB PRAC. IV(ELECTIVE III)	PR 50 20 36 P TW 100 40 96 P
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	OR 50 20	4	18 P C 40419	LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50 20 47 P
GRAND TOTAL = 968/1500 , RESULT:FIRST C					

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PU	JNE INSTITUTE	OF COMPUTER	TECHNOLOG	GY, PUNE.	PAGE NO.	33	
NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, MAX. N	F THE CANDIDAT: MARKS, MIN.PAS	E, MOTHER, S MARKS, IN	PERMANENT IT. MARKS,	reg. No. Th. MARKS	., PREVIOUS SEAT NO., COLLEG S, TOTAL MARKS OBTAINED, P/F:	GE, SEAT NO. PASS/FAIL, C:PREV	IOUS CARRY OVER
B120053098 KARPE ANJALI SHRIKANT		ARCHANA		,7163	33627J ,PICT ,B1200	053098	
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	14 57 29 40 21 44 17 55 22 50	71 P C 69 P C 72 P C 72 P C 45 P C 36 P C 42 P C 44 P C 38 P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 25 PP 100 40 22 PP 100 40 27 PP 100 40 26 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	44 69 P 51 73 P 56 83 P 41 67 P 43 P 24 P 32 P 38 P 92 P 45 P
GRAND TOTAL = 1120/1500 , RESULT:FIRST C	CLASS WITH DIS	TINCTION					
B120053099 KASTURE DEEPALI VINAYAK		MANISHA			27302H ,PICT ,B1200)53099	
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	13 47 22 32 05 44 16 44 10 37	60 P C 54 P C 49 P C 60 P C 47 P C 42 P C 42 P C 41 P C 34 P C 42 P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACT. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 16 PP 100 40 19 PP 100 40 22 PP 100 40 16 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	23# 39# P 30 49 P 42 64 P 39 55 P 40 P 29 P 40 P 38 P 94 P 45 P
GRAND TOTAL = 964/1500 , RESULT:FIRST CI	LASS [# 0.4]						
B120053100 KATIKAR VIKRANT RAJEEV		SUNITA		,7152	27305B ,PICT ,B1200	53100	
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 PROJECT PHASE I	PP 100 40 PP 100 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	17 60 28 46 20 50 22 59 16 50	77 P C 74 P C 70 P C 81 P C 66 P C 37 P C 42 P C 38 P C 38 P C 45 P C	404189 404190 404191C 404192D 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 23 PP 100 40 28 PP 100 40 21 PP 100 40 29 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	45 68 P 49 77 P 60 81 P 47 76 P 37 P 36 P 42 P 37 P 85 P 40 P
GRAND TOTAL = 1147/1500 , RESULT:FIRST C	CLASS WITH DIS	TINCTION					

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

OTHER LINES: HEAD	SEAT NO., NAME OF OF PASSING, MAX. MA	THE ARKS,	CAND MIN	IDAT .PAS	E, M	OTHER,	PER	MANENT	REG. NO.	PREVI	OUS SEAT NO	COLLEGE	SEAT	· NO.	• • • • •			
B120053101 KAUSH			MIN	.PAS	SMAR			7 D T C C	THE MAN DIE	,	MARKE ORBATATER	D/E.D	7 00 / 03 77		. DD 117	TOTTO /	, , D D ;	. 01781
B120053101 KAUSH						NO, IN	1.T. • IA	ARKS,	TH. MARKS	, TOTAL	MARKS OBTAINED	, P/F:P	ASS/FAIL	ı, C	: PREV	1008 (:ARR)	. OVER
SEM.:					P	AMY			,7152	7306L	,PICT	,B12005	3101					
404181 VI.ST DEST	1									SEM	1.:2							
TO THOSE VEDE DEDI	GN & TECHNOLOGY	PP	100	40	16	50	66	РC	404189	MOBILE	COMMUNICATION		PP 100	40	23	45	68	P
404182 COMPUTER	NETWORKS	PP	100	40	27	40	67	РC	404190	BROADBA	AND COMMUNICATION	ON SYS.	PP 100	40	28	48	76	P
404183 MICROWAVE	ENGINEERING	PP	100	40	25	51	76	P C	404191C	AUDIO V	/IDEO ENGINEERIN	1G	PP 100	40	22	54	76	P
404184A DIGITAL I	MAGE PROCESSING	PP	100	40	22	51 47	/3	PC	404192A	BIOMED1	CAL SIGNAL PROC	JESSING	DE 100	40	26	45	/ <u>T</u>	Ь
404185C PLCS AND .	AUIOMAIION	PP	T00	20	22	4 /	46	PC	404193	LAB PRA	ACTICE III (MC &	BCS)	1M 20	20			38	P
404100 LAB PRACI	ICE I (CN & MWE)	O.D.	50	20			40	P C	404193	LAB PRA	ACIICE III(MC &	BCS)	UR 50	20			12	P D
404180 HAB FRACT	TT/VIOT SELEC T)	יעה.	50	20			36	D C	404194	TAB DDZ	AC. IV(EDECIIVE	TTT)	DP 50	20			40	D D
404187 LAB PRAC	TI(VIST &ELEC. I)	DR	50	20			47	D C	404195	DROJECT	P DHACE IT	/	TW 100	40			90	D
404188 PROJECT P	HASE I	OR	50	20			39	PC	404195	PROJECT	PHASE II		PR 50	20			42	P
GRAND TOTAL = 1141/15	00 , RESULT:FIRST CL	LASS	WITH	DIS	TINCT	ION												
B120053103 KHADE	YASHWANT BHASKAR		• • • •		 R	 ESHMA			.7152	 7310J	,PICT	 .B12005	3103	• • • •	• • • • •	• • • • •		• • • • •
												,						
SEM.:		DD	100	4.0	1.0	47	6 F	D G	404100	SEN	1.:2		DD 100	4.0	0.5	2.0	63	Б
404181 VLSI DESI	JN & TECHNOLOGY	PP	100	40	7.R	4 /	65	PC	404189	MORITE	COMMUNICATION AND COMMUNICATION					38 54		
404162 COMPUTER .	NEIWORKS	DD	100	40	20	3 <u>4</u>	5/	P C	404190	BRUADBA	AND COMMONICALL	JN SIS.	PP 100	40	20	52		
404163 MICKOWAVE	SAGLING & DAUG	DD	100	40	20	16	66	P C	4041910	MIDELE C	/IDEO ENGINEERIN SS NETWORKS	NG	DD 100	40	20	3 5	63	D
404185C DLCS AND	AUTOMATION	DD	100	40	19	45	64	D C	4041920	T.AR DRZ	ACTICE III(MC &	RCS)	TW 50	20	20	33	41	D
404186 LAB PRACT	TOE T (CN & MWE)	TW	50	20	10	13	35	PC	404193	LAB PRA	ACTICE III(MC &	BCS)	OR 50	20			40	P
404186 LAB PRACT	ICE I (CN & MWE)	OR	50	20			41	PC	404194	LAB PRA	C. TV(ELECTIVE	TTT)	TW 50	20			41	P
404187 LAB PRAC.	II(VLSI &ELEC. I)	TW	50	20			37	PC	404194	LAB PRA	AC. IV(ELECTIVE	III)	PR 50	20			39	P
404187 LAB PRAC.	II(VLSI &ELEC. I)	PR	50	20			37	PC	404195	PROJECT	PHASE II	,	TW 100	40			90	P
404188 PROJECT P	1 GN & TECHNOLOGY NETWORKS ENGINEERING SYSTEMS & RTOS AUTOMATION ICE I (CN & MWE) ICE I (CN & MWE) II(VLSI & ELEC. I) II(VLSI & ELEC. I) HASE I	OR	50	20			40	РC	404195	PROJECT	ACTICE III(MC & ACTICE III(MC & ACTICE III(MC & AC. IV(ELECTIVE AC. IV(ELECTIVE T PHASE II T PHASE II		PR 50	20			40	P
GRAND TOTAL = 1071/15	00 , RESULT:FIRST CL	LASS	WITH	DIS	TINCT	ION												
В120053104 КНАМА	MKAR RACHANA NITIN		• • • •		D	EEPA			,7152	7311G	,PICT	,B12005	3104			• • • • •		• • • • •
SEM.:	1									SEM	1.:2							
404181 VLSI DESI	GN & TECHNOLOGY	PP	100	40	18	58	76	РC	404189	MOBILE	COMMUNICATION		PP 100	40	20		76	
404182 COMPUTER	NETWORKS	PP	100	40	27	43	70	РC	404190	BROADBA	AND COMMUNICATION	ON SYS.	PP 100	40	24		80	_
404183 MICROWAVE	ENGINEERING	PP	100	40	25	45	70	РC	404191C	AUDIO V	/IDEO ENGINEERIN	1G	PP 100	40	23	61		
404184B EMBEDDED	SYSTEMS & RTOS	PP	100	40	24	60	84	РC	404192D	WIRELES	SS NETWORKS		PP 100	40	25	41	66	P
404185B ELECTRONI	I GN & TECHNOLOGY NETWORKS ENGINEERING SYSTEMS & RTOS C PRODUCT DESIGN	PP	T00	40	20	52	72	PC	404193	LAB PRA	ACTICE III(MC &	BCS)	TW 50	20			43	P
404186 LAB PRACT	LCE I (CN & MWE)	J.M	50	20			48	P C	404193	LAB PRA	ACTICE III(MC &	BCS)	OR 50	20			31	Ь
404186 LAB PRACT	TCE T (CN & WME)	OR	50	20			45	ь с	404194	LAB PRA	AC. IV(ELECTIVE	TTT)	T.M 20	20			38 40	P
404187 LAB PRAC. 404187 LAB PRAC.	II(VLSI &ELEC. I)	J.M	50	∠U			46	ь с	404194	LAB PRA	AC. IV(ELECTIVE	ттт)	PK 50	∠U 40			40 94	P.
404187 LAB PRAC. 404188 PROJECT P	C PRODUCT DESIGN ICE I (CN & MWE) ICE I (CN & MWE) II(VLSI &ELEC. I) II(VLSI &ELEC. I) HASE I	OR	50 50	⊿0 20			39 44	P C	404195 404195	PROJECI	ACTICE III(MC & ACTICE III(MC & ACTICE III(MC & AC. IV(ELECTIVE AC. IV(ELECTIVE PHASE II		TW 100	20			94 43	P P
GRAND TOTAL = 1189/15								- 0	-01223	_ 1.00 201			_1. 50				-3	-

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PUNE	E INSTITUTE	OF COMPUTE	R TECHNOLOG	GY, PUNE.	PAGE NO.	35	
NOTE: FIRST LINE : SEAT NO., NAME OF TO OTHER LINES: HEAD OF PASSING, MAX. MAR	THE CANDIDAT RKS, MIN.PAS	TE, MOTHER SS MARKS, II	, PERMANENT NT. MARKS,	reg. No. Th. Marks	, PREVIOUS SEAT NO., COLLEG , TOTAL MARKS OBTAINED, P/F:	E, SEAT NO. PASS/FAIL, C:PR	EVIOUS CARRY OVER
B120053105 KHATRI GEETA HARISH		KAMLA		,7152		53105	
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 OR 50 20 OR 50 20 OR 50 20	15 46 24 39 16 48 25 38 20 37	61 P C 63 P C 64 P C 63 P C 57 P C 46 P C 45 P C 41 P C 46 P C 38 P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 15 PP 100 40 27 PP 100 40 18 PP 100 40 19 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	50 65 P 51 78 P 47 65 P 34 53 P 40 P 40 P 45 P 46 P 87 P 42 P
GRAND TOTAL = 1085/1500 , RESULT:FIRST CLA	ASS WITH DIS	STINCTION					
B120053106 KOKATE PUJA SURESH		INDUBA		 7163,	33628G ,PICT ,B1200	53106	
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 TW 50 20 PR 50 20 OR 50 20 OR 50 20	13 53 29 36 22 40 21 52 21 43	66 P C 65 P C 62 P C 73 P C 64 P C 40 P C 40 P C 40 P C 41 P C	404189 4041910 404191C 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 19 PP 100 40 24 PP 100 40 18 PP 100 40 25 TW 50 20 OR 50 20 TW 50 20 TW 50 20 TW 100 40 PR 50 20	44 63 P 41 65 P 53 71 P 39 64 P 42 P 31 P 36 P 40 P 85 P 40 P
GRAND TOTAL = 1059/1500 , RESULT:FIRST CLA	ASS WITH DIS	STINCTION					
B120053107 KOKATE VIJAY SUNIL		VIMAL		,7140	0955F ,PICT ,B1200	53107	
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	12 49 21 36 07 43 18 51 20 42	61 P C 57 P C 50 P C 69 P C 62 P C 44 P C 32 P C 39 P C 35 P C 42 P C	404189 404190 404191C 404192A 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING BIOMEDICAL SIGNAL PROCESSING LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 18 PP 100 40 22 PP 100 40 20 PP 100 40 21 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	33 51 P 45 67 P 48 68 P 47 68 P 39 P 25 P 32 P 38 P 82 P 40 P
GRAND TOTAL = 1001/1500 , RESULT:FIRST CLA	ASS WITH DIS	STINCTION					

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BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCE DATE :	04 JUL 2018 CENTRE: PU	ICS &TELE NE INSTIT	COMU UTE	OF CO	ION) MPUTER	TEC	HNOLOG	Y, PUNE.	PAGE NO. 3	36					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	 TACI	 Е, М	OTHER,	PER	MANENT	REG. NO.	, PREVIOUS SEAT NO., COLLEGE	 E, SEAT	NO.				• • • • •
OTHER LI	NES: HEAD OF PASSING, MAX. M.	ARKS, MIN	I.PAS	S MAR	KS, IN	IT. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED, P/F:	PASS/FAIL	, C	::PREV	IOUS C	CARRY	OVER
B1200531	08 KONDE ATHARWA AJIT			S	ANGEET	'A		,7152	7324J ,PICT ,B12005	3108	• • • •				• • • • •
	SEM.:1								SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	05	39	44	P C	404189	MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING BIOMEDICAL SIGNAL PROCESSING LAB PRACTICE III(MC & BCS)	PP 100	40	16	32	48	P
404182	MICROWAVE ENGINEERING	PP 100	40	10	3 U 4 1	40 51	PC	404190 404191C	AUDIO VIDEO ENGINEERING	PP 100	40	⊿3 15	35 47	58 62	P P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	09	46	55	РC	404192A	BIOMEDICAL SIGNAL PROCESSING	PP 100	40	19	37	56	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	15	31	46	РC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			47	P C	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			25	P
404186 404187	LAB PRACTICE I (CN & MWE)	OR 50	∠0 20			3 U 4 4	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20 20			38 38	P D
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			25	PC	404195	PROJECT PHASE II	TW 100	40			94	P
404188	PROJECT PHASE I	OR 50	20			40	P C	404195	LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50	20			43	P
GRAND TOTAL	, = 932/1500 , RESULT:FIRST CL	ASS													
B1200531	09 KONDE ESHWARA AJIT			S	ANGEET	'A		,7152	7325G ,PICT ,B12005	53109				• • • •	
404181	SEM.:1 VIST DESIGN & TECHNOLOGY	PP 100	40	15	42	57	РC	404189	SEM.:2	PP 100	40	23	42	65	Þ
404182	COMPUTER NETWORKS	PP 100	40	23	29	52	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	25	47	72	P
404183	MICROWAVE ENGINEERING	PP 100	40	20	39	59	РC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	16	54	70	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	14	56	70	P C	404192A	BIOMEDICAL SIGNAL PROCESSING	PP 100	40	26	42	68	P
404185B 404186	LAR PRACTICE I (CN & MWE)	TW 50	20	19	34	38	PC	404193	LAB PRACTICE III (MC & BCS)	OR 50	20			28	P P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			40	PC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			44	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			35	P C	404195	PROJECT PHASE II	TW 100	40			94	P
404188	PROJECT PHASE I	OR 50	20			41	ЪС	404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING BIOMEDICAL SIGNAL PROCESSING LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50	20			44	Р
GRAND TOTAL	= 1051/1500 . RESULT:FIRST C	LASS WITH	DIS	TINCT	ION										
B1200531	10 KOTHEKAR BHAKTI SUNIL			C	HITRA			,7163	3629E ,PICT ,B12005	53110	• • • •				• • • • •
	SEM.:1								SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	17	54	71	РC	404189	MOBILE COMMUNICATION	PP 100	40	21	41	62	P
404182	COMPUTER NETWORKS	PP 100	40	28	40	68	PC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	27	55 55	82	P
404183 404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	20	60	80	PC	404191C 404192D	WIRELESS NETWORKS	PP 100 PP 100	40	27	35 45	72	P
404185C	PLCS AND AUTOMATION	PP 100	40	16	45	61	РC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20	_ ,	15	40	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			36	P C	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			45	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			38	P
404187 404187	LAB PRAC. II(VLSI &ELEC. I) LAB DRAC II(VLSI &ELEC I)	DB 50	∠0 20			4 L 4 O	ъс ъс	404194 404195	LAB PKAC. IV(ELECTIVE III) DROJECT DHASE II	PK 50 판W 100	∠U 4∩			43 94	D F
404188	PROJECT PHASE I	OR 50	20			43	PC	404195	PROJECT PHASE II	PR 50	20			44	P
GRAND TOTAL	= 1148/1500 , RESULT:FIRST C	LASS WITH	DIS	STINCT	ION				SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACT IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II						

GRAND TOTAL = 1148/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH: 71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 37											
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER											
B120053111 KULKARNI HARSH MAHESH		SHILE	PA	,7152	7335D ,PICT ,B1200	53111					
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	07 48 14 35 14 42 05 49 15 51	35 P C 36 P C 37 P C 38 P C 37 P C 38 P C	404189 404190 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 20 PP 100 40 28 PP 100 40 21 PP 100 40 23 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	39 59 P 52 80 P 52 73 P 47 70 P 43 P 37 P 42 P 40 P 78 P 35 P				
GRAND TOTAL = 1023/1500 , RESULT:FIRST (CLASS WITH DIS	2.I.TNC.I.TON									
B120053112 KULKARNI ISHA SHRIKANT		MUKTA	<i>A</i>	,7152	7336B ,PICT ,B1200	53112					
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	14 57 27 38 22 50 24 55 24 51	7 71 P C 6 65 P C 72 P C 75 P C 37 P C 40 P C 44 P C 47 P C 40 P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 25 PP 100 40 26 PP 100 40 22 PP 100 40 25 TW 50 20 OR 50 20 TW 50 20 TW 50 20 TW 100 40 PR 50 20	39 64 P 52 78 P 54 76 P 44 69 P 38 P 40 P 42 P 40 P 93 P 44 P				
GRAND TOTAL = 1154/1500 , RESULT:FIRST (. 										
B120053113 KULKARNI RIGVED RAJESH		SAMPA	ADA	,7152	7340L ,PICT ,B1200	53113					
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 OR 50 20 OR 50 20	12 49 18 29 15 44 17 38 15 41	61 P C 47 P C 4 59 P C 3 55 P C 47 P C 46 P C 46 P C 43 P C 43 P C 47 P C	404189 404190 404191D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. SOFT COMPUTING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACT. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 12 PP 100 40 19 PP 100 40 16 PP 100 40 18 TW 50 20 OR 50 20 TW 50 20 TW 50 20 TW 100 40 PR 50 20 PR 50 20	30 42 P 42 61 P 36 52 P 36 54 P 40 P 41 P 43 P 42 P 97 P 48 P				
GRAND TOTAL = 1014/1500 , RESULT:FIRST (LASS WITH DIS	2.I.TNC.I.TON									

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BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

	1:71-B.E. (2012 PAT.)(ELECTRON. : 04 JUL 2018 CENTRE: PU				R TEC	HNOLOG	GY, PUNE.	PAGE NO. 3	38			
NOTE: F	IRST LINE : SEAT NO., NAME OF INES: HEAD OF PASSING, MAX. M	THE CANDI	DATE,	MOTHER	, PER	MANENT	reg. No.	, PREVIOUS SEAT NO., COLLEGI	E, SEAT	NO.		
	L14 KULKARNI SHEKHAR DIPAK											······
B120053				JYOII			,/103		03114			
404181 404182 404183 404184B 404185C 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 14 40 27 40 23 40 23 40 19 20 20 20 20 20	52 38 46 56 50	66 65 69 79 69 36 42 43 41 44	P C P C P C P C P C P C P C P C P C P C	404189 404191D 404191D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. SOFT COMPUTING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACT. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 1 40 1 40 1 40 2 20 20 20 20 40 20	3 39 4 46 8 44 3 39	52 P 60 P 62 P 62 P 42 P 40 P 41 P 35 P 89 P 41 P
GRAND TOTAI	L = 1078/1500 , RESULT:FIRST C	LASS WITH :	DISTIN	CTION								
B1200531	 L15 KUMBHARKAR POOJA ASHOK			SWATI				7344C ,PICT ,B1200!	 53115	• • • • • •		• • • • • • • • •
										40 0	0 51	72 D
404181 404182 404183 404184A 404185B 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 18 40 29 40 26 40 21 40 20 20 20 20 20 20	54 38 37 51 47	72 67 63 72 67 37 45 41 46 39	P C C C C C C C P P C C C C C P P C C C C C P	404189 4041910 404192D 404193 404193 404194 404194 404195	MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 2 40 2 40 1 20 20 20 20 40 20	2 51 5 49 1 60 4 40	73 P 74 P 81 P 54 P 40 P 41 P 40 P 40 P 90 P 42 P
GRAND TOTAL	L = 1128/1500 , RESULT:FIRST C	LASS WITH	DISTIN	CTION								
B120053	L16 KURANI ANKIT DEEPAK			HEENA			,7152	7345M ,PICT ,B1200	 53116			• • • • • • • • •
404186 404186 404187 404187 404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I L = 1118/1500 , RESULT:FIRST C	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 25 40 18 40 22 40 27 20 20 20 20 20 20	36 45 56 47	61 63 78	P C P C P C	404190 404191C	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING BIOMEDICAL SIGNAL PROCESSING LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 PP 100	40 2 40 2	5 48 2 50	72 P

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)
DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY. PUNE.

BRANCI DATE	BRANCH: 71-B.E. (2012 PAT.) (ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 39											
NOTE: F	DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 39 NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.											
OTHER L	INES: HEAD OF PASSING, MAX. M	ARKS, MIN	.PAS	S MAR	KS, IN	NT. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OV	ER		
B120053	117 KUTE SHUBHAM SUNIL			J	YOTI			,7140	0979C ,PICT ,B120053117	• •		
	SEM.:1								SEM.:2			
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	06 13	40	46	P	404189	MOBILE COMMUNICATION PP 100 40 25 16 F			
404182	MICROWAVE ENGINEERING	PP 100	40	07	3∠ 40	45 47	P P	404190 404191C	AUDIO VIDEO ENGINEERING PP 100 40 19 32 51 P			
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	12	29	41	P C	404192D	WIRELESS NETWORKS PP 100 40 10 42 52 P			
404185C	PLCS AND AUTOMATION	PP 100	40	06	37	43	P	404193	LAB PRACTICE III(MC & BCS) TW 50 20 20 P			
404186 404186	LAB PRACTICE I (CN & MWE)	TW 50	20			23	РC	404193 404194	LAB PRACTICE 111 (MC & BCS) OR 50 20 22 P			
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			21	РC	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20 32 P			
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			28	P	404195	PROJECT PHASE II TW 100 40 90 P			
404188	PROJECT PHASE I	OR 50	20			40	РC	404195	SEM.:2 MOBILE COMMUNICATION			
GRAND TOTAL	r =/1500 RESULT: FAILS											
							F	RESULT RES	ERVED FOR BACKLOGS.			
B120053	118 LAYKUMAR BHARAT PATEL			K	RISHNA	Ą		,7152	7350H ,PICT ,B120053118			
SEM.:1 SEM.:2 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 16 47 63 P C 404189 MOBILE COMMUNICATION PP 100 40 23 44 67 P 404182 COMPUTER NETWORKS PP 100 40 27 34 61 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 26 45 71 P 404183 MICROWAVE ENGINEERING PP 100 40 23 44 67 P C 404191D SOFT COMPUTING PP 100 40 18 45 63 P 404184A DIGITAL IMAGE PROCESSING PP 100 40 19 55 74 P C 404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 25 43 68 P 404185D ARTIFICIAL INTELLIGENCE PP 100 40 18 45 63 P C 404193 LAB PRACTICE III (MC & BCS) TW 50 20 37 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 36 P C 404193 LAB PRACTICE III (MC & BCS) OR 50 20 43 P 404186 LAB PRACTICE I (CN & MWE) OR 50 20 47 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 35 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 42 P 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 44 P C 404195 PROJECT PHASE II TW 100 40 96 P 404188 PROJECT PHASE I OR 50 20 46 P												
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	16	47	63	РC	404189	MOBILE COMMUNICATION PP 100 40 23 44 67 P			
404182	COMPUTER NETWORKS	PP 100	40	27	34	61	PC	404190	BROADBAND COMMUNICATION SYS. PP 100 40 26 45 71 P			
404183 404184A	DIGITAL IMAGE PROCESSING	PP 100	40	19	55	74	PC	404191D 404192A	BIOMEDICAL SIGNAL PROCESSING PP 100 40 16 43 68 P			
404185D	ARTIFICIAL INTELLIGENCE	PP 100	40	18	45	63	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20 37 P			
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			36	РC	404193	LAB PRACTICE III(MC & BCS) OR 50 20 43 P			
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			4.7	P C	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 35 P			
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			44	PC	404195	PROJECT PHASE II TW 100 40 96 P			
404188	PROJECT PHASE I	OR 50	20			42	РC	404195	PROJECT PHASE II PR 50 20 46 P			
GRAND TOTAL	L = 1105/1500 RESULT:FIRST C	LASS WITH	DIS	TINCT	'ION							
B120053					 AURI				7351F ,PICT ,B120053119	• •		
404181	SEM.:1 VIST DESIGN & TECHNOLOGY	DD 100	40	21	57	78	DС	404189	SEM.:2 MORTLE COMMINICATION DD 100 40 20 38 58 D			
404182	COMPUTER NETWORKS	PP 100	40	28	35	63	РC	404190	BROADBAND COMMUNICATION SYS. PP 100 40 28 51 79 P			
404183	MICROWAVE ENGINEERING	PP 100	40	24	43	67	РC	404191C	AUDIO VIDEO ENGINEERING PP 100 40 23 51 74 P			
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	22	49	71	P C	404192D	WIRELESS NETWORKS PP 100 40 28 39 67 P			
404185D 404186	TAB PRACTICE I (CN & MWE)	TW 50	20	Ι/	21	38	PC	404193	LAB PRACTICE III (MC & BCS) IW 50 20 46 P			
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			47	P C	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 44 P			
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			42	P C	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20 44 P			
404187 404188	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20 20			44 45	P C	404195 404195	PROJECT PHASE II TW 100 40 94 P			
404100	INOUECI FIRDE I	010 30	۷ ک			40	r C	ユロユエシ ン	SEM.:2 MOBILE COMMUNICATION PP 100 40 20 38 58 P BROADBAND COMMUNICATION SYS. PP 100 40 28 51 79 P AUDIO VIDEO ENGINEERING PP 100 40 23 51 74 P WIRELESS NETWORKS PP 100 40 28 39 67 P LAB PRACTICE III(MC & BCS) TW 50 20 46 P LAB PRACTICE III(MC & BCS) OR 50 20 44 P LAB PRAC. IV(ELECTIVE III) TW 50 20 44 P LAB PRAC. IV(ELECTIVE III) PR 50 20 44 P PROJECT PHASE II TW 100 40 94 P PROJECT PHASE II PR 50 20 46 P			
GRAND TOTAL	L = 1159/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	ION							

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PU	NE INSTITU	JTE	OF COMPUT	ER TE	CHNOLO	GY, PUNE.	PAGE NO.	40					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CANDI	 IDAT	 E, MOTHE	R. PE	RMANEN	 I REG. NO.	, PREVIOUS SEAT NO., COLLEC	E. SEAT	NO.	• • • • • •	• • • • •	• • • •	
OTHER LI	NES: HEAD OF PASSING, MAX. MAX. MAX. MAX. MAX. MAX. MAX. MAX.	ARKS, MIN.	.PAS	S MARKS,	INT.	MARKS,	TH. MARKS	, TOTAL MARKS OBTAINED, P/F:	PASS/FAIL	, C	:PREVI	OUS C	ARRY	OVER
B1200531	20 LOKHANDE HARSHVARDHAN RA	JENDRA		SUJAT	Α	• • • • •	,7163	3632E ,PICT ,B1200	53120	• • • •				• • • • •
	SEM.:1							SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	16 53	69	P C	404189	MOBILE COMMUNICATION	PP 100	40	19	31	50	P
404182 404183	MICROWAVE ENGINEERING	PP 100 PP 100	40 40	24 38 09 58	62 67	PC	404190 404191C	AUDIO VIDEO ENGINEERING	PP 100 PP 100	40 40	23 24	40 54	78	P D
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	22 58	80	PC	404192D	WIRELESS NETWORKS	PP 100	40	28	47	75	P
404185C	PLCS AND AUTOMATION	PP 100	40	23 55	78	РC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20		38	P C	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20		42	PC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			28	P
404187 404187	LAB PRAC. II(VLSI &ELEC. I)	1W 5U	20 20		34	PC	404194 404195	DROTECT DRACE II	PK 50	20 40			3 O	P D
404188	PROJECT PHASE I	OR 50	20		39	РC	404195	PROJECT PHASE II	PR 50	20			42	P
	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACT II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I													
GRAND TOTAL	_ = 1090/1500 , RESULT:FIRST C	LASS WITH	DIS	LINCITON										
B1200531				NILAW	ATI		,7163	3633C ,PICT ,B1200	53121					
404181	SEM.:1	DD 100	40	14 33	47	DС	404189	SEM.:2	DD 100	40	15	44	59	D
404182	COMPUTER NETWORKS	PP 100	40	24 36	60	PC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	20	43	63	P
404183	MICROWAVE ENGINEERING	PP 100	40	15 35	50	РC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	19	53	72	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	20 52	72	РC	404192D	WIRELESS NETWORKS	PP 100	40	24	46	70	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	22 38	60	PC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			41	P
404186 404186	LAB PRACILCE I (CN & MWE)	1W 5U	20 20		42	PC	404193	LAB PRACTICE III (MC & BCS)	UK 50	20 20			40 24	P D
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20		37	PC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20		35	РC	404195	PROJECT PHASE II	TW 100	40			90	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20		35	РC	404195	PROJECT PHASE II	PR 50	20			38	P
GRAND TOTAL	L = 1023/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCTION										
B1200531				KALPA	NA		,7163	3634M ,PICT ,B1200	53122	• • • •	• • • • •	• • • • •		• • • • •
	SEM.:1							SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	17 37	54	РC	404189	MOBILE COMMUNICATION	PP 100	40	22	29	51	P
404182	COMPUTER NETWORKS	PP 100	40	29 49	78	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	28	45	73	P
404183	MICROWAVE ENGINEERING	PP 100	40	19 52	71	РC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	20	57	77	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	24 56	80	P C	404192D	WIRELESS NETWORKS	PP 100	40	29	46	75	P
404185B	ELECTRONIC PRODUCT DESIGN	DD TOO	20	23 43	66	P C	404193	LAB PRACTICE III(MC & BCS)	J.M 20	20			39 41	P
404186	LAB PRACTICE I (CN & MWE)	1W 50	20		40	PC	404193	IAB PRACTICE III (MC & BCS)	UK 50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20		36	PC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20		46	P C	404195	PROJECT PHASE II	TW 100	40			95	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20		42	РC	404195	PROJECT PHASE II	PR 50	20			40	P
	- 1122/1500 PECHT TIPCT C													

GRAND TOTAL = 1122/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	BRANCH: 71-B.E. (2012 PAT.) (ELECTRONICS & TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 41															
NOTE: FI	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.															
OTHER LI	NES: HEAD OF PASSING, MAX. MA	ARKS, MIN	.PAS	S MAI	RKS, IN	JT. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAIN	NED, P/F:	PASS/FAIL	. C	PREVI	OUS C	ARRY	OVER
B1200531	23 LOUTE SHWETA SUNIL				VANDANA	<i>A</i>		,7140	0988B ,PICT	,B12005	53123	• • • •				
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	09	32	41	P D G	404189	MOBILE COMMUNICATIO	ON	PP 100	40	19	32	51	P
404182	COMPUTER NETWORKS	PP 100	40	14 05	30 40	44	P C	404190 404191	BROADBAND COMMUNICA	ATION SYS.	PP 100	40	10 16	3 L	41 60	Ъ
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	19	28	47	РC	404192A	BIOMEDICAL SIGNAL A	PROCESSING	PP 100	40	21	40	61	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	17	37	54	РC	404193	LAB PRACTICE III(MO	C & BCS)	TW 50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			36	P C	404193	LAB PRACTICE III(MC	C & BCS)	OR 50	20			42	P
404187	LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE)	TW 50	20			39	P C	404194	LAB PRAC. IV(ELECTI	IVE III)	PR 50	20			38	P P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			32	РC	404195	PROJECT PHASE II	,	TW 100	40			85	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			33	РC	404195	PROJECT PHASE II		PR 50	20			43	P
CRAND TOTAL	900/1500 PEGIII.T: ETPGT CI.	227														
							F	RESULT RES	ERVED FOR BACKLOGS.							
B1200531					 ΓARIKA			,7152	7356G ,PICT	,B12005	53124	• • • •				
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	10	34	44	P	404189	MOBILE COMMUNICATION	ON	PP 100	40	09	35	44	P
404182	COMPUTER NETWORKS	PP 100	40	17	31	48	РC	404190	BROADBAND COMMUNICA	ATION SYS.	PP 100	40	18	36 46	54 60	P
404183 404184A	DIGITAL IMAGE PROCESSING	PP 100 PP 100	40	13	33	46	РC	404191C 404192A	BIOMEDICAL SIGNAL I	PROCESSING	PP 100 PP 100	40	23	30	53	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	14	29	43	РC	404193	LAB PRACTICE III(MO	C & BCS)	TW 50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			42	P C	404193	LAB PRACTICE III(MC	C & BCS)	OR 50	20			44	P
404186 404187	LAB PRACTICE I (CN & MWE)	OR 50	20 20			4∠ 33	P C	404194	LAB PRAC. IV(ELECT	INE III) INE III)	TW 50 PR 50	20 20			31 36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			32	РC	404195	PROJECT PHASE II		TW 100	40			95	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			42	РC	404195	PROJECT PHASE II		PR 50	20			44	P
GRAND TOTAL	L = 923 + 10/1500 , RESULT:FIRS	ST CLASS	[& 0.2	163]											
B1200531	25 LUNAWAT KARISHMA SANDEEP				VEENA			,7152	7357E ,PICT	,B12005	53125					• • • • •
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	21	37	58	РC	404189	MOBILE COMMUNICATIO	ON	PP 100	40	27	47	74	P
404182	COMPUTER NETWORKS	PP 100	40	28	46	74	P C	404190	BROADBAND COMMUNICA	ATION SYS.	PP 100	40	26 1 E	55 63	81	P
404183 404184A	DIGITAL IMAGE PROCESSING	PP 100 PP 100	40	23 26	49 57	7.2 8.3	PC	404191C 404192A	BIOMEDICAL SIGNAL I	PROCESSING	PP 100 PP 100	40	28	6∠ 58	7 7 8 6	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	24	50	74	P C	404193	LAB PRACTICE III(MC	C & BCS)	TW 50	20		-	37	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			42	P C	404193	LAB PRACTICE III(MC	C & BCS)	OR 50	20			40	P
404186 404187	LAB PRACTICE I (CN & MWE)	OR 50	20			40 32	ъс БС	404194 404194	LAB PRAC. IV(ELECTION IN THE PRACE IN TRACE IN THE PRACE	INE III) INE TTT)	DB 20	20			40 39	D F
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			38	P	404195	PROJECT PHASE II		TW 100	40			94	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			44	P C	404195	PROJECT PHASE II		PR 50	20			44	P
	_ = 1169/1500 , RESULT:FIRST CI															

GRAND TOTAL = 1169/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PU	NE INSTITUT	MUNICA E OF C	OMPUTER	R TEC	CHNOLOG	Y, PUNE.	PAGE NO.	42					
NOTE: FI	DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 42 NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER													
OTHER LI	NES: HEAD OF PASSING, MAX. M.	ARKS, MIN.P	ASS MA	ARKS, IN	JT. N	IARKS,	TH. MARKS	S, TOTAL MARKS OBTAINED, P/F:	PASS/FAII	ı, C	:PREVI	COUS C	CARRY	OVER
B1200531	26 MACHALE SHREYASH KISHOR	• • • • • • • • • • •		VAISHL]	[,7152	7359M ,PICT ,B1200	53126					
	SEM.:1							SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100 4	0 07	33\$	40	P	404189	MOBILE COMMUNICATION	PP 100	40	12	35	47	P
404182	COMPUTER NETWORKS	PP 100 4	0 26	35	61	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	17	41	58	P
404183	MICROWAVE ENGINEERING	PP 100 4	0 16	39	55	РC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	09	43	52	P
404184A	DIGITAL IMAGE PROCESSING	PP 100 4	0 21	34	55	PC	404192D	WIRELESS NETWORKS	PP 100	40	22	40	62	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100 4	0 17	33	50	P C	404193	LAB PRACTICE III (MC & BCS)	.I.M 20	20			35	Ь
404186	LAB PRACTICE I (CN & MWE)	1M 20 Z	0		2/	P C	404193	LAB PRACTICE III (MC & BCS)	UK 50	20			24	P
404180	TAR DRACTICE I (CN & MWE)	TW 50 2	0 0		30	P C	404194	TAR DRAC TV(FIECTIVE III)	DP 50	20			36	D
404187	IAB PRAC II(VISI & ELEC. I)	PR 50 2	0		35	PC	404195	PROJECT PHASE II	TW 100	40			50	Þ
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50 2	0		31	РC	404195	PROJECT PHASE II	PR 50	20			25	P
GRAND TOTAL	= 845/1500 , RESULT:HIGHER S	ECOND CLASS	[\$	0.1]		_								
	= 845/1500 , RESULT:HIGHER S					ŀ	RESULT RES	SERVED FOR BACKLOGS.						
B1200531	27 MADHAVE AISHWARYA RAJEND	 RA		SWATI			,7153	39461E ,PICT ,B1200	 53127					
404101	SEM.:1	100 4	0 10	2.5	- 4	_ ~	404100	SEM.:2	100	4.0		4.0	- 1	_
404181	VLSI DESIGN & TECHNOLOGY	PP 100 4	0 73 0 T8	36	54	P C	404189	MOBILE COMMUNICATION GVG	PP 100	40	16	48	64	Ъ
404182	MICDOWAVE ENGINEEDING	PP 100 4	0 23	43	50	P C	404190	BRUADBAND COMMUNICATION SIS.	PP 100	40	∠⊥ 10	45 E1	60	P
404183 404184a	DIGITAL IMAGE PROCESSING	DD 100 4	0 08 0 18	50	68	PC	4041910	WIRELESS NETWORKS	PP 100 PD 100	40	25	45	70	D
404185C	PLCS AND AUTOMATION	PP 100 4	0 25	37	62	PC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20	23	15	38	P
404186	LAB PRACTICE I (CN & MWE)	TW 50 2	0		41	РC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR 50 2	0		35	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			24	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50 2	0		33	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50 2	0		34	РC	404195	PROJECT PHASE II	TW 100	40			88	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50 2	0		38	РC	404195	PROJECT PHASE II	PR 50	20			42	P
GRAND TOTAL	= 1017/1500 , RESULT:FIRST C	LASS WITH D	ISTINC	CTION										
B1200531	28 MAHADIK PRADNYA PRAKASH			SUNANDA	A		,7163	33635K ,PICT ,B1200	53128					
	SEM.:1							SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100 4	0 13	32	45	РC	404189	MOBILE COMMUNICATION	PP 100	40	23	39	62	P
404182	COMPUTER NETWORKS	PP 100 4	0 19	33	52	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	16	45	61	P
404183	MICROWAVE ENGINEERING	PP 100 4	0 17	50	67	РC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	18	56	74	P
404184A	DIGITAL IMAGE PROCESSING	PP 100 4	0 20	57	77	РC	404192D	WIRELESS NETWORKS	PP 100	40	23	39	62	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100 4	0 25	39	64	PC	404193	LAB PRACTICE III (MC & BCS)	TW 50	20			42	Ъ
4U4186 40410 <i>6</i>	LAB PRACTICE I (CN & MWE)	TW 50 2	0		42	ь с	404193 404104	LAB PRACTICE III(MC & BCS)	UK 50	20			4U 24	D F
404100	LAD PRACILCE I (CN & MWE)	UK 50 2	0		43 40	P C	404194	TAR DRAC IV(ELECTIVE III)	DB EU	⊿∪ 2∩			∆4 4∩	D
404187	LAB PRAC. II(VISI & ELEC. I)	PR 50 2	0		33	P	404195	PROJECT PHASE IT	TW 100	40			90	Þ
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50 2	0		37	P C	404195	PROJECT PHASE II	PR 50	20			38	P
	= 1045/1500 , RESULT:FIRST C													

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PINE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

BRANCH: 71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 43										
DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 43 NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.										
OTHER LINES: HEAD OF PASSING, MAX.	MARKS, MIN.PA	SS MARKS,	INT. MARKS,	TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER						
B120053129 MAHAJAN LEENA HUKUMCHAN	D	SADHA	NA	,71633636H ,PICT ,B120053129						
SEM.:1				SEM.:2						
404181 VLSI DESIGN & TECHNOLOGY	PP 100 40	18 32	50 P C	SEM.:2 404189 MOBILE COMMUNICATION PP 100 40 21 49 70 P 404190 BROADBAND COMMUNICATION SYS. PP 100 40 19 47 66 P 404191C AUDIO VIDEO ENGINEERING PP 100 40 27 56 83 P 404192D WIRELESS NETWORKS PP 100 40 28 47 75 P 404193 LAB PRACTICE III(MC & BCS) TW 50 20 42 P 404193 LAB PRACTICE III(MC & BCS) OR 50 20 39 P 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 35 P 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 39 P 404195 PROJECT PHASE II TW 100 40 92 P 404195 PROJECT PHASE II PR 50 20 42 P						
404183 MICROWAVE ENGINEERING	PP 100 40	19 52	75 P C	404191C AUDIO VIDEO ENGINEERING PP 100 40 19 47 00 P						
404184A DIGITAL IMAGE PROCESSING	PP 100 40	22 59	81 P C	404192D WIRELESS NETWORKS PP 100 40 28 47 75 P						
404185C PLCS AND AUTOMATION	PP 100 40	25 52	77 P C	404193 LAB PRACTICE III(MC & BCS) TW 50 20 42 P						
404186 LAB PRACTICE I (CN & MWE)	TW 50 20		48 P C	404193 LAB PRACTICE III(MC & BCS) OR 50 20 39 P						
404187 LAB PRAC. II(VLSI & ELEC. I)	TW 50 20		39 P.C	404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 39 P						
404187 LAB PRAC. II(VLSI &ELEC. I)	PR 50 20		30 P C	404195 PROJECT PHASE II TW 100 40 92 P						
404188 PROJECT PHASE I	OR 50 20		44 P C	404195 PROJECT PHASE II PR 50 20 42 P						
GRAND TOTAL = 1143/1500 , RESULT:FIRST	CLASS WITH DI	STINCTION								
B120053130 MAHAJAN PRANJALI BINDUN	 IADHAV	VIDYA		,71527362M ,PICT ,B120053130						
SEM.:1 404181 VIST DESIGN & TECHNOLOGY	DD 100 40	13 33	46 P.C	SEM.: 2 404189 MORTLE COMMINICATION DD 100 40 22 40 62 D						
404182 COMPUTER NETWORKS	PP 100 40	27 42	69 P C	404190 BROADBAND COMMUNICATION SYS. PP 100 40 21 52 73 P						
404183 MICROWAVE ENGINEERING	PP 100 40	23 48	71 P C	404191D SOFT COMPUTING PP 100 40 21 40 61 P						
404184B EMBEDDED SYSTEMS & RTOS	PP 100 40	22 50	72 P C	404192D WIRELESS NETWORKS PP 100 40 28 48 76 P						
404185D ARTIFICIAL INTELLIGENCE	PP 100 40	20 45	65 P C	404193 LAB PRACTICE III(MC & BCS) TW 50 20 38 P						
404186 LAB PRACTICE I (CN & MWE)	OR 50 20		41 P.C	404194 LAB PRAC. TV(ELECTIVE III) TW 50 20 30 P						
404187 LAB PRAC. II(VLSI &ELEC. I)	TW 50 20		43 P C	404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 32 P						
404187 LAB PRAC. II(VLSI &ELEC. I)	PR 50 20		40 P C	404195 PROJECT PHASE II TW 100 40 95 P						
404188 PROJECT PHASE I	OR 50 20		37 P C	SEM.:2 404189 MOBILE COMMUNICATION PP 100 40 22 40 62 P 404190 BROADBAND COMMUNICATION SYS. PP 100 40 21 52 73 P 404191D SOFT COMPUTING PP 100 40 21 40 61 P 404192D WIRELESS NETWORKS PP 100 40 28 48 76 P 404193 LAB PRACTICE III(MC & BCS) TW 50 20 38 P 404193 LAB PRACTICE III(MC & BCS) OR 50 20 40 P 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 30 P 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 32 P 404195 PROJECT PHASE II TW 100 40 95 P 404195 PROJECT PHASE II PR 50 20 43 P						
GRAND TOTAL = 1072/1500 , RESULT:FIRST	CLASS WITH DI	STINCTION								
B120053131 MAHAJAN SWAPNIL RAVINDE		SUNIT		,71400991B ,PICT ,B120053131						
SEM.:1 404181 VIST DESIGN & TECHNOLOGY	DD 100 40	7 7 7 7 7 7 T	F	SEM.: 2 404189 MORTLE COMMINICATION DD 100 40 24 31 55 D						
404182 COMPUTER NETWORKS	PP 100 40	15 30	45 P	404190 BROADBAND COMMUNICATION SYS. PP 100 40 20 36 56 P						
404183 MICROWAVE ENGINEERING	PP 100 40	AA AA	F	404191C AUDIO VIDEO ENGINEERING PP 100 40 11 40 51 P						
404184A DIGITAL IMAGE PROCESSING	PP 100 40	AA AA	F	404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 20 40 60 P						
404185B ELECTRONIC PRODUCT DESIGN	PP 100 40	13 34	47 P C	404193 LAB PRACTICE III(MC & BCS) TW 50 20 30 P						
404186 LAB PRACTICE I (CN & MWE)	OR 50 20		27 P	404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 24 P						
404187 LAB PRAC. II(VLSI &ELEC. I)	TW 50 20		27 P	404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 34 P						
404187 LAB PRAC. II(VLSI &ELEC. I)	PR 50 20		30 P	404195 PROJECT PHASE II TW 100 40 65 P						
4U4188 PROJECT PHASE I	OR 50 20		35 P C	SEM.:2 404189 MOBILE COMMUNICATION PP 100 40 24 31 55 P 404190 BROADBAND COMMUNICATION SYS. PP 100 40 20 36 56 P 404191C AUDIO VIDEO ENGINEERING PP 100 40 11 40 51 P 404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 20 40 60 P 404193 LAB PRACTICE III(MC & BCS) TW 50 20 30 P 404194 LAB PRACTICE III(MC & BCS) OR 50 20 32 P 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 24 P 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 34 P 404195 PROJECT PHASE II TW 100 40 65 P 404195 PROJECT PHASE II PR 50 20 35 P						
GRAND TOTAL =/1500 , RESULT:FAILS	[& 0.163]									
				RESULT RESERVED FOR BACKLOGS.						

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH DATE :	04 JUL 2018 CENTRE: PUI	ICS &TELE	COMU	OF CO	ION) MPUTER	TEC	HNOLOG	Y, PUNE.	PAGE NO. 44	
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	IDAT	Έ, M	OTHER,	PER	MANENT	REG. NO.	PREVIOUS SEAT NO., COLLEGE, SEAT NO.	
OTHER LI	NES: HEAD OF PASSING, MAX. M	ARKS, MIN	.PAS	S MAR	KS, IN	IT. M	IARKS,	TH. MARKS	TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARI	RY OVER
B1200531	32 MAHAJAN TEJASWI RAJENDRA			v	ANDANA			,7163	637F ,PICT ,B120053132	
	SEM.:1								SEM.:2	
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	19	47	66	P C	404189	SEM.:2 MOBILE COMMUNICATION PP 100 40 22 58 80 BROADBAND COMMUNICATION SYS. PP 100 40 22 52 74 AUDIO VIDEO ENGINEERING PP 100 40 26 62 88 WIRELESS NETWORKS PP 100 40 28 49 7 LAB PRACTICE III(MC & BCS) TW 50 20 41 LAB PRACTICE III(MC & BCS) OR 50 20 42 LAB PRAC. IV(ELECTIVE III) TW 50 20 31 LAB PRAC. IV(ELECTIVE III) PR 50 20 41 LAB PRAC. IV(ELECTIVE III) PR 50 20 41 PROJECT PHASE II TW 100 40 90 PROJECT PHASE II PR 50 20 41 PR 50 20) P
404182 404183	COMPUTER NETWORKS	PP 100	40 40	29	52 52	81 75	P C	404190 404191	BROADBAND COMMUNICATION SYS. PP 100 40 22 52 74	ł P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	27	63	90	PC	404191C	WIRELESS NETWORKS PP 100 40 28 49 7	7 P
404185C	PLCS AND AUTOMATION	PP 100	40	25	58	83	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20 43	3 P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			48	РC	404193	LAB PRACTICE III(MC & BCS) OR 50 20 4:	L P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			42	P C	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 39	5 P
404187 404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20 20			41 42	P C	404194 404195	DRATECT DEAGE II TW 100 40	ם ה
404188	PROJECT PHASE I	OR 50	20			44	PC	404195	PROJECT PHASE II PR 50 20 41	L P
GRAND TOTAL	= 1226/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	ION					
	• • • • • • • • • • • • • • • • • • • •									
	33 MAHIMA MUNDRA								363K ,PICT ,B120053133	
	SEM.:1								SEM.:2 MOBILE COMMUNICATION PP 100 40 20 48 68 BROADBAND COMMUNICATION SYS. PP 100 40 19 50 69 AUDIO VIDEO ENGINEERING PP 100 40 14 49 65 WIRELESS NETWORKS PP 100 40 22 45 66 LAB PRACTICE III(MC & BCS) TW 50 20 36 LAB PRACTICE III(MC & BCS) OR 50 20 45 LAB PRAC. IV(ELECTIVE III) TW 50 20 39 LAB PRAC. IV(ELECTIVE III) PR 50 20 39 PROJECT PHASE II TW 100 40 90 PROJECT PHASE II PR 50 20 41 19 PROJECT PHASE II PR 50 20 41 19 19 19 19 19 10 10 10 10 10 10 10 10 10 10 10 10 10	
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	11	30	41	РC	404189	MOBILE COMMUNICATION PP 100 40 20 48 68	3 P
404182	COMPUTER NETWORKS	PP 100	40	26	32	58	PC	404190	BROADBAND COMMUNICATION SYS. PP 100 40 19 50 69) P
404163 404184A	DIGITAL IMAGE PROCESSING	PP 100 PP 100	40	19	49	68	PC	404191C 404192D	WIRELESS NETWORKS PP 100 40 14 49 6.	7 P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	17	43	60	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20 36	, <u> </u>
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			40	РC	404193	LAB PRACTICE III (MC & BCS) OR 50 20 43	3 P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			36	РC	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 39	5 P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			40	P C	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20 39) P
404187 404188	DROIFCT DHASE I	PR 50	20 20			35 41	P C	404195 404195	PROJECT PHASE II TW 100 40 9. DROTECT DHASE II DR 50 20 40	3 P
							1 0	101175	INOUNCE III IN SO ZO	, ,
GRAND TOTAL	= 1046/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	ION					
B1200531	34 MAJI SHREYA SAMBHUNATH			C	HANDAN	ΙA		,7152	365F ,PICT ,B120053134	
	SEM.:1								SEM.:2	
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	14	31	45	P C	404189	MOBILE COMMUNICATION PP 100 40 23 43 66	5 P
404182	COMPUTER NETWORKS	PP 100	40	26	39	65	P C	404190	BROADBAND COMMUNICATION SYS. PP 100 40 17 54 7	L P
404183	MICROWAVE ENGINEERING	PP 100	40	11 16	46	5/	P C	404191D	SOFT COMPUTING PP 100 40 17 50 6	/ P
404185D	ARTIFICIAL INTELLIGENCE	PP 100	40	26	46	72	PC	404193	LAB PRACTICE III(MC & BCS) TW 50 20 21	l P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			38	РC	404193	LAB PRACTICE III(MC & BCS) OR 50 20	2 P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			38	P C	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 29) P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			33	P C	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20 4	L P
404187 404189	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			38 42	ъс БС	404195 404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 23 43 66 BROADBAND COMMUNICATION SYS. PP 100 40 17 54 73 SOFT COMPUTING PP 100 40 17 50 66 WIRELESS NETWORKS PP 100 40 25 44 69 LAB PRACTICE III(MC & BCS) TW 50 20 LAB PRACTICE III(MC & BCS) OR 50 20 LAB PRAC. IV(ELECTIVE III) TW 50 20 LAB PRAC. IV(ELECTIVE III) PR 50 20 PROJECT PHASE II TW 100 40 92 PROJECT PHASE II PR 50 20 44	2 P 1 D
404100	FROUDCI PRASE I	UC AU	∠∪			42	PC	404133	FROUDCT FIRSE II PR 30 20 44	ı r
	= 1013/1500 , RESULT:FIRST C									

GRAND TOTAL = 1013/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

GRAND TOTAL = 1037/1500 , RESULT:FIRST CLASS WITH DISTINCTION

DATE :	1:71-B.E. (2012 PAT.)(ELECTRON. 04 JUL 2018 CENTRE: PUI	ICS &TELEC NE INSTITU	JOMU. JTE	OF CO	ION) MPUTER	TEC	HNOLOG	Y, PUNE.	PAGE NO.	45					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CANDI	IDAT	E, M	OTHER,	PER	MANENT	REG. NO.	, PREVIOUS SEAT NO., COLLEG	GE, SEAT	NO.				
	NES: HEAD OF PASSING, MAX. MAX.													ARRY	OVER
B1200531	35 MANASI RAVINDRA JAGTAP			В	EBINAN	DA		,7152	7371L ,PICT ,B120	053135					
	SEM.:1								SEM.:2						
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACT. II(VLSI &ELEC. I) LAB PRAC. II(VLSI &ELEC. I) PROJECT PHASE I	PP 100	40	09	36	45	P	404189	MOBILE COMMUNICATION	PP 100	40	22	32	54	P
404182 404183	COMPUTER NETWORKS MICROWAVE ENGINEERING	PP 100 PP 100	40 40	18 12	32 32	50 44	P C	404190 404191C	AUDIO VIDEO ENGINEERING	PP 100	40 40	16 12	32 37	48 49	P P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	17	33	50	PC	404192D	WIRELESS NETWORKS	PP 100	40	13	38	51	P
404185C	PLCS AND AUTOMATION	PP 100	40	07	28	35#	P	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			36	P
404186 404186	LAB PRACTICE I (CN & MWE)	TW 50	20			32	ъс	404193 404194	LAB PRACTICE III(MC & BCS)	OR 50	20			42 35	ъ
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			28	PC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			40	P C	404195	PROJECT PHASE II	TW 100	40			90	P
404188	PROJECT PHASE I	OR 50	20			35	РC	404195	PROJECT PHASE II	PR 50	20			40	Р
CRAND TOTAL	881/1500 PEGIILT: HIGHER GI	CONTO CIAS	20	[# 0	41										
D1000F31								7150	72707 DTOM D100						• • • • •
B1200531	136 MANDE SHIVANEE VISHWAS			А	NUKADH	Α.		,/152	/3/20 ,PICI ,BI20	753136					
	SEM.:1								SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	11	31	42	P C	404189	MOBILE COMMUNICATION	PP 100	40	22	37	59 61	P
404183	MICROWAVE ENGINEERING	PP 100 PP 100	40	21	39	60	P C	404190 404191C	AUDIO VIDEO ENGINEERING	PP 100	40	10	30 47	57	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	23	53	76	РC	404192A	BIOMEDICAL SIGNAL PROCESSING	G PP 100	40	20	46	66	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	20	45	65	P C	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			40 38	PC	404193	LAB PRACTICE III (MC & BCS)	OR 50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			40	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			41	P
404187	LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PR 50	20			34	P C	404195	PROJECT PHASE II	TW 100	40			94	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			35	РC	404195	PROJECT PHASE II	PR 50	20			44	Р
GRAND TOTAL	L = 1034 + 10/1500 , RESULT:FIR	RST CLASS	WIT	H DIS	TINCTI	ON	[& 0	.163]							
B1200531	37 MANDORA TEJAS SANDIP			A	RCHANA	••••		,7152	7374E ,PICT ,B120	053137	••••	• • • • • •		••••	
	SEM.:1								SEM.:2						
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING BLCS AND AUTOMATION	PP 100	40	14	28	42	P C	404189	MOBILE COMMUNICATION				34		
404182 404183	COMPUTER NETWORKS MICROWAVE ENGINEERING	PP 100	40 40	22 20	34 46	56 66	P C	404190 4041910	BROADBAND COMMUNICATION SYS	PP 100	40 40	21 19	49 44	70 63	
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	24	50	74	PC	404192D	AUDIO VIDEO ENGINEERING WIRELESS NETWORKS	PP 100	40	27	4.0		_
404185C	PLCS AND AUTOMATION	PP 100	40	27	47	74	P C	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			39	PC	404193 404194	LAB PRACTICE III(MC & BCS)	OR 50	20			34	P
404187	LAB PRAC. II(VLSI & ELEC. I)	TW 50	20			41	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			40	P
404187	PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PR 50	20			40	P C	404195	WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	TW 100	40			89	P
404188	PROJECT PHASE I	OR 50	20			38	РC	404195	PROJECT PHASE II	PR 50	20			39	Р
	1005/1500														

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BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PUI	NE INSTITU	JTE C	F COMPUT	ER TEC	CHNOLOG	Y, PUNE.	PAGE NO.	46					
NOTE: FI	RST LINE : SEAT NO., NAME OF NES: HEAD OF PASSING, MAX. M	THE CAND	IDATE	l, MOTHE	R, PEI	RMANENT	REG. NO.	, PREVIOUS SEAT NO., COLLEG	E, SEAT	NO.				
											· · · · · ·		ARRY	OVER
B1200531	38 MANGIRE SNEHAL VIHANG			SHAIL	A		,7152	7376M ,PICT ,B1200	53138					
	SEM.:1							SEM.:2						
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) PROJECT PHASE I	PP 100	40	17 40	57	P C	404189	MOBILE COMMUNICATION	PP 100	40	19	40	59	P
404182	COMPUTER NETWORKS	PP 100	40	29 52	81	P C	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	24	56 52	80	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100 PP 100	40	22 59	81	PC	404191C 404192D	WIRELESS NETWORKS	PP 100 PP 100	40	29	47	76	P
404185D	ARTIFICIAL INTELLIGENCE	PP 100	40	25 50	75	РC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20		37	РC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			34	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20		41	PC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			42	P
404187	LAB PRAC. II(VLSI & ELEC. I)	PR 50	20		34	PC	404194	PROJECT PHASE IT	TW 100	40			93	P
404188	PROJECT PHASE I	OR 50	20		44	РC	404195	PROJECT PHASE II	PR 50	20			43	P
CDAND TOTAL	= 1139/1500 , RESULT:FIRST C	лее мтти	חדפיד	TNOTTON										
GRAND TOTAL				TINCTION										
B1200531	39 MANTRI NIKITA RAJESH			SAROJ			,7152	7380K ,PICT ,B1200	53139					
	SEM.:1							SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	13 39	52	РC	404189	MOBILE COMMUNICATION	PP 100	40	17	31	48	P
404182	COMPUTER NETWORKS	PP 100	40	26 40	66	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	26	48	74	P
404183	MICROWAVE ENGINEERING	PP 100	40	20 48	68	P C	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	18	40	58	P
404184A 404185D	DIGITAL IMAGE PROCESSING	PP 100	40 40	1/ 56 19 49	/ 3 6.8	P C	404192D 404193	WIRELESS NETWORKS	PP 100	40 20	22	48	/U 42	D D
404186	LAB PRACTICE I (CN & MWE)	TW 50	20	10 40	32	PC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20		37	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20		32	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			39	P
404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PR 50	20		34	PC	404195	PROJECT PHASE II	TW 100	40			92	P
404100	PROJECT PHASE I	OR 50	20		40	PC	404195	PROJECT PHASE II	PR 50	20			43	Р
GRAND TOTAL	= 1042/1500 , RESULT:FIRST C	LASS WITH	DIST	INCTION										
D1 2005 21	40 MANTRI PRIYANKA SHRINIWA			CANCE	יייא. דידא		7140		53140	• • • •	• • • • •	• • • • •	• • • •	
B1200551	10 MANIKI EKITANKA SIKINIWA.	5		SANGE	TIA		, / 140	099/M ,FICI ,BI200	33140					
	SEM.:1							SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	08 32	40	P	404189	MOBILE COMMUNICATION CVC	PP 100	40	18	30	48	P
404162	MICROWAVE ENGINEERING	PP 100 PP 100	40	20 32	50 52	P C	404190 404191A	SPEECH & AUDIO SIGNAL PROC.	PP 100	40	AA	43	43	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	15 39	54	РC	404192A	BIOMEDICAL SIGNAL PROCESSING	PP 100	40	13	29	42	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	17 46	63	P	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20		35	P C	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			32	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20		35	PC	404194	LAB PRAC. IV(ELECTIVE III) IAB PRAC TV(ELECTIVE III)	IW 50	20			30 36	P P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20		35	PC	404195	PROJECT PHASE II	TW 100	40			84	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20		32	РC	404195	PROJECT PHASE II	PR 50	20			42	P
	= 882/1500 , RESULT:HIGHER SI													

GRAND TOTAL = 882/1500 , RESULT:HIGHER SECOND CLASS

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 47													
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER													
B120053141 MANTRI RUCHI VENUGOPAL		ARCHAI		,7152	7381H ,PICT ,B12009	53141	• • • • • • • • • • • • • • • • • • • •						
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 OR 50 20 OR 50 20 OR 50 20	18 31 28 43 19 47 26 56 17 59	49 P C 71 P C 66 P C 82 P C 76 P C 41 P C 40 P C 41 P C 45 P C 42 P C	404189 404190 404191D 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. SOFT COMPUTING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 25 PP 100 40 20 PP 100 40 24 PP 100 40 24 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	31 56 P 50 70 P 52 76 P 43 67 P 43 P 38 P 39 P 38 P 91 P 41 P						
GRAND TOTAL - TITZ/TJOO , RESULTITIEST CH	WOO MIII DI	O T TIVC T TOIN											
B120053142 MANWAR PRASHIK KISHOR SUJATA ,71527383D ,PICT ,B120053142													
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 OR 50 20 OR 50 20 OR 50 20	13 29 25 30 20 40 18 41 12 31	42 P C 55 P C 60 P C 59 P C 43 P C 40 P C 38 P C 43 P C 43 P C 43 P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 23 PP 100 40 13 PP 100 40 16 PP 100 40 21 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	36 59 P 36 49 P 37 53 P 38 59 P 39 P 40 P 41 P 39 P 85 P 40 P						
GRAND TOTAL = 965/1500 . RESULT:FIRST CLA	SS												
B120053143 MARKAD NIKHIL NANDKISHOR	• • • • • • • • • • • • • • • • • • • •	KAMAL		,7152	7384B ,PICT ,B12009	53143							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I GRAND TOTAL = 953 + 10/1500 , RESULT:FIRS	OR 50 20 TW 50 20 PR 50 20 OR 50 20		47 P C 45 P C 65 P C 69 P C 57 P C 41 P C 37 P C 39 P C 30 P C 37 P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	DD 100 40 03	35 51 P 48 51 P 42 55 P 42 64 P 34 P 28 P 24 P 38 P 96 P 45 P						
ONTINO TOTAL - >>> TO/TYOU , KENDII-FIKO	T CHUDO	[4 0.103]											

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH:71-B.E. (2012 PAT.)(ELECTRO DATE: 04 JUL 2018 CENTRE: P			NOLOGY, PUNE.	PAGE NO. 48
NOTE: FIRST LINE : SEAT NO., NAME O	F THE CANDIDATE,	MOTHER, PERMA	ANENT REG. NO., PREVIOUS SEAT NO.	, COLLEGE, SEAT NO. ED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
B120053144 MARLIWAR PRADHYUMNA ANI				
CDM +1			GTW +2	
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 PROJECT PHASE I	PP 100 40 0 PP 100 40 1 PP 100 40 1 PP 100 40 1 PP 100 40 1 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20 OR 50 20	9 41 50 P 9 40 59 P 3 42 55 P 3 52 65 P 1 41 52 P 22 P 33 P 43 P	SEM.:2 404189 MOBILE COMMUNICATIO P C 404190 BROADBAND COMMUNICA P C 404191D SOFT COMPUTING P C 404192D WIRELESS NETWORKS P C 404193 LAB PRACTICE III(MC P C 404194 LAB PRAC. IV(ELECTI P C 404194 PROJECT PHASE II P C 404195 PROJECT PHASE II	N PP 100 40 17 31 48 P TION SYS. PP 100 40 17 42 59 P PP 100 40 13 51 64 P PP 100 40 22 46 68 P & BCS) TW 50 20 42 P & BCS) OR 50 20 44 P VE III) PR 50 20 43 P VE III) PR 50 20 41 P TW 100 40 94 P PR 50 20 45 P
GRAND IOIAL - 991/1300 , RESULITERSI C	THOS MIIU DISIIN	CIION		
B120053145 MAYURI DNYANESHWAR MAHA	 NDULE	TARA	,71527389C ,PICT	,B120053145
404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 PROJECT PHASE I	PP 100 40 0 PP 100 40 2 PP 100 40 0 PP 100 40 1 PP 100 40 1 TW 50 20 OR 50 20 TW 50 20 OR 50 20 OR 50 20 OR 50 20 OR 50 20	3 37\$ 40 P 0 30 50 P 9 35 44 P 7 47 64 P 5 45 60 P 43 P 38 P 40 P 45 P	404189 MOBILE COMMUNICATIO C 404190 BROADBAND COMMUNICA C 404191D SOFT COMPUTING C 404192D WIRELESS NETWORKS C 404193 LAB PRACTICE III(MC C 404193 LAB PRACTICE III(MC C 404194 LAB PRAC. IV(ELECTI C 404194 LAB PRAC. IV(ELECTI C 404195 PROJECT PHASE II C 404195 PROJECT PHASE II	N PP 100 40 14 33 47 P TION SYS. PP 100 40 09 40 49 P PP 100 40 11 45 56 P PP 100 40 19 37 56 P & BCS) TW 50 20 31 P & BCS) OR 50 20 34 P VE III) TW 50 20 31 P VE III) PR 50 20 27 P TW 100 40 93 P PR 50 20 45 P
GRAND TOTAL = 930/1500 , RESULT:FIRST C	LASS [\$ 0.1]			
B120053146 MEHARE VAISHNAVI PRABHA	 KAR	VIJAYA	,71527392C ,PICT	,B120053146
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185B ELECTRONIC PRODUCT DESIGN 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I GRAND TOTAL = 1122/1500 , RESULT:FIRST	PP 100 40 1 PP 100 40 2 PP 100 40 2 PP 100 40 2 PP 100 40 1 TW 50 20 OR 50 20 PR 50 20 OR 50 20 OR 50 20 OR 50 20 OR 50 20	8 35 53 P 9 32 61 P 4 51 75 P 2 60 82 P 9 43 62 P 38 P 42 P 40 P 37 P 45 P	SEM.:2 P.C. 404189 MOBILE COMMUNICATIO	N PP 100 40 29 42 71 P TION SYS. PP 100 40 26 48 74 P RING PP 100 40 19 47 66 P PP 100 40 29 44 73 P & BCS) TW 50 20 41 P & BCS) OR 50 20 46 P VE III) TW 50 20 36 P VE III) PR 50 20 37 P TW 100 40 96 P PR 50 20 47 P
3.2.2.2 1011.12 1122,1300 , KD0011.F1K01				

SAVITRIBAI PHULE PUNE UNIVERSITY, B.E.(2012 COURSE) EXAMINATION, MAY 2018 BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PUI	NE INSTIT	UTE	OF COM	IPUTER	TEC	HNOLOG	Y, PUNE.	I	PAGE NO.	49					
NOTE: FI	RST LINE : SEAT NO., NAME OF NES: HEAD OF PASSING, MAX. M	THE CAND	IDAT:	E, MC	THER,	PER	MANENT	REG. NO.	, PREVIOUS SEAT NO.,	, COLLEG	E, SEAT	r no.				
	47 MEHETRE PRIYANKA BHAUSAH															
	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) PROJECT PHASE I											40 40 40 40 20 20 20 20 40 20	23 19 14 27	53 56 47 44	76 75 61 71 47 44 41 38 91 41	P P P P P P P P P
GRAND TOTAL	. = 1124/1500 , RESULT:FIRST C	LASS WITH	DIS	I.TNC.I.T	.ON											
B1200531	48 MEHTA STUTI PUNIT			PR	 RITI		• • • • • •	,7152	7396F ,PICT	,B1200	53148		• • • • •		• • • •	• • • • •
404181 404182 404183 404184A 404185C 404186 404187 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20 20	18 20 24 18 14	45 49 42 57 51	63 69 66 75 65 32 34 39 36 36	P C C C C C C C C C C C C C C C C C C C	404189 404190 404191D 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICAT SOFT COMPUTING WIRELESS NETWORKS LAB PRACTICE III(MC LAB PRACTICE III(MC LAB PRAC. IV(ELECTIV LAB PRAC. IV(ELECTIV PROJECT PHASE II PROJECT PHASE II	TION SYS. & BCS) & BCS) /E III) /E III)	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20	20 17 14 23	45 48 51 46	65 65 69 39 45 29 40 94	P P P P P P P P P P
GRAND TOTAL	= 1071/1500 , RESULT:FIRST C	LASS WITH	DIS'	TINCTI	ON											
B1200531	49 MENGAJE PRANAY NILKANTH			VI	MAL			,7152	7398B ,PICT	,B1200	53149					
404181 404182 404183 404184A 404185C 404186 404187 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20	10 21 17 22 14	35 34 45 39 29	45 55 62 61 43 40 38 31 38 33	P C C C C C C C C C C C	404189 404190 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICAT AUDIO VIDEO ENGINEER WIRELESS NETWORKS LAB PRACTICE III(MC LAB PRACTICE III(MC LAB PRAC. IV(ELECTIV LAB PRAC. IV(ELECTIV PROJECT PHASE II PROJECT PHASE II	TION SYS. RING & BCS) & BCS) /E III) /E III)	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20	20 15 12 19	35 47 41 39	55 62 53 58 26 31 25 36 90 40	P P P P P P P P
GRAND TOTAL	a = 922/1500 , RESULT:FIRST CL	SSA														

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PU	NE INSTITU	JTE C	F COMPUTE	R TEC	CHNOLOG	Y, PUNE.	PAGE NO. !	50					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CANDI	 IDATE		 R, PEI	RMANENT	REG. NO.	, PREVIOUS SEAT NO., COLLEGI	 E, SEAT	NO.			• • • •	
OTHER LI	NES: HEAD OF PASSING, MAX. M	ARKS, MIN.	. PASS	MARKS, I	NT. N	MARKS,	TH. MARKS	, TOTAL MARKS OBTAINED, P/F:1	PASS/FAIL	, c	:PREVI	OUS C	ARRY	OVER
B1200531	50 MHASKE SAURABH SUKUMAR			FIONA	• • • •	• • • • • •	,7152	7400H ,PICT ,B1200!	53150					
	SEM.:1							SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	15 30	45	РC	404189	MOBILE COMMUNICATION	PP 100	40	17	33	50	P
404182	COMPUTER NETWORKS	PP 100	40	21 43	64	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	17	45	62	P
404183	MICROWAVE ENGINEERING	PP 100	40	15 38	53	PC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	18	45	63	P
404184B	APPTETATAL THPELLICENCE	PP 100	40	10 20	62 57	P C	404192D 404102	WIRELESS NETWORKS	DD TOO	20	23	35	20	P
404186	IAR PRACTICE I (CN & MWE)	TW 50	20	10 39	37	PC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			40	P D
404186	LAB PRACTICE I (CN & MWE)	OR 50	20		24	PC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20		32	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20		32	РC	404195	PROJECT PHASE II	TW 100	40			93	P
404188	PROJECT PHASE I	OR 50	20		37	P C	404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50	20			42	P
GRAND TOTAL	= 962/1500 , RESULT:FIRST CL	ASS												
	51 MHATRE AJINKYA AVINASH									• • • •			• • • •	• • • • •
	SEM.:1							SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	12 47	59	РC	404189	MOBILE COMMUNICATION	PP 100	40	19	41	60	P
404182	COMPUTER NETWORKS	PP 100	40	20 33	53	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	06	48	54	P
404183	MICROWAVE ENGINEERING	PP 100	40	13 44	57	P C	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	23	44	67	Р
404184A 404185C	DIGITAL IMAGE PROCESSING	PP 100	40	17 50	6 / 5 Q	P C	404192D 404192	MIKETESS NEIMOKKS	PP 100	20	24	43	0 / 21	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20	13 44	22	PC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20		33	PС	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			26	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20		21	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20		25	P	404195	PROJECT PHASE II	TW 100	40			85	P
404188	PROJECT PHASE I	OR 50	20		42	P C	404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50	20			42	P
GRAND TOTAL	= 938/1500 , RESULT:FIRST CL	224												
GIGIND TOTAL	i - 550/1500 , REBUELTIRET CE	100				F	RESULT RES	ERVED FOR BACKLOGS.						
B1200531	52 MHETRE SAYALI NARAYAN			MEERA	• • • •		,7152	7401F ,PICT ,B12009	 53152	• • • •				
	SEM.:1							SEM.:2						
404181	VIST DESIGN & TECHNOLOGY	PP 100	40	19 62	81	РС	404189	MORILE COMMINICATION	PP 100	40	22	43	65	Þ
404182	COMPUTER NETWORKS	PP 100	40	26 43	69	PC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	20	56	76	P
404183	MICROWAVE ENGINEERING	PP 100	40	21 54	75	РC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	21	49	70	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	23 60	83	PС	404192D	WIRELESS NETWORKS	PP 100	40	29	55	84	P
404185C	PLCS AND AUTOMATION	PP 100	40	27 51	78	P C	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			45	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20		36	P C	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20		45	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			40	P
404187	LAB PRAC. II(VLSI & ELEC. I)	.T.M 20	20		39	ь с	404194 40410E	LAB PRAC. IV(ELECTIVE III)	PK 50	20			39 01	D F
404187	PROJECT PHASE T	OR 50	20 20		43 42	PC	404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	TW 100	20			91 41	P
									-11 50					-
GRAND TOTAL	= 1184 + 10/1500 , RESULT:FI	RST CLASS	WITH	DISTINCT	CION	[& C	0.163]							

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH: DATE :	04 JUL 2018 CENTRE: PUN	CS &TELE IE INSTIT	COMU UTE	OF CO	TION) OMPUTER	TEC	HNOLOG	Y, PUNE.		PAGE NO. 5	51					
NOTE: FIR OTHER LIN	ST LINE : SEAT NO., NAME OF ES: HEAD OF PASSING, MAX. MA	THE CAND	IDAT .PAS	E, N S MAF	MOTHER, RKS, IN	PER T. M	MANENT IARKS,	REG. NO. TH. MARKS	, PREVIOUS SEAT NO. , TOTAL MARKS OBTAIN	, COLLEGI	E, SEAT PASS/FAIL	NO.	:PREV]	OUS C	'ARRY	OVER
B12005315	3 MINCHANAL VISHVARADHYA SA	NGANNA		5	SHANTA			,7152	7402D ,PICT	,B12005	53153					
404181 404182 404183 404184A 404185C 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I = 1110/1500 . RESULT:FIRST CI	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20	12 20 20 22 13	56 35 44 61 47	68 55 64 83 60 34 41 40 41	P C C C C C C C C C C C C C C	404189 404190 404191D 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SOFT COMPUTING WIRELESS NETWORKS LAB PRACTICE III(MOTELLE LAB PRACTICE III(MOTELLE LAB PRACTICE PROJECT PHASE II PROJECT PHASE II	TION SYS. & BCS) & BCS) VE III) VE III)	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20	25 20 19 28	46 52 46 52	71 72 65 80 37 43 43 45 93	P P P P P P P P
B12005315	4 MISHRA AYUSHI DEEPAK	• • • • • • • •			 SHIPRA		• • • • • •	,7152	7404L ,PICT	,B12005	 53154			• • • • •	• • • •	• • • • •
404181 404182 404183 404184A 404185C 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20 20	20 23 26 20 20	51 36 51 60 51							40 40 40 20 20 20 20 20	26 26 24 27	59 53 49 50	85 79 73 77 45 40 48 46 85 40	P P P P P P P
	= 1179/1500 , RESULT:FIRST CI															
B12005315 404181 404182 404183 404184A 404185C 404186 404186 404187 404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I = 984/1500 , RESULT:FIRST CLA	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50		Ċ	JAYSHRE	E		,7152	7405J ,PICT	,B12005	53155					P P P P P P P P

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PU	ICS &TELECOM NE INSTITUTE	OF COMPU) TER TECHNOLOG	GY, PUNE.	PAGE NO.	52	
NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, MAX. M	THE CANDIDA ARKS, MIN.PA	TE, MOTH SS MARKS,	ER, PERMANEN' INT. MARKS,	Γ REG. NO. TH. MARKS	, PREVIOUS SEAT NO., COLLEG , TOTAL MARKS OBTAINED, P/F:	E, SEAT NO. PASS/FAIL, C:PREVI	IOUS CARRY OVER
B120053157 MODOKAR SAYALI RAJESH	• • • • • • • • • • • • • • • • • • • •	VIDY		,7152	7408C ,PICT ,B1200	53157	
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185B ELECTRONIC PRODUCT DESIGN 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 PROJECT PHASE I	PP 100 40 PP 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20 OR 50 20	18 4 23 4 23 4 20 5 21 5	8 66 P C 6 69 P C 9 79 P C 3 74 P C 37 P C 42 P C 28 P C 24 P C 36 P C	404189 404190 404191D 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. SOFT COMPUTING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 24 PP 100 40 16 PP 100 40 19 PP 100 40 29 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	50 74 P 52 68 P 49 68 P 52 81 P 34 P 38 P 26 P 39 P 91 P 42 P
GRAND TOTAL = 1085/1500 , RESULT:FIRST C	LASS WITH DI	STINCTION					
B120053158 MRADUL BADOLE		SUDH	A	,7152	7414H ,PICT ,B1200	53158	
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 OR 50 20 OR 50 20 OR 50 20	14 4 24 3 19 3 16 4 21 3	5 59 P C 3 57 P C 9 58 P C 0 56 P 3 54 P C 36 P C 34 P C 30 P C 35 P C 38 P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS)	PP 100 40 20 PP 100 40 21 PP 100 40 08 PP 100 40 26 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	34 54 P 47 68 P 42 50 P 33 59 P 40 P 37 P 36 P 38 P 92 P 46 P
GRAND TOTAL = $977/1500$. RESULT:FIRST CI	ASS						
B120053159 MUGAVE GOPINATH SHRIDHAR		SANG	ITA	,7140	1013J ,PICT ,B1200	53159	
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185B ELECTRONIC PRODUCT DESIGN 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	01 A 08 3 09 1 02 A 12 4	A F 2\$ 40 P 7 F A F 4 56 P 36 P C 20 P C 25 P C 22 P 26 P C	404189 404190 404191C 404192D 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 15 PP 100 40 02 PP 100 40 13 PP 100 40 20 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	31 46 P AA F 38 51 P 31 51 P 21 P 25 P 24 P 33 P 42 P 22 P
GRAND TOTAL =/1500 , RESULT:FAILS [\$ 0.1]		I	RESULT RES	ERVED FOR BACKLOGS.		

RESULT RESERVED FOR BACKLOGS.

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE:	04 JUL 2018 CENTRE: PUN	CS &TELE IE INSTIT	UTE	OF CO	MPUTER	TEC	HNOLOG	Y, PUNE.	PAGE NO. 53	
NOTE: FIR	ST LINE : SEAT NO., NAME OF ES: HEAD OF PASSING, MAX. MA	THE CAND ARKS, MIN	.PAS	E, M SMAR	OTHER, KS, IN	PER T. M	MANEN'I IARKS ,	TH. MARKS	., PREVIOUS SEAT NO., COLLEGE, SEAT NO. S. TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY	OVER
B12005316	0 NAGARE SWAPNALI SANJAY			P	USHPA			,7163		
404181 404182 404183 404184A 404185B 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20 20	07 24 21 24 24	48 45 55 57 47	55 69 76 81 71 38 32 34 33 37	P C C C C C C C C C C C C C C C C C C C	404189 4041910 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 20 53 73 BROADBAND COMMUNICATION SYS. PP 100 40 27 49 76 AUDIO VIDEO ENGINEERING PP 100 40 23 48 71 WIRELESS NETWORKS PP 100 40 28 53 81 LAB PRACTICE III(MC & BCS) TW 50 20 39 LAB PRACTICE III(MC & BCS) OR 50 20 39 LAB PRAC. IV(ELECTIVE III) TW 50 20 42 LAB PRAC. IV(ELECTIVE III) PR 50 20 36 PROJECT PHASE II TW 100 40 84 PROJECT PHASE II PR 50 20 42	P P P P P P P P
B12005316	1 NANDNAWARE AKASH JAGNNATH	I		D	RAUPAD	Α		,7110	00910E ,PICT ,B120053161	
	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I = 910/1500 , RESULT:FIRST CLF		40 40 40 40 20 20 20 20	13 09 12 19 19	36 37 42 43 45	49 46 54 62 64 24 32 21 25 46	P C P C C P C C P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 18 33 51 BROADBAND COMMUNICATION SYS. PP 100 40 15 42 57 AUDIO VIDEO ENGINEERING PP 100 40 14 45 59 WIRELESS NETWORKS PP 100 40 26 42 68 LAB PRACTICE III(MC & BCS) TW 50 20 27 LAB PRACTICE III(MC & BCS) OR 50 20 32 LAB PRAC. IV(ELECTIVE III) TW 50 20 25 LAB PRAC. IV(ELECTIVE III) PR 50 20 30 PROJECT PHASE II TW 100 40 92 PROJECT PHASE II PR 50 20 46	P P P P P P P P
B12005316 404181 404182 404183 404184A 404185C 404186 404186 404187 404187	2 NANGARE AKSHAY ARUN	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 PR 50 OR 50		A	NITA			,7152	SEM.:2 MOBILE COMMUNICATION PP 100 40 20 36 56 BROADBAND COMMUNICATION SYS. PP 100 40 22 33 55 SOFT COMPUTING PP 100 40 18 29 47 WIRELESS NETWORKS PP 100 40 19 37 56 LAB PRACTICE III(MC & BCS) TW 50 20 39 LAB PRACTICE III(MC & BCS) OR 50 20 38 LAB PRAC. IV(ELECTIVE III) TW 50 20 32 LAB PRAC. IV(ELECTIVE III) PR 50 20 37 PROJECT PHASE II TW 100 40 94 PROJECT PHASE II PR 50 20 46	P P P P P P P

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)
DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY. PUNE.

BRANCH:71-B.E. (2012 PAT.)(ELECTRONDATE: 04 JUL 2018 CENTRE: PU	NICS &TELECOM JNE INSTITUTE	UNICATIO OF COME	ON) PUTER TEC	CHNOLOG	Y, PUNE.	PAGE NO.	54							
NOTE: FIRST LINE : SEAT NO., NAME OF	THE CANDIDA	re, mon	THER, PER	RMANENT	REG. NO.	, PREVIOUS SEAT NO., COLLEG	E, SEAT NO.							
OTHER LINES: HEAD OF PASSING, MAX. N														
B120053163 NATU HRUSHIKESH PRADIP	• • • • • • • • • • • • • • • • • • • •	SAV	/ITA		,7140	1032E ,PICT ,B1200	53163							
SEM.:1						SEM.:2								
404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185C PLCS AND AUTOMATION	PP 100 40	17	36 53	РC	404189	MOBILE COMMUNICATION	PP 100 40 23	38 61 P 37 60 P						
404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING	PP 100 40 PP 100 40	16 10	38 54 36 46	P C	404190 404191C	AUDIO VIDEO ENGINEERING	PP 100 40 23 PP 100 40 13	37 60 P 40 53 P						
404184B EMBEDDED SYSTEMS & RTOS	PP 100 40	21	34 55	РC	404192D	WIRELESS NETWORKS	PP 100 40 28	27 65 D						
404185C PLCS AND AUTOMATION	PP 100 40	19	32 51	РC	404193	LAB PRACTICE III(MC & BCS)	TW 50 20	29 P						
404185C PLCS AND ACTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	TW 50 20		37	Þ	404193 404194	LAB PRACTICE III(MC & BCS)	OR 50 20	30 P 32 D						
404187 LAB PRAC. II(VLSI &ELEC. I)	TW 50 20		25	P	404194	LAB PRAC. IV(ELECTIVE III)	PR 50 20	33 P						
404187 LAB PRAC. II(VLSI &ELEC. I)	PR 50 20		25	P	404195	PROJECT PHASE II	TW 100 40	96 P						
404188 PROJECT PHASE I	OR 50 20		38	РC	404195	LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50 20	45 P						
GRAND TOTAL = 920/1500 , RESULT:FIRST CI	LASS													
B120053164 NAVRANGE ROHIT VILASRAO			 [SHALI		,7152		53164							
B120053164 NAVRANGE ROHIT VILASRAO VAISHALI ,71527437G ,PICT ,B120053164														
SEM.:1 404181 VIST DESTGN & TECHNOLOGY	PP 100 40	17	34 51	PC	404189	SEM.:2 MOBILE COMMUNICATION	PP 100 40 24	32 56 P						
404182 COMPUTER NETWORKS	PP 100 40	19	32 51	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100 40 16	46 62 P						
404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING	PP 100 40	20	42 62	P	404191C	AUDIO VIDEO ENGINEERING	PP 100 40 22	48 70 P						
404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION	PP 100 40	15 20	52 67	P C	404192D 404193	WIRELESS NETWORKS	PP 100 40 28	46 74 P						
404186 LAB PRACTICE I (CN & MWE)	TW 50 20	20	40	PC	404193	LAB PRACTICE III(MC & BCS)	OR 50 20	38 P						
404186 LAB PRACTICE I (CN & MWE)	OR 50 20		37	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50 20	30 P						
404187 LAB PRAC. II(VLSI &ELEC. I) 404187 LAB PRAC. II(VLSI &ELEC. I)	TW 50 20		28	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR 50 20	38 P						
404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	OR 50 20		33	P C	404195	LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50 20	90 P 41 P						
GRAND TOTAL = 989/1500 , RESULT:FIRST CI														
B120053165 NEEL PARAG PENDHARKAR		ILA	Ą		,7152	.7438E ,PICT ,B1200	53165							
SEM.:1						SEM.:2								
404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING	PP 100 40	08	41 49	P C	404189	MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS.		36 57 P 35\$ 40 P						
404183 MICROWAVE ENGINEERING	PP 100 40	09	33 42	PC	404190 404191C	AUDIO VIDEO ENGINEERING	DD 100 40 13	355 40 P 38 51 P						
404184A DIGITAL IMAGE PROCESSING	PP 100 40	15	40 55	РC	404192D	MIDELECC METWODKC	DD 100 40 20	36 56 D						
					404193	LAB PRACTICE III(MC & BCS)	TW 50 20	34 P						
404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE)	TW 50 20		40 36	P C	404193 404194	LAB PRACTICE III(MC & BCS)	OR 50 20	36 P 35 P						
404187 LAB PRAC. II(VLSI &ELEC. I)	TW 50 20		30	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR 50 20	37 P						
404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PR 50 20		41	P C	404195	LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	TW 100 40	93 P						
404188 PROJECT PHASE I	OR 50 20		42	РC	404195	PROJECT PHASE II	PR 50 20	44 P						
GRAND TOTAL = 915/1500 , RESULT:FIRST CI	LASS [\$ 0.1]												

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL	(2012 PAT.)(ELECTRON 2018 CENTRE: PU	NE INSTITU	JTE (OF CON	ION) IPUTER	TEC	HNOLOG	Y, PUNE.		PAGE NO.	55					
NOTE: FIRST LINE OTHER LINES: HEA	: SEAT NO., NAME OF D OF PASSING, MAX. M	THE CANDI ARKS, MIN	IDAT: .PAS:	E, MO S MARI	OTHER, (S, IN	PER T. M	MANENT ARKS, '	REG. NO. TH. MARKS	, PREVIOUS SI , TOTAL MARKS	EAT NO., COLLE OBTAINED, P/F	GE, SEAT :PASS/FAII	NO.	:PREVI	OUS C	ARRY	OVER
B120053166 NEH				N]	LLU			,7152	7439C ,PIC	 г ,В120	053166					
404181 VLSI DE 404182 COMPUTE 404183 MICROWA 404184A DIGITAI 404185B ELECTRO 404186 LAB PRA 404186 LAB PRA 404187 LAB PRA 404187 LAB PRA 404187 LAB PRA 404188 PROJECT	-	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40	15 17	28 32	43 49	P C P C	404190 404191D	BROADBAND COM SOFT COMPUTIN	NICATION MMUNICATION SYS NG WORKS III(MC & BCS) III(MC & BCS) (ELECTIVE III) (ELECTIVE III) E II	PP 100 PP 100	40 40	08 07	30 28 33\$ 45	40	F P
GRAND TOTAL = $/15$	00 , RESULT: FAILS [\$ 0.1]														
GRAND TOTAL =/1500 , RESULT:FAILS [\$ 0.1]																
SEM 404181 VLSI DE 404182 COMPUTE 404183 MICROWA 404184B EMBEDDE 404185C PLCS AN 404186 LAB PRA 404186 LAB PRA 404187 LAB PRA 404187 LAB PRA 404187 LAB PRA 404188 PROJECT	.:1 SIGN & TECHNOLOGY R NETWORKS VE ENGINEERING D SYSTEMS & RTOS D AUTOMATION CTICE I (CN & MWE) CTICE I (CN & MWE) C. II(VLSI & ELEC. I) PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20 20	14 29 22 19 25	40 37 37 53 41	54 66 59 72 66 38 39 35 39	P C P C C P C C P C C P C C	404189 404190 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.: 2 MOBILE COMMUI BROADBAND COI AUDIO VIDEO I WIRELESS NETI LAB PRACTICE LAB PRACTICE LAB PRAC. IV LAB PRAC. IV PROJECT PHASI	NICATION MMUNICATION SYS ENGINEERING WORKS III(MC & BCS) III(MC & BCS) (ELECTIVE III) (ELECTIVE III) E II	PP 100	40	27 21	43 50 48 48	67 77 69 77 39 43 24 38 80 34	P P
	1500 , RESULT:FIRST C								• • • • • • • • • • • • • • • • • • • •					. .		
B120053168 NIM	JE PIYUSH MADAN			R.A	ANJANA			,7152	7445H ,PIC	Г ,В120	053168					
	.:1 SIGN & TECHNOLOGY R NETWORKS VE ENGINEERING D SYSTEMS & RTOS D AUTOMATION CTICE I (CN & MWE) CTICE I (CN & ELEC. I) C. II(VLSI & ELEC. I) PHASE I 1500 , RESULT:FIRST C					75 71 71 83 65 48 32 42 35 42	P C C C C C C C C C C C C C C C C C C C	404189 4041910 404191C 404193 404193 404193 404194 404194 404195 404195	BROADBAND COM	NICATION MMUNICATION SYS ENGINEERING WORKS III(MC & BCS) III(MC & BCS) (ELECTIVE III) (ELECTIVE III) E II	PP 100 PP 100	40 40	25 23	55 52 47 51	81 77 70 80 45 35 40 39 87 40	P P

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	1.71-B.E. (2012 PAI.)(ELECTRON 04 JUL 2018 CENTRE: PUL	NE INSTIT	UTE	OF CO	. TON))MPUTEF	R TEC	CHNOLOG	Y, PUNE.	PAGE NO.	56					
NOTE: FI OTHER LI	RST LINE : SEAT NO., NAME OF TNES: HEAD OF PASSING, MAX. M	THE CAND ARKS, MIN	IDAT	E, N S MAF	MOTHER, RKS, IN	, PEF	MANENT	REG. NO. TH. MARKS	, PREVIOUS SEAT NO., COLLEG , TOTAL MARKS OBTAINED, P/F:	E, SEAT PASS/FAIL	NO.	:PREVI	OUS C	!ARRY	OVER
B1200531	69 NITNAWARE VISHWAJIT MADA	NLAL	• • • •		 IIRMAL			,7163	3641D ,PICT ,B1200	 53169	• • • •			• • • •	
	SEM.:1								SEM.:2						
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100	40	14	41	55	P C	404189	MOBILE COMMUNICATION	PP 100	40	17	29	46	P
404182	COMPUTER NETWORKS	PP 100	40	17	28	45	PC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	11	32	43	P
404163 4041841	DIGITAL IMAGE PROCESSING	PP 100 PP 100	40	U8	33 32¢	40	P D	404191C 404192D	WIRELESS NETWORKS	PP 100 PP 100	40	23	37 36	59 59	P D
404185C	PLCS AND AUTOMATION	PP 100	40	16	28	44	PС	404193	LAB PRACTICE III(MC & BCS)	TW 50	20	23	30	37	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			40	РC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			33	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			20	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			30	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			39	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			33	P C	404195	PROJECT PHASE II	J.M T00	40			89 42	Р
404100	PROJECT PHASE I	OR 50	20			30	PC	404195	PROJECT PHASE II	PR 50	20			42	Р
GRAND TOTAL	_ = 881/1500	ECOND CLA	.SS	15 (). I I										
R1200531				· · · · · ·	FENΔ			7152	7449T. DTCT R1200	 53170	• • • •			• • • •	• • • • •
404101	SEM.:1	DD 100	4.0	1.0	5 6		D 0	404100	SEM.: 2	DD 100	4.0	0.17	4.0		_
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	20 TR	20	/4 50	P C	404189	MORITE COMMUNICATION	PP 100	40	27	48 56	75	P
404183	MICROWAVE ENGINEERING	PP 100	40	20	51	71	PC	404191D	SOFT COMPLITING	PP 100	40	13	52	65	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	21	51	72	PC	404192D	WIRELESS NETWORKS	PP 100	40	23	49	72	P
404185C	PLCS AND AUTOMATION	PP 100	40	13	32	45	РC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			35	РC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			32	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			38	PC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			41	P C	404195	PROJECT PHASE II	.I.M TOO	20			95 45	D P
404100	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 30	20			40	PC	404193	PROJECT PRASE II	PR 50	20			45	P
GRAND TOTAL	_ = 1106/1500 , RESULT:FIRST C	LASS WITH	DIS	STINCT	CION										
B1200531		HKUMAR	• • • •	1	IEETA			,7152	7450D ,PICT ,B1200	53171					
	SEM.:1								SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	18	49	67	РC	404189	MOBILE COMMUNICATION	PP 100	40	24	32	56	P
404182	COMPUTER NETWORKS	PP 100	40	27	40	67	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	23	43	66	P
404183	MICROWAVE ENGINEERING	PP 100	40	22	38	60	РC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	21	45	66	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	21	57	78	РC	404192D	WIRELESS NETWORKS	PP 100	40	29	47	76	P
404185C	PLCS AND AUTOMATION	PP 100	40	25	44	69	P C	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			35	P
404186 404186	LAB PRACTICE I (CN & MWE)	UB EU	∠∪ 2∩			პ წ 2 1	ъс БС	404193 40410 <i>1</i>	LAB PRACTICE III(MC & BCS)	UK 50	∠∪ 2∩			4 Z 4 O	D F
404187	TAB PRACTICE I (CN & MWE)	TW 50	20			33	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			41	PC	404195	PROJECT PHASE II	TW 100	40			95	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			45	РC	404195	PROJECT PHASE II	PR 50	20			46	P
	_ = 1092/1500 , RESULT:FIRST C														

GRAND TOTAL = 1092/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH: DATE :	04 JUL 2018 CENTRE: PUR	ICS &TELEC NE INSTITU	JTE (NICATI	LON) MPUTER	R TEC	'HNOLOG	Y, PUNE.	PAGE NO. 57					
NOTE: FIR	RST LINE : SEAT NO., NAME OF	THE CAND	 [DAT]	 Е, МО	 OTHER ,	PER	 MANENT	REG. NO.	PAGE NO. 57, PREVIOUS SEAT NO., COLLEGE,	EAT NO			• • • •	• • • • •
OTHER LIN	NES: HEAD OF PASSING, MAX. MA	ARKS, MIN.	PAS:	S MARI	KS, IN	IT. M	IARKS,	TH. MARKS	G, TOTAL MARKS OBTAINED, P/F:PASS/	AIL,	C:PREVI	COUS C	'ARRY	OVER
B12005317	72 PAHADE ABHIJEETKUMAR VIJ	AY		HI	EMLATA	7		,7140	1047C ,PICT ,B120053172					• • • • •
	SEM.:1								SEM.:2					
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	09	41	50	P C	404189	MOBILE COMMUNICATION PP	00 40	17	33	50	P
404182	COMPUTER NETWORKS	PP 100	40	13	38	51	P	404190	BROADBAND COMMUNICATION SYS. PP	00 40	19	29	48	P
404183 4041847	MICROWAVE ENGINEERING	PP 100	40 40	14	28 36	42	P C	4041910	AUDIO VIDEO ENGINEERING PP	00 40	11 25	40 41	66 51	P D
404185C	PLCS AND AUTOMATION	PP 100	40	23	28	51	PC	404193	LAB PRACTICE III(MC & BCS) TW	50 20	23	41	35	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			22	РC	404193	LAB PRACTICE III(MC & BCS) OR	50 20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			32	РC	404194	LAB PRAC. IV(ELECTIVE III) TW	50 20			24	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			20	РC	404194	LAB PRAC. IV(ELECTIVE III) PR	50 20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			33	P	404195	PROJECT PHASE II TW 3	00 40			85	P
404188	PROJECT PHASE I	OR 50	20			32	РC	404195	SEM.:2 MOBILE COMMUNICATION PP COMMUNICATION SYS. PP COMMUNICATIO	50 20			43	Р
GRAND TOTAL	= 861/1500 , RESULT:HIGHER SH	ECOND CLAS	SS				_							
	RESULT RESERVED FOR BACKLOGS.													
B12005317	73 PANDAGALE SHRUTI JAIWANT			Al	ATIV			,7152	7456C ,PICT ,B120053173					
	SEM.:1								SEM.:2					
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	15	61	76	РC	404189	MOBILE COMMUNICATION PP 1	00 40	26	43	69	P
404182	COMPUTER NETWORKS	PP 100	40	18	44	62	РC	404190	BROADBAND COMMUNICATION SYS. PP	00 40	20	54	74	P
404183	MICROWAVE ENGINEERING	PP 100	40	27	39 44	66	P C	404191C	AUDIO VIDEO ENGINEERING PP	00 40	23	45	68 77	Ь
404164A 404185C	DIGITAL IMAGE PROCESSING DI.CS AND AUTOMATION	PP 100 PP 100	40	18	44	62	PC	4041920	IAR DRACTICE III (MC & RCS) TW	50 20	29	40	28	P D
404186	LAB PRACTICE I (CN & MWE)	TW 50	20	10		39	PC	404193	LAB PRACTICE III(MC & BCS) OR	50 20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			34	РC	404194	LAB PRAC. IV(ELECTIVE III) TW	50 20			31	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			34	РC	404194	LAB PRAC. IV(ELECTIVE III) PR	50 20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			36	РC	404195	PROJECT PHASE II TW 1	00 40			87	P
404188	PROJECT PHASE I	OR 50	20			40	РC	404195	SEM.:2 MOBILE COMMUNICATION PP COMMUNICATION SYS. PP COMMUNICATIO	50 20			41	P
GRAND TOTAL	= 1066/1500 RESULT: FIRST CI	HTTW 224.	יצדת	TTNCT	T () N									
	SEM.:1								SEM.:2					
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	16	43	59	РC	404189	MOBILE COMMUNICATION PP 1	00 40	22	31	53	P
404182	COMPUTER NETWORKS	PP 100	40	26	39	65	РC	404190	BROADBAND COMMUNICATION SYS. PP	00 40	19	48	67	P
404183	MICROWAVE ENGINEERING	PP 100	40	20	44	64	PC	404191D	SOFT COMPUTING PP 1	00 40	22	40	62	P
404184A 404185	DIGITAL IMAGE PROCESSING	PP 100	40	24	P.3	85 72	PC	404192A	BIOMEDICAL SIGNAL PROCESSING PP .	50 40	29	45	/4 27	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20	20	JJ	7 <i>3</i>	P C	404193	IAB PRACTICE III (MC & BCS) OR	50 20			40	P P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			43	P C	404194	LAB PRAC. IV(ELECTIVE III) TW	50 20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			40	РC	404194	LAB PRAC. IV(ELECTIVE III) PR	50 20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			36	РC	404195	PROJECT PHASE II TW 1	00 40			96	P
404188	PROJECT PHASE I	OR 50	20			43	РC	404195	SEM.:2 MOBILE COMMUNICATION PP I BROADBAND COMMUNICATION SYS. PP I SOFT COMPUTING PP I BIOMEDICAL SIGNAL PROCESSING PP I LAB PRACTICE III(MC & BCS) TW LAB PRACTICE III(MC & BCS) OR LAB PRAC. IV(ELECTIVE III) TW LAB PRAC. IV(ELECTIVE III) PR PROJECT PHASE II TW I PROJECT PHASE II PR	50 20			46	P
GRAND TOTAL	= 1099/1500 , RESULT:FIRST CI	LASS WITH	DIS'	TINCT	ION									

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	1.71-B.E. (2012 PAI.)(ELECTRON E 04 JUL 2018 CENTRE: PU	NE INSTITU	JOMO. JTE	OF CO	ION) MPUTER	TEC	HNOLOG	Y, PUNE.	PAGE NO. 5	58					
NOTE: FI OTHER LI	RST LINE : SEAT NO., NAME OF TNES: HEAD OF PASSING, MAX. M	THE CANDI ARKS, MIN	IDAT .PAS	E, MO S MARI	OTHER, KS, IN	PER IT. M	MANENT IARKS,	REG. NO. TH. MARKS	, PREVIOUS SEAT NO., COLLEGE, TOTAL MARKS OBTAINED, P/F:	E, SEAT PASS/FAIL	NO.	:PREVI	OUS C	CARRY	OVER
B1200531	.75 PARATE DEVYANI VIJAY	• • • • • • • • •		V	ANDANA		• • • • • •	,7152	7461K ,PICT ,B12005	 53175					
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY	PP 100	40	13	40	53	РC	404189	SEM.:2 MOBILE COMMUNICATION	PP 100	40	24	44	68	P
404182 404183 404184B 404185C	COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION	PP 100 PP 100 PP 100 PP 100	40 40 40 40	27 25 25 18	37 49 52 43	64 74 77 61	P C P C P C	404190 404191C 404192D 404193	BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS)	PP 100 PP 100 PP 100 TW 50	40 40 40 20	20 21 29	53 47 45	73 68 74 36	P P P
404186 404187 404187 404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50 TW 50 PR 50 OR 50	20 20 20 20 20			44 41 41 42	P C P C P C P C	404193 404194 404194 404195 404195	LAB PRACTICE III (MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	TW 50 PR 50 TW 100 PR 50	20 20 20 40 20			40 38 94 44	P P P P
GRAND TOTAL	J = 1115/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT:	ION										
B1200531	.76 PARDESHI ABHIJEET VIJAYS	INGH		AI	RCHANA			,7163	3643L ,PICT ,B12005	3176					
404181 404182 404183	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING	PP 100 PP 100 PP 100	40 40 40	18 27 23	54 34 40	72 61 63	P C P C P C	404189 404190 404191C	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING	PP 100 PP 100 PP 100	40 40 40	21 22 24	35 41 41	56 63 65	P P P
404184A 404185C 404186 404186 404187 404187 404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 20 20 20 20 20 20	13 19	58 36	71 55 40 43 40 28 38	P C P C P C P C	404192D 404193 404193 404194 404194 404195 404195	WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 20 20 20 20 40 20	29	42	71 40 46 40 36 93 45	P P P P P
GRAND TOTAL	= 1066/1500 . RESULT: FIRST C	LASS WITH	DTS	TINCT	TON										
B1200531	.77 PARDESHI SHUBHAM DINESH		• • • •	D1	EEPIKA			,7163	3644J ,PICT ,B12005	53177				• • • •	• • • • •
404181 404182 404183 404184A 404185C 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I = 1066/1500 , RESULT:FIRST C	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20 20	15 17 24 14 18	47 40 44 64 46										

GRAND TOTAL = 1066/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PUI	NE INSTIT	UTE	OF CO	MPUTER	TEC	HNOLOG	Y, PUNE.	PA	AGE NO. 5	59					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	 TATI	 ъ. М	OTHER.	PER	MANENT	REG. NO.	PREVIOUS SEAT NO	COLLEGE	SEAT	NO.				• • • • •
OTHER LI	NES: HEAD OF PASSING, MAX. MAX. MAX. MAX. MAX. MAX. MAX. MAX.	ARKS, MIN	.PAS	S MAR	KS, IN	T. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED), P/F:E	PASS/FAIL	, (::PREVI	OUS C	ARRY	OVER
B1200531	78 PATHAK NISHANT MANOJ				 INAKSH	 I		,7152	7466L ,PICT	,B12005	 53178					• • • • •
	SEM.:1								SEM.:2							
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100	40	12	36	48	P C	404189	MOBILE COMMUNICATION		PP 100	40	25	40	65	P
404182	COMPUTER NETWORKS	PP 100	40	18 12	29	4 /	PC	404190	BROADBAND COMMUNICAT	LON SYS.	PP 100	40	21 21	46	67	P
404183	DIGITAL IMAGE PROCESSING	PP 100	40	22	57 58	80	PC	404191D 404192D	WIRELESS NETWORKS		PP 100 PP 100	40	29	38	67	P
404185C	PLCS AND AUTOMATION	PP 100	40	12	37	49	РC	404193	LAB PRACTICE III(MC 8	BCS)	TW 50	20		50	36	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			38	РC	404193	LAB PRACTICE III(MC 8	BCS)	OR 50	20			46	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			44	P C	404194	LAB PRAC. IV(ELECTIVE	E III)	TW 50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			39	PC	404194	LAB PRAC. IV(ELECTIVE	E III)	PR 50	20			31	P
404187	PROJECT PHASE T	OR 50	20			43	PC	404195	PROJECT PHASE II		PR 50	20			93 43	P P
101100	TROOLET TIMES T	010 30	20			13	1 0	101173	INCOLCT TIMES II		110 50	20			13	-
GRAND TOTAL	. = 103//1500 , RESULT:FIRST CI	LASS WITH	בבע ו	LTNC.I.	TON											
D1 200E 21	79 PATIL HRUSHIKESH PRAMOD		• • • •					7150	7470 T DIGT	D1200		• • • •			• • • •	• • • • •
P1700331	179 PAILL HRUSHIRESH PRAMOD			1/	TAFDII	A		, /152	74700 ,PICI	,612003	031/9					
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	19	47	66	РC	404189	MOBILE COMMUNICATION		PP 100	40	24	41	65	P
404182	COMPUTER NETWORKS	PP 100	40	26	38	64	P C	404190	BROADBAND COMMUNICAT	ION SYS.	PP 100	40	24	40	64	P
404183 404194D	MICROWAVE ENGINEERING	PP 100	40	25	51 5⊥	76	PC	404191D	SOFT COMPUTING		PP 100	40	24	47 50	71	Р
404164B 404185D	ARTIFICIAL INTELLIGENCE	PP 100	40	23 18	48	66	PC	4041920	TAR PRACTICE TIT(MC A	BCS)	TW 50	20	29	50	48	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20	10	10	36	РC	404193	LAB PRACTICE III(MC 8	BCS)	OR 50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			43	РC	404194	LAB PRAC. IV(ELECTIVE	E III)	TW 50	20			46	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			45	РC	404194	LAB PRAC. IV(ELECTIVE	E III)	PR 50	20			45	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			37	P C	404195	PROJECT PHASE II		TW 100	40			97	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			42	PC	404195	PROJECT PHASE II		PR 50	20			4 /	Р
GRAND TOTAL	= 1157/1500 , RESULT:FIRST CI	LASS WITH	DIS	TINCT	ION											
B1200531	80 PATIL PRAJAKTA BHAUSAHEB			v	IJAYA			,7152	7472E ,PICT	,B12005	53180					
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	23	49	72	РC	404189	MOBILE COMMUNICATION		PP 100	40	25	40	65	P
404182	COMPUTER NETWORKS	PP 100	40	19	44	63	РC	404190	BROADBAND COMMUNICAT	ION SYS.	PP 100	40	19	48	67	P
404183	MICROWAVE ENGINEERING	PP 100	40	20	53	73	P C	404191C	AUDIO VIDEO ENGINEER	ING	PP 100	40	20	46	66	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	18 17	58 51	/ b	PC	404192A	BIOMEDICAL SIGNAL PRO	DCESSING	DD TOO	20	29	58	8 /	P
404186	IAB PRACTICE I (CN & MWE)	TW 50	20	Ι/	31	41	PC	404193	LAB PRACTICE III (MC 8	RCS)	OR 50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			42	PC	404194	LAB PRAC. IV(ELECTIVE	E III)	TW 50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			45	РC	404194	LAB PRAC. IV(ELECTIVE	E III)	PR 50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			34	P C	404195	PROJECT PHASE II		TW 100	40			95	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			41	РC	404195	PROJECT PHASE II		PR 50	20			45	Р
	- 1159/1500 preiii m. rtpem ei															

GRAND TOTAL = 1158/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PU	NICS &TELECOM INE INSTITUTE	OF COMPUT	ER TECHNOLO	GY, PUNE.	PAGE NO.	60								
NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, MAX. M	THE CANDIDA MARKS, MIN.PA	TE, MOTHE SS MARKS,	R, PERMANEN INT. MARKS,	T REG. NO. TH. MARKS	, PREVIOUS SEAT NO., COLLEG S, TOTAL MARKS OBTAINED, P/F:	E, SEAT NO. PASS/FAIL, C:PREVI	IOUS CARRY OVER							
B120053181 PATIL ROHINI JIVAN		RENUK	A	,7163	33645G ,PICT ,B1200	53181								
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 OR 50 20 OR 50 20 OR 50 20	20 59 27 43 25 68 25 58 24 49	79 P C 70 P C 93 P C 83 P C 73 P C 41 P C 44 P C 36 P C 34 P C 44 P C	404189 404190 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS)	PP 100 40 28 PP 100 40 20 PP 100 40 24 PP 100 40 29 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20 PR 50 20	46 74 P 60 80 P 46 70 P 48 77 P 39 P 42 P 38 P 36 P 93 P 43 P							
OKAND TOTAL - 1107/1300 , KESOLITIKSI CHASS WITH DISTINCTION														
GRAND TOTAL - ITO7/ISOU , RESULTIFICAT CLASS WITH DISTINCTION														
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING	PP 100 40 PP 100 40 PP 100 40	16 55 21 36 15 46	71 P C 57 P C 61 P C	404189 404190 404191C	CFM · 2	PP 100 40 22 PP 100 40 20 PP 100 40 21	39 61 P 48 68 P 50 71 P 40 69 P 36 P 38 P 24 P 39 P 91 P 42 P							
GRAND TOTAL = 1062/1500 , RESULT:FIRST C	CLASS WITH DI	STINCTION												
B120053183 PATIL SHRUTI BABASAHEB	• • • • • • • • • • • • • • • • • • • •	REKHA		,7163	33646E ,PICT ,B1200	53183	• • • • • • • • • • • • • • • • • • • •							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I GRAND TOTAL = 1225/1500 , RESULT:FIRST C	PP 100 40 PP 50 20 PR 50 20 PR 50 20 OR 50 20	16 58 28 53 22 55 24 66 19 53	74 P C 81 P C 77 P C 90 P C 72 P C 48 P C 43 P C 39 P C 38 P C 43 P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 25 PP 100 40 29 PP 100 40 23 PP 100 40 30 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	59 84 P 57 86 P 52 75 P 48 78 P 41 P 42 P 38 P 38 P 93 P 45 P							
GIVATAN TOTAL - I772/1200 ' KEPOPI-LIKPI (THOS MIIH DI	DITINCTION												

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 61													
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER													
B120053184 PAWAR ANKUSH SANJAY RANJANA ,71633647C ,PICT ,B120053184													
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 18 57 75 P C 404189 MOBILE COMMUNICATION PP 100 40 22 47 404182 COMPUTER NETWORKS PP 100 40 29 46 75 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 19 54 404183 MICROWAVE ENGINEERING PP 100 40 24 59 83 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 24 45 404184A DIGITAL IMAGE PROCESSING PP 100 40 20 55 75 P C 404192D WIRELESS NETWORKS PP 100 40 28 50 404185C PLCS AND AUTOMATION PP 100 40 19 49 68 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 404186 LAB PRACTICE I (CN & MWE) TW 50 20 404186 LAB PRACTICE I (CN & MWE) OR 50 20 43 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 42 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 38 P 404195 PROJECT PHASE II TW 100 40 40 404188 PROJECT PHASE II OR 50 20 38 P C 404195 PROJECT PHASE II PR 50 20	69 P 73 P 69 P 78 P 43 P 40 P 38 P 35 P 90 P 45 P												
GRAND TOTAL = 1157/1500 RESULT: FIRST CLASS WITH DISTINCTION													
GRAND TOTAL = 1157/1500 , RESULT:FIRST CLASS WITH DISTINCTION B120053185 PAWAR GAUTAMI RAMCHANDRA BHAGYASHREE ,71527480F ,PICT ,B120053185													
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 19 59 78 P C 404189 MOBILE COMMUNICATION PP 100 40 26 42 404182 COMPUTER NETWORKS PP 100 40 29 45 74 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 16 51 404183 MICROWAVE ENGINEERING PP 100 40 25 65 90 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 24 54 404184A DIGITAL IMAGE PROCESSING PP 100 40 20 65 85 P C 404192D WIRELESS NETWORKS PP 100 40 29 48 404185C PLCS AND AUTOMATION PP 100 40 20 52 72 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 404186 LAB PRACTICE I (CN & MWE) TW 50 20 42 P C 404193 LAB PRACTICE III(MC & BCS) OR 50 20 404186 LAB PRACTICE I (CN & MWE) OR 50 20 42 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 42 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 40 P C 404195 PROJECT PHASE II TW 100 40 404188 PROJECT PHASE II OR 50 20 40 P C 404195 PROJECT PHASE II PR 50 20	68 P 67 P 78 P 77 P 45 P 40 P 40 P 85 P 38 P												
GRAND TOTAL = 1166/1500 , RESULT:FIRST CLASS WITH DISTINCTION													
B120053186 PAWAR NIKITA NIVAS NEHA ,71527482B ,PICT ,B120053186	• • • • • • • • • •												
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 09 45 54 P C 404189 MOBILE COMMUNICATION PP 100 40 15 30 404182 COMPUTER NETWORKS PP 100 40 15 41 56 P 404190 BROADBAND COMMUNICATION SYS. PP 100 40 14 38 404183 MICROWAVE ENGINEERING PP 100 40 14 38 52 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 14 33 404184B EMBEDDED SYSTEMS & RTOS PP 100 40 07 45 52 P C 404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 13 28 404185D ARTIFICIAL INTELLIGENCE PP 100 40 05 36 41 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 404186 LAB PRACTICE I (CN & MWE) TW 50 20 30 P C 404193 LAB PRACTICE III(MC & BCS) OR 50 20 404186 LAB PRACTICE I (CN & MWE) OR 50 20 38 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 34 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 36 P C 404195 PROJECT PHASE II TW 100 40 404188 PROJECT PHASE I OR 50 20 36 P C 404195 PROJECT PHASE II PR 50 20	45 P 51 P 47 P 41 P 43 P 41 P 24 P 38 P 90 P 36 P												
GRAND TOTAL = 877/1500 , RESULT: HIGHER SECOND CLASS													

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PUI	NE INSTIT	UTE	OF COM	PUTER	TEC	HNOLOG	BY, PUNE.	PAGE	NO. 6	2					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	 IDAT	E, MO	THER,	PER	 MANENT	REG. NO.	, PREVIOUS SEAT NO., C	COLLEGE		NO.		• • • • •	• • • •	• • • • •
OTHER LI	NES: HEAD OF PASSING, MAX. MAX. MAX. MAX. MAX. MAX. MAX. MAX.	ARKS, MIN	.PAS	S MARK	S, IN	T. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED,	P/F:P	ASS/FAIL	, (::PREVI	OUS C	ARRY	OVER
B1200531				SA	VITA		• • • • • •	,7163	3648M ,PICT ,	B12005	3187		• • • • • •	• • • • •		• • • • •
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	15	56 40	71	P C	404189	MOBILE COMMUNICATION	1 070	PP 100	40	20	46	66 72	P
404182	MICROWAVE ENGINEERING	PP 100	40	20	62	82	PC	404190 404191C	AUDIO VIDEO ENGINEERING	l SIS.	PP 100	40	23	48	72 71	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	22	60	82	РC	404192A	BIOMEDICAL SIGNAL PROCE	SSING	PP 100	40	25	43	68	P
404185C	PLCS AND AUTOMATION	PP 100	40	19	44	63	РC	404193	LAB PRACTICE III(MC & B	BCS)	TW 50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			43	P C	404193	LAB PRACTICE III(MC & B	BCS)	OR 50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			3.7	PC	404194	LAB PRAC. IV(ELECTIVE I	III)	TW 50	20			38	P
404167	LAB PRAC. II(VLSI &ELEC. I)	IW 50 PR 50	20			33	PC	404194	PROJECT PHASE IT	/	TW 100	40			90	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			35	P C	404195	PROJECT PHASE II		PR 50	20			38	P
CDAND TOTAL	_ = 1113/1500 , RESULT:FIRST C	ACC MITMII	DIC	TTMOTT.	ONT											
GRAND IOIAL	1 = 1113/1500 , RESULI.FIRSI CI	LASS WITH	DI2	IINCII	OIN											
B1200531				GA	UTAMI			,7152	7484J ,PICT ,	B12005	3188					
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	12	53	65	РC	404189	MOBILE COMMUNICATION		PP 100	40	22	39	61	P
404182	COMPUTER NETWORKS	PP 100	40	15	44	59	РC	404190	BROADBAND COMMUNICATION	SYS.	PP 100	40	20	42	62	P
404183	MICROWAVE ENGINEERING	PP 100	40	20	45	65	РC	404191C	AUDIO VIDEO ENGINEERING	j	PP 100	40	16	33	49	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	18	49	67	P C	404192D	WIRELESS NETWORKS	\ a a \	PP 100	40	19	37	56	P
404185C 404186	PLCS AND AUTOMATION	PP 100	20	Ι/	40	5 / 37	P C	404193	LAB PRACTICE III (MC & B	BCS)	UB 50	20			39 40	D P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			38	PC	404194	LAB PRACTICE TITCHE & B	TT)	TW 50	20			39	P P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			37	РC	404194	LAB PRAC. IV(ELECTIVE I	III)	PR 50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			30	РC	404195	PROJECT PHASE II		TW 100	40			88	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACT I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			37	РC	404195	PROJECT PHASE II		PR 50	20			43	P
GRAND TOTAL	= 1006/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCTI	ON											
B1200531				JY	OTI			,7163	3649K ,PICT ,	B12005	 3189					• • • • •
404191	SEM.:1	חח 100	40	1 5	E /I	60	D C	404100	SEM.: 2		חח 100	40	2.2	4 5	67	D
404182	COMPUTER NETWORKS	PP 100	40	27	44	71	PC	404189	BROADBAND COMMUNICATION	I SYS.	PP 100	40	15	49	64	P
404183	MICROWAVE ENGINEERING	PP 100	40	20	66	86	РC	404191C	AUDIO VIDEO ENGINEERING	;	PP 100	40	23	47	70	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	17	63	80	РC	404192D	WIRELESS NETWORKS		PP 100	40	26	46	72	P
404185C	PLCS AND AUTOMATION	PP 100	40	25	50	75	P C	404193	LAB PRACTICE III(MC & B	BCS)	TW 50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			44	P C	404193	LAB PRACTICE III(MC & B	BCS)	OR 50	20			41	P
404186 404187	LAB PRACIICE I (CN & MWE)	UK 50	∠∪ 20			30 42	D C	404194 404194	LAB PRAC. IV(ELECTIVE I	. T T)	TM 20	∠∪ 2∩			40 36	D D
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			35	РC	404195	PROJECT PHASE II	/	TW 100	40			93	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ME) LAB PRACTICE I (CN & ME) LAB PRACTICE I (VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			38	РC	404195	PROJECT PHASE II		PR 50	20			45	P
	- 11/8/1500 PECHT TIPET C															

GRAND TOTAL = 1148/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.	PAGE NO. 63
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL	IOUS SEAT NO., COLLEGE, SEAT NO. MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
B120053190 PETHKAR KAUSTUBH VIJAY VIDYA ,71633650C	,PICT ,B120053190
	M.:2 COMMUNICATION PP 100 40 22 40 62 P AND COMMUNICATION SYS. PP 100 40 24 48 72 P VIDEO ENGINEERING PP 100 40 19 38 57 P SS NETWORKS PP 100 40 27 36 63 P ACTICE III(MC & BCS) TW 50 20 44 P ACTICE III(MC & BCS) OR 50 20 42 P AC. IV(ELECTIVE III) TW 50 20 34 P AC. IV(ELECTIVE III) PR 50 20 40 P T PHASE II TW 100 40 92 P T PHASE II PR 50 20 43 P
GRAND TOTAL = 1078/1500 , RESULT:FIRST CLASS WITH DISTINCTION	
B120053191 PHASE SIDHANT RAJKUMAR SAVITRI ,71633651M	,PICT ,B120053191
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 14 50 64 P C 404189 MOBILE 404182 COMPUTER NETWORKS PP 100 40 26 44 70 P C 404190 BROADBY 404183 MICROWAVE ENGINEERING PP 100 40 13 56 69 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 13 56 69 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404191D SOFT COMPUTER NETWORKS PP 100 40 20 58 78 P C 404193 LAB PR 100 40 20 58 78 P C 404193 LAB PR 100 40 20 58 78 P C 404193 LAB PR 100 40 20 58 78 P C 404194 LAB PR 100 40 40 40 40 40 40 40 40 40 40 40 40 4	M.:2 COMMUNICATION PP 100 40 23 41 64 P AND COMMUNICATION SYS. PP 100 40 18 46 64 P OMPUTING PP 100 40 17 47 64 P SS NETWORKS PP 100 40 22 40 62 P ACTICE III(MC & BCS) TW 50 20 38 P ACTICE III(MC & BCS) OR 50 20 45 P AC. IV(ELECTIVE III) TW 50 20 30 P AC. IV(ELECTIVE III) PR 50 20 40 P T PHASE II TW 100 40 88 P T PHASE II PR 50 20 40 P
GRAND TOTAL = 1072/1500 , RESULT:FIRST CLASS WITH DISTINCTION	
B120053192 POORSWANI NISHANT ARUN MEENA ,71527487C	,PICT ,B120053192
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 14 57 71 P C 404189 MOBILE 404182 COMPUTER NETWORKS PP 100 40 29 54 83 P C 404190 BROADBA 404183 MICROWAVE ENGINEERING PP 100 40 23 66 89 P C 404191C AUDIO V 404184B EMBEDDED SYSTEMS & RTOS PP 100 40 21 55 76 P C 404192D WIRELES 404185C PLCS AND AUTOMATION PP 100 40 18 44 62 P C 404193 LAB PRA 404186 LAB PRACTICE I (CN & MWE) TW 50 20 45 P C 404193 LAB PRA 404187 LAB PRACT. II(VLSI & ELEC. I) TW 50 20 46 P C 404194 LAB PRA 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 47 P C 404194 LAB PRA 404188 PROJECT PHASE I OR 50 20 43 P C 404195 PROJECT	M.:2 COMMUNICATION PP 100 40 26 53 79 P AND COMMUNICATION SYS. PP 100 40 25 51 76 P VIDEO ENGINEERING PP 100 40 21 38 59 P SS NETWORKS PP 100 40 27 33 60 P ACTICE III(MC & BCS) TW 50 20 45 P ACTICE III(MC & BCS) OR 50 20 46 P AC. IV(ELECTIVE III) TW 50 20 44 P AC. IV(ELECTIVE III) PR 50 20 45 P T PHASE II TW 100 40 94 P T PHASE II PR 50 20 44 P
GRAND TOTAL = 1201/1500 , RESULT: FIRST CLASS WITH DISTINCTION	

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH: 71-B.E. (2012 PAT.) (ELECTRON DATE: 04 JUL 2018 CENTRE: PU				HNOLOG	Y, PUNE.	PAGE NO.	64			
NOTE: FIRST LINE : SEAT NO., NAME OF	THE CANDIDA	TE, MO	THER, PER	MANENT	REG. NO.	, PREVIOUS SEAT NO., COLLEG	E, SEAT	NO.		
OTHER LINES: HEAD OF PASSING, MAX. N										
B120053193 PRACHI KADU		ARI	UNA		,7152	7490C ,PICT ,B1200	53193			
SEM.:1						SEM.:2				
404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS	PP 100 40	07 21	39 46 30 51	P C	404189 404190	MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS	PP 100	40 20 40 16		42 P 51 P
404183 MICROWAVE ENGINEERING	PP 100 40	15	36 51	РC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40 13	32	45 P
404181 VEST DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185D ARTIFICIAL INTELLIGENCE	PP 100 40	16 11	45 61	P C	404192D	WIRELESS NETWORKS	PP 100	40 16	28	44 P
404186 LAB PRACTICE I (CN & MWE)	TW 50 20	11	37	P C	404193	LAB PRACTICE III(MC & BCS)	OR 50	20		43 P
404186 LAB PRACTICE I (CN & MWE)	OR 50 20		46	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20		42 P
404187 LAB PRAC. II(VLSI &ELEC. I) 404187 LAB PRAC. II(VLSI &ELEC. I)	PR 50 20		23	P C	404194	PROJECT PHASE II	TW 100	40		38 P 92 P
404184 DIGITAL IMAGE PROCESSING 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	OR 50 20		41	P C	404195	PROJECT PHASE II	PR 50	20		43 P
GRAND TOTAL = 915/1500 RESULT:FIRST CI	ASS # 0.4									
B120053194 PRAKASH KUMAR	• • • • • • • • • • • • • • • • • • • •	MAI	 NJUDEVI		,7154		 53194		• • • • •	
					, -					
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY	PP 100 40	04	46 50	РC	404189	SEM.:2 MOBILE COMMUNICATION	PP 100	40 18	37	55 P
404182 COMPUTER NETWORKS	PP 100 40	16	30 46	P C	404190	BROADBAND COMMUNICATION SYS.	PP 100	40 12		46 P
404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING	PP 100 40 PP 100 40	13 18	49 62 56 74	P C	404191C 404192A	AUDIO VIDEO ENGINEERING BIOMEDICAL SIGNAL PROCESSING	DD 100	40 01	4.0	50 P 61 P
404185D ARTIFICIAL INTELLIGENCE	PP 100 40	13	39 52	РC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20		38 P
404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE)	TW 50 20		45 43	P C	404193 404194	LAB PRACTICE III(MC & BCS)	OR 50	20 20		38 P 24 D
404187 LAB PRAC. II(VLSI &ELEC. I)	TW 50 20		34	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20		37 P
404184A DIGITAL IMAGE PROCESSING 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 PROJECT PHASE I	PR 50 20		38	P C	404195	LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	TW 100	40		92 P
			39	PC	404193	PROUECT PHASE II	PR 50	20		45 P
GRAND TOTAL = 969/1500 , RESULT:FIRST CI	ASS									
B120053195 PRANAV DILIP BOROLE		SAI	NGITA		,7152	7492K ,PICT ,B1200	53195			
SEM.:1						SEM.:2				
404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS	PP 100 40	13 22	38 51 28 50	P C	404189 404190	MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS.	DD 100	40 15	2.0	57 P 47 P
404183 MICROWAVE ENGINEERING	PP 100 40	18	42 60	РC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40 12	35	47 P
404184B EMBEDDED SYSTEMS & RTOS 404185C PLCS AND AUTOMATION	PP 100 40	19 12	36 55 34 46	P C	404192D	WIRELESS NETWORKS	PP 100	40 19	28	47 P 31 P
404186 LAB PRACTICE I (CN & MWE)	TW 50 20	12	36	P C	404193	LAB PRACTICE III(MC & BCS)	OR 50	20		44 P
404186 LAB PRACTICE I (CN & MWE)	OR 50 20		41	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20		28 P
404187 LAB PRAC. II(VLSI &ELEC. I) 404187 LAB PRAC. II(VLSI &ELEC. I)	PR 50 20		35 40	P C	404194	PROJECT PHASE II	TW 100	∠∪ 40		39 P 90 P
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	OR 50 20		42	P C	404195	BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50	20		40 P
GRAND TOTAL = 926/1500 , RESULT:FIRST CI										

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BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

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DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 65 NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.												
OTHER LINES: HEAD OF PASSING, MAX. M	MARKS, MIN.PAS	S MARKS, II	NT. MARKS,	TH. MARKS	G, TOTAL MARKS OBTAINED, P/F:	PASS/FAIL, C:PREV	JIOUS CARRY OVER					
B120053196 PURVA KISHOR DESHMUKH		NEHA		,7152		53196	• • • • • • • • • • • • • • • • • •					
SEM.:1					SEM.:2							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40	12 46	58 P C	404189	MOBILE COMMUNICATION	PP 100 40 18	43 61 P					
404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING	PP 100 40 PP 100 40	21 42 16 41	57 P.C	404190 404191	BROADBAND COMMUNICATION SYS.	PP 100 40 19 PP 100 40 18	42 61 P					
404184A DIGITAL IMAGE PROCESSING	PP 100 40	14 42	56 P C	404192D	WIRELESS NETWORKS	PP 100 40 18	40 58 P					
404185C PLCS AND AUTOMATION	PP 100 40	14 39	53 P C	404193	LAB PRACTICE III(MC & BCS)	TW 50 20	39 P					
404186 LAB PRACTICE I (CN & MWE)	TW 50 20		36 P C	404193	LAB PRACTICE III(MC & BCS)	OR 50 20	41 P					
404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC II(VIST & ELEC I)	TW 50 20		42 P C	404194	TAB PRAC. IV(ELECTIVE III)	PR 50 20	30 P 37 P					
404187 LAB PRAC. II(VLSI &ELEC. I)	PR 50 20		40 P C	404195	PROJECT PHASE II	TW 100 40	94 P					
404188 PROJECT PHASE I	OR 50 20		41 P C	404195	PROJECT PHASE II	PR 50 20	44 P					
GRAND TOTAL = 1013/1500 , RESULT:FIRST (LASS WITH DIS	T.TNC.T.TON										
B120053197 RACHIT KUMAR THAKUR		DEOKAN		7152		53197						
SEM.:1	100 40	10 00	40 - ~	404100	SEM.:2	100 10 00	41 64 -					
404181 VLS1 DESIGN & TECHNOLOGY	PP 100 40	12 37	49 P C	404189	MOBILE COMMUNICATION CVC	PP 100 40 23	41 64 P					
404183 MICROWAVE ENGINEERING	PP 100 40	21 59	80 P C	404191C	AUDIO VIDEO ENGINEERING	PP 100 40 23	34 52 P					
404184A DIGITAL IMAGE PROCESSING	PP 100 40	11 50	61 P C	404192D	WIRELESS NETWORKS	PP 100 40 26	39 65 P					
404185B ELECTRONIC PRODUCT DESIGN	PP 100 40	17 29	46 P C	404193	LAB PRACTICE III(MC & BCS)	TW 50 20	42 P					
404186 LAB PRACTICE I (CN & MWE)	TW 50 20		39 P C	404193	LAB PRACTICE III (MC & BCS)	OR 50 20	40 P					
404187 LAB PRAC. II(VLSI &ELEC. I)	TW 50 20		42 P C	404194	LAB PRAC. IV(ELECTIVE III)	PR 50 20	30 P					
404187 LAB PRAC. II(VLSI &ELEC. I)	PR 50 20		30 P C	404195	PROJECT PHASE II	TW 100 40	88 P					
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185B ELECTRONIC PRODUCT DESIGN 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 PROJECT PHASE I	OR 50 20		39 P C	404195	PROJECT PHASE II	PR 50 20	43 P					
GRAND TOTAL = 1024/1500 , RESULT:FIRST (LASS WITH DIS	TINCTION										
B120053198 RAKSHE VEDANT SHASHIKANI	· · · · · · · · · · · · · · · · · · ·	MADHUR		,7152		53198						
SEM.:1					SEM.:2							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40	15 51	66 P C	404189	MOBILE COMMUNICATION	PP 100 40 24	44 68 P					
404182 COMPUTER NETWORKS	PP 100 40	12 37	49 P C	404190	BROADBAND COMMUNICATION SYS.	PP 100 40 20	52 72 P					
404183 MICROWAVE ENGINEERING	PP 100 40	23 56	79 P C	404191D	SOFT COMPUTING	PP 100 40 20	52 72 P					
404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION	PP 100 40	23 58 12 47	81 P C	404192D	WIRELESS NETWORKS	PP 100 40 24	39 63 P					
404186 LAB PRACTICE I (CN & MWE)	TW 50 20	14 4/	43 P C	404193	LAB PRACTICE III(MC & BCS)	OR 50 20	36 P					
404186 LAB PRACTICE I (CN & MWE)	OR 50 20		39 P C	404194	LAB PRAC. IV(ELECTIVE III)	TW 50 20	35 P					
404187 LAB PRAC. II(VLSI &ELEC. I)	TW 50 20		43 P C	404194	LAB PRAC. IV(ELECTIVE III)	PR 50 20	37 P					
404187 LAB PRAC. II(VLSI &ELEC. I) 404188 PROJECT PHASE I	PR 50 20		35 P C	404195	PROJECT PHASE II	TW 100 40	94 P					
404100 PROUECI PHASE I	OK 30 20		34 P C	404133	PROUECT PRASE II	PR 30 20	40 P					
GRAND TOTAL = 1096/1500 RESULT: FIRST (TLASS WITH DIS	TINCTION										

GRAND TOTAL = 1096/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

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DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 66 NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER													
B120053199 RAMAIYA APURVA UMESH		KAVITA	,71633652K ,PICT ,B1200	53199									
			SEM.:2 404189 MOBILE COMMUNICATION 404190 BROADBAND COMMUNICATION SYS. 404191C AUDIO VIDEO ENGINEERING 404192D WIRELESS NETWORKS 404193 LAB PRACTICE III(MC & BCS) 404194 LAB PRACTICE III(MC & BCS) 404194 LAB PRAC. IV(ELECTIVE III) 404194 LAB PRAC. IV(ELECTIVE III) 404195 PROJECT PHASE II 404195 PROJECT PHASE II										
GRAND TOTAL = 1087/1500 , RESULT:FIRST	CLASS WITH DISTINC	CTION											
B120053200 RANADE TANVI UDAY		PURWA	,71527512H ,PICT ,B1200	53200									
			SEM.:2 404189 MOBILE COMMUNICATION 404190 BROADBAND COMMUNICATION SYS. 404191D SOFT COMPUTING 404192D WIRELESS NETWORKS 404193 LAB PRACTICE III(MC & BCS) 404193 LAB PRACTICE III(MC & BCS) 404194 LAB PRAC. IV(ELECTIVE III) 404194 LAB PRAC. IV(ELECTIVE III) 404195 PROJECT PHASE II 404195 PROJECT PHASE II										
GRAND TOTAL = 956/1500 . RESULT:FIRST (TIASS												
B120053201 RANDAD PRATIK PRAKASH		SANGITA	,71527513F ,PICT ,B1200	53201									
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185B ELECTRONIC PRODUCT DESIGN 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI &ELEC. I 404187 PROJECT PHASE I	PP 100 40 13 PP 100 40 28 PP 100 40 23 PP 100 40 21 PP 100 40 20 TW 50 20 OR 50 20 PR 50 20 OR 50 20 OR 50 20 OR 50 20	50 63 P C 44 72 P C 58 81 P C 58 79 P C 35 55 P C 34 P C 39 P C 34 P C 35 P C 45 P C		PP 100 40 25 41 66 P PP 100 40 25 49 74 P									
GRAND TOTAL = 1099/1500 , RESULT:FIRST													

GRAND TOTAL = 1099/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PUNI	E INSTITUTE	OF COMPUTE	R TECHNOLOG	GY, PUNE.	PAGE NO.	67	
NOTE: FIRST LINE : SEAT NO., NAME OF TOTHER LINES: HEAD OF PASSING, MAX. MAR	THE CANDIDAT	TE, MOTHER	, PERMANENT	reg. No.	, PREVIOUS SEAT NO., COLLEG	E, SEAT NO.	
							······································
B120053202 RANE MITESH MANOHAR		PRADNY	A	,7152	7515B ,PICT ,B1200	53202	
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 PP 50 20 TW 50 20 PR 50 20 OR 50 20 OR 50 20	14 49 27 45 24 58 24 40 24 49	63 P C 72 P C 82 P C 64 P C 73 P C 41 P C 41 P C 41 P C 36 P C 42 P C	404189 4041910 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 21 PP 100 40 21 PP 100 40 15 PP 100 40 25 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	46 67 P 53 74 P 39 54 P 36 61 P 37 P 40 P 35 P 37 P 93 P 42 P
GRAND TOTAL = 1094/1500 , RESULT:FIRST CLA	ASS WITH DIS	STINCTION					
B120053203 RASIKA HEMANT KULKARNI						53203	
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20 OR 50 20	10 35 19 43 13 29 21 38 16 38	45 P C 62 P C 42 P C 59 P C 54 P C 36 P C 38 P C 39 P C 25 P C 44 P C	404189 4041910 4041912D 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 20 PP 100 40 07 PP 100 40 13 PP 100 40 22 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	21# 41 P 40 47 P 36 49 P 38 60 P 37 P 38 P 32 P 35 P 94 P 45 P
GRAND TOTAL = 922/1500 , RESULT: FIRST CLAS	ASS [# 0.4]						
B120053204 RATHOD AISHWARYA RAJESH		SUPRIY		 7163,	3653H ,PICT ,B1200	53204	• • • • • • • • • • • • • • • • • • • •
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185B ELECTRONIC PRODUCT DESIGN 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I GRAND TOTAL = 1102/1500 , RESULT:FIRST CLA	PP 100 40 PP 50 20 OR 50 20 PR 50 20 OR 50 20 OR 50 20	12 46 24 44 17 56 21 54 21 43	58 P C 68 P C 73 P C 75 P C	404189 404190 404191C	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS	PP 100 40 22 PP 100 40 19 PP 100 40 20	49 71 P 48 67 P 51 71 P 41 66 P 38 P 41 P 42 P 38 P 94 P 44 P
GREET TOTAL - TIOZ/TOOO , REDULLITEDI CLE	TIOO WIIII DIE) T TINC T T OIN					

BRANCH: 71-B.E. (2012 PAT.) (ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PINE INSTITUTE OF COMPUTER TECHNOLOGY, PINE.

DATE : (04 JUL 2018 CENTRE: PUN	NE INSTITUT	E OF C	COMPUTER	TECHNOL	OGY, PUNE.	PAGE NO	. 68				
NOTE: FIRS	ST LINE : SEAT NO., NAME OF ES: HEAD OF PASSING, MAX. MA	THE CANDID	ATE, ASS M	MOTHER, ARKS, IN	PERMANE T. MARKS	NT REG. NO. , TH. MARKS	, PREVIOUS SEAT NO., COLL. 5, TOTAL MARKS OBTAINED, P/	EGE, SEAT F:PASS/FAIL	NO. , C:PREV	OUS (CARRY O	VER
B120053206	RAUT HRISHIKESH PURUSHOTT	ГАМ	• • • • •	SUNITA		,7152		0053206	· • • • • • • • • •	• • • • •		
404181 V 404182 C 404183 M 404184B H 404185C H 404186 H 404186 H 404187 H 404187 H 404188 H	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 4 PP 100 2 OR 50 2 TW 50 2 PR 50 2 OR 50 2	0 15 0 23 0 20 0 22 0 16 0 0	53 45 58 46 49	68 P C 68 P C 78 P C 65 P C 40 P C 39 P C 45 P C 45 P C	404189 404190 404191C 404193 404193 404194 404194 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SY AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 S. PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 28 40 21 40 21 40 27 20 20 20 20 40 20	42 46 41 39	70 P 67 P 62 P 66 P 37 P 42 P 43 P 40 P 95 P 45 P	
GRAND TOTAL =	= 1128/1500 . RESULT:FIRST CI	LASS WITH D	ISTING	CTION								
B120053207	7 RAUT RINKOO POPAT			СННАЧА		,7163	33654F ,PICT ,B12	0053207	· • • • • • • • • •	• • • • •		• • •
404181 V 404182 C 404183 M 404184A I 404185C I 404186 I 404186 I 404187 I 404187 I 404188 I	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 4 TW 50 2 OR 50 2 TW 50 2 PR 50 2 OR 50 2	0 16 0 24 0 18 0 17 0 10 0 0	49 38 55 50 49	65 P C 62 P C 73 P C 59 P C 43 P C 39 P C 38 P C 36 P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SY AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 S. PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 21 40 20 40 20 40 24 20 20 20 20 20 40 20	38 31 45 42	59 P 51 P 65 P 66 P 39 P 30 P 36 P 78 P 33 P	
GRAND TOTAL =	= 1002/1500 , RESULT:FIRST CI	JASS WITH D	TS.I.TM(T.I.TON								
B120053208	RAUTMARE VISHWAJIT LAXMIK	KANT	• • • • •	SHUBHAN	 IGI	,7152		0053208		• • • • •	• • • • • • •	
404181 V 404182 C 404183 M 404184A I 404185C I 404186 I 404187 I 404187 I 404188 I	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACT. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 4 TW 50 2 OR 50 2 TW 50 2 PR 50 2 OR 50 2	0 07 0 19 0 01 0 13 0 05 0	28 30 47 45 40	35# P 49 P C 48 P C 58 P C 45 P C 22 P C 41 P C 21 P C 36 P 40 P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SY AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 S. PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 15 40 21 40 07 40 21 20 20 20 20 20 20 20	32 34 34 30	47 P 55 P 41 P 51 P 21 P 23 P 24 P 36 P 90 P 40 P	
GRAND TOTAL =	= 823/1500 , RESULT:SECOND CI	LASS [# O	.4]				SERVED FOR BACKLOGS.					

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PU	NICS &TELECOMU JNE INSTITUTE	OF COMPUTE	R TECHNOLOG	GY, PUNE.	PAGE NO. 6	69	
NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, MAX. N	F THE CANDIDAT MARKS, MIN.PAS	ΓΕ, MOTHER SS MARKS, I	R, PERMANENT INT. MARKS,	reg. No. Th. Marks	, PREVIOUS SEAT NO., COLLEGI , TOTAL MARKS OBTAINED, P/F:1	E, SEAT NO. PASS/FAIL, C:PREV	IOUS CARRY OVER
B120053209 RAVIRAJ KUMAR							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 PP 50 20 OR 50 20 OR 50 20 OR 50 20 OR 50 20	16 53 26 46 23 61 20 52 17 53	69 P C 72 P C 84 P C 72 P C 70 P C 34 P C 44 P C 39 P C 44 P C 39 P C	404189 404190 404191C 404192A 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING BIOMEDICAL SIGNAL PROCESSING LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACT. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 23 PP 100 40 21 PP 100 40 22 PP 100 40 29 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	41 64 P 48 69 P 46 68 P 53 82 P 41 P 38 P 36 P 38 P 90 P 42 P
GRAND ICIAL - IIJJ/IJCC , RESCHIFIIRSI (THOS WITTH DIE) T TINC T TOIN					
B120053210 RIPUL DUBEY							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I		12 36 16 35 04 45 12 32 17 40	48 P 51 P C 49 P C 44 P C 57 P C 30 P C 43 P C 31 P C 28 P C 31 P C	404189 404191D 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. SOFT COMPUTING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACT. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 15 PP 100 40 19 PP 100 40 11 PP 100 40 20 TW 50 20 OR 50 20 TW 50 20 TW 50 20 TW 50 20 TW 100 40 PR 50 20	36 51 P 36 55 P 41 52 P 37 57 P 20 P 36 P 26 P 25 P 85 P 30 P
GRAND TOTAL = 849/1500 , RESULT:HIGHER S B120053211 RUPAWATE RAHUL RAVINDRA	SECOND CLASS						
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20 OR 50 20	03 32 17 38 22 29 14 32 16 38	35# P 55 P 51 P C 46 P C 54 P C 41 P C 35 P C 32 P C 35 P C 41 P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 24 PP 100 40 09 PP 100 40 16 PP 100 40 13 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	29 53 P 41 50 P 40 56 P 34 47 P 34 P 34 P 24 P 37 P 87 P 41 P
GRAND TOTAL = 888/1500 , RESULT:HIGHER S	SECOND CLASS	[# 0.4]					

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PUI	NE INSTITU	TE O	F COMPUTE	R TEC	HNOLOG	SY, PUNE.	PAG	E NO. 7	0					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CANDI	DATE	, MOTHER	, PER	MANENT	REG. NO.	, PREVIOUS SEAT NO.,	COLLEGE	. SEAT	NO.				
	NES: HEAD OF PASSING, MAX. MAX. MAX.										,	PREVI		ARRY:	OVER
B1200532	212 RUSHABH UTTAM KAMBLE			REKHA U	JTTAM	KAMBL	JE ,7152	7545D ,PICT	,B12005	3212					
404181 404182 404183 404184B 404185D 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 40 20 20 20 20 20	11 38 16 44 23 32 14 38 13 40	49 60 55 52 53 34 39 35 33 38	P P P P P P P P P P	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATIO AUDIO VIDEO ENGINEERIN WIRELESS NETWORKS LAB PRACTICE III(MC & LAB PRACTICE III(MC & LAB PRAC. IV(ELECTIVE LAB PRAC. IV(ELECTIVE PROJECT PHASE II PROJECT PHASE II	N SYS. G BCS) BCS) III) III)	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20 20	24 08 16 20	32 43 47 36	56 51 63 56 29 39 33 34 87 42	P P P P P P P P
GRAND TOTAL	」= 938/1500 , RESULT:FIRST CLA	ASS													
R1200533	213 SAKAT TANAYA VASANT		• • • •	CHARDA			7152	7552G DICT	B12005	3213		• • • • • •		• • • •	• • • • •
404181 404182 404183 404184A 404185C 404186 404186 404187 404187 404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 40 20 20 20 20 20	18 39 14 39 25 38 17 30 11 38	57 53 63 47 49 40 32 34 40 43	P C C C C C C C C C C C	404189 404190 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATIO AUDIO VIDEO ENGINEERIN WIRELESS NETWORKS LAB PRACTICE III(MC & LAB PRACTICE III(MC & LAB PRAC. IV(ELECTIVE LAB PRAC. IV(ELECTIVE PROJECT PHASE II PROJECT PHASE II	N SYS. G BCS) BCS) III) III)	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20	18 14 14 17	36 46 35 30	54 60 49 47 40 32 40 38 95 42	P P P P P P P P P
GRAND TOTAL	$_{\perp}$ = 955/1500 . RESULT:FIRST CLA	ASS													
B1200532	214 SAKET M SIDDHESHWAR		• • • •	ANJALI			,7152	7553E ,PICT	,B12005	3214					• • • • •
	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I = 1065/1500 . RESULT:FIRST CI				63 71 67 57 57 42 43 43 41	P P P P P P P P P P	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATIO AUDIO VIDEO ENGINEERIN WIRELESS NETWORKS LAB PRACTICE III(MC & LAB PRACTICE III(MC & LAB PRAC. IV(ELECTIVE LAB PRAC. IV(ELECTIVE PROJECT PHASE II PROJECT PHASE II	N SYS. G BCS) BCS) III) III)	PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20	17 22 16 27	44 48 41 36	61 70 57 63 44 40 24 38 96 45	P P P P P P P P

GRAND TOTAL = 1065/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCE DATE :	1:71-B.E. (2012 PAT.)(ELECTRON 04 JUL 2018 CENTRE: PU	ICS &TELE NE INSTIT	COMU UTE	OF CC	TON) MPUTER	R TEC	CHNOLOG	Y, PUNE.	PAGE NO. 71	
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	 TACI	 Е, М	OTHER,	PEF	 RMANENT	REG. NO.	, PREVIOUS SEAT NO., COLLEGE, SEAT NO.	• • • • • • • •
OTHER LI	NES: HEAD OF PASSING, MAX. M.	ARKS, MIN	.PAS	S MAR	KS, IN	JT. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS C.	ARRY OVER
B1200532	215 SAKSHI GUPTA			A	SHOO		• • • • • •	,7152	7555M ,PICT ,B120053215	
	SEM.:1								SEM.:2	
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	12	34	46	P C	404189	SEM.:2 MOBILE COMMUNICATION PP 100 40 29 37 BROADBAND COMMUNICATION SYS. PP 100 40 26 41 AUDIO VIDEO ENGINEERING PP 100 40 18 47 WIRELESS NETWORKS PP 100 40 25 34 LAB PRACTICE III(MC & BCS) TW 50 20 LAB PRACTICE III(MC & BCS) OR 50 20 LAB PRAC. IV(ELECTIVE III) TW 50 20 LAB PRAC. IV(ELECTIVE III) PR 50 20 PROJECT PHASE II TW 100 40 PROJECT PHASE II PR 50 20	66 P
404182 404183	MICROWAVE ENGINEERING	PP 100 PP 100	40 40	1 / 22	44 45	61 67	PC	404190 404191	AUDIO VIDEO ENGINEERING DD 100 40 26 41	67 P 65 P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	22	31	53	PC	404192D	WIRELESS NETWORKS PP 100 40 25 34	59 P
404185C	PLCS AND AUTOMATION	PP 100	40	18	36	54	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20	45 P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			38	P C	404193	LAB PRACTICE III(MC & BCS) OR 50 20	42 P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			42	PC	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20	38 P
404187	TAB PRAC. II(VLSI &ELEC. I)	TW 50	20			33 37	PC	404194	PROJECT PHASE IT TW 100 40	39 P 92 P
404188	PROJECT PHASE I	OR 50	20			37	P C	404195	PROJECT PHASE II PR 50 20	35 P
GRAND TOTAL] = 1016/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	'ION					
B1200532	216 SALT GANESH TIKARAM				R			7163		
	SEM.:1								SEM.:2	
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	15	42	57	P C	404189	MOBILE COMMUNICATION PP 100 40 22 41	63 P
404182	COMPUTER NETWORKS	PP 100	40	27	49 52	76	P C	404190	BROADBAND COMMUNICATION SYS. PP 100 40 28 46	74 P
404183 404184A	DIGITAL IMAGE PROCESSING	PP 100	40	21	60	81	PC	404191C	WIRELESS NETWORKS PP 100 40 10 39	55 P 51 P
404185D	ARTIFICIAL INTELLIGENCE	PP 100	40	15	46	61	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20	36 P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			46	РC	404193	LAB PRACTICE III(MC & BCS) OR 50 20	40 P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			37	P C	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20	34 P
404187	LAB PRAC. II(VLSI & ELEC. I)	TW 50	20			37	PC	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20	37 P
404187 404188	PROJECT PHASE T	OR 50	20			38	PC	404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 22 41 BROADBAND COMMUNICATION SYS. PP 100 40 28 46 AUDIO VIDEO ENGINEERING PP 100 40 16 39 WIRELESS NETWORKS PP 100 40 23 28 LAB PRACTICE III(MC & BCS) TW 50 20 LAB PRACTICE III(MC & BCS) OR 50 20 LAB PRAC. IV(ELECTIVE III) TW 50 20 LAB PRAC. IV(ELECTIVE III) PR 50 20 PROJECT PHASE II TW 100 40 PROJECT PHASE II PR 50 20	85 P 30 P
101100	TROOLET THISE T	010 30	20			30	1 0	101133	TROOPET TIMES IT	30 1
GRAND TOTAL	L = 1043/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	CION					
B1200532	217 SALUNKE RUTUJA DEELIP				IANGAL			,7152	7557H ,PICT ,B120053217	• • • • • • • •
	SEM.:1								SEM.:2	
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	15	43	58	РC	404189	MOBILE COMMUNICATION PP 100 40 22 33	55 P
404182	COMPUTER NETWORKS	PP 100	40	24	41	65	P C	404190	BROADBAND COMMUNICATION SYS. PP 100 40 24 42	66 P
404183 404194D	MICROWAVE ENGINEERING	PP 100	40	19 21	44	63	P C	404191D	SOFT COMPUTING PP 100 40 22 39 WIDELESS METRODES DD 100 40 22 41	61 P
404184B	ELECTRONIC PRODUCT DESIGN	PP 100	40	13	43	56	PC	4041920	LAB PRACTICE III (MC & BCS) TW 50 20	35 P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			35	PC	404193	LAB PRACTICE III(MC & BCS) OR 50 20	42 P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			38	РC	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20	29 P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			41	P C	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20	28 P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			34	P C	404195	PROJECT PHASE II TW 100 40	93 P
404188	PROUECT PHASE I	OK 50	∠∪			45	PC	404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 22 33 BROADBAND COMMUNICATION SYS. PP 100 40 24 42 SOFT COMPUTING PP 100 40 22 39 WIRELESS NETWORKS PP 100 40 23 41 LAB PRACTICE III(MC & BCS) TW 50 20 LAB PRACTICE III(MC & BCS) OR 50 20 LAB PRAC. IV(ELECTIVE III) TW 50 20 LAB PRAC. IV(ELECTIVE III) PR 50 20 PROJECT PHASE II TW 100 40 PROJECT PHASE II PR 50 20	44 P
	= 1017/1500 , RESULT:FIRST C									

GRAND TOTAL = 1017/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE	: 04 JUL 2018 CENTRE: PUR	ICS &TELECC IE INSTITUT	MUNIC E OF	ATION) COMPUTE	R TEC	HNOLOG	GY, PUNE.	PAGE NO.	72					
DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 72 NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER													OVER	
B1200532	 218 SALUNKHE PRASHANTI PRAKAS	 SH	• • • • •	RASHMI			,7163	3656B ,PICT ,B1200	 53218	• • • •			• • • •	
404181 404182 404183 404184A 404185B 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 4 TW 50 2 OR 50 2 TW 50 2 OR 50 2 OR 50 2	0 15 0 20 0 22 0 16 0 24	48 49 49 62 37	63 69 71 78 61 39 43 39 42	P C C C C C C C C C C C C C C C C C C C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20	29 26 19 28	41 54 50 41	70 80 69 69 42 44 42 39 92 43	P P P P P P P P
GRAND TOTAI	L = 1134/1500 , RESULT:FIRST CI	LASS WITH D	ISTIN	CTION										
B1200532				MINAL			,7152	7558F ,PICT ,B1200	 53219	• • • •			• • • •	
404181 404182 404183 404184A 404185B 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 4 TW 50 2 TW 50 2 PR 50 2 OR 50 2	0 10 0 12 0 15 0 15 0 11 0 0	34 31 32 43 29	44 43 47 58 40 33 27 31 23 37	P C C C C C C C C C C C C C C C C C C C	404189 404190 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20	21 29 10 18	35 41 35 23#	56 70 45 41 22 32 38 40 92 43	P P P P P P P P
(±RΔΝΠ) Π(ΠΤΔΙ	. = 867/1500 PESIDI HIGHER SE	:('() X) ('1.\(\Delta \S\S\S		() 4 1										
D1200E2	220 SAMDURKAR SHARDUL ANIL						7150	OFFERD DIGE D1200						
	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I				49 60 58 64 54 47 33 39 46	P C P C P C C P C C P C C	404189 404191D 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. SOFT COMPUTING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20	17 24 23 23	35 36 39 29	52 60 62 52 42 42 34 33 95 46	P P P P P P P P P
GRAMD IOIAI	L = 1009/1500 , RESULT:FIRST CI	TUDD MITH F	тотти	CIIOM										

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

B120053221 SANAGAR SNEHA PRAMOD PRAMODINI ,71527561F ,PICT ,B120053221														
B120053221 SANAGAR SNEHA PRAMOD PRAMODINI ,71527561F ,PICT ,B120053221	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER													
SEM.:1	• • •													
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 15 39 54 P C 404189 MOBILE COMMUNICATION PP 100 40 19 43 62 P 404182 COMPUTER NETWORKS PP 100 40 22 45 67 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 27 50 77 P 404183 MICROWAVE ENGINEERING PP 100 40 22 39 61 P C 404191 AUDIO VIDEO ENGINEERING PP 100 40 15 45 60 P 404184A DIGITAL IMAGE PROCESSING PP 100 40 24 46 70 P C 404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 16 50 66 P 404185B ELECTRONIC PRODUCT DESIGN PP 100 40 18 37 55 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 44 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 43 P C 404193 LAB PRACTICE III(MC & BCS) OR 50 20 44 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 43 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 43 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 43 P C 404195 PROJECT PHASE II TW 100 40 93 P 404188 PROJECT PHASE I OR 50 20 33 P C 404195 PROJECT PHASE II PR 50 20 43 P														
404100 PRODECT PHASE T OR SU ZU SS P C 404193 PRODECT PHASE II PR SU ZU 43 P														
GRAND TOTAL = 10// + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION [& 0.163]														
B120053222 SANSARE TEJASVINI NANDKISHOR MAYA ,71527569M ,PICT ,B120053222														
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 12 44 56 P C 404189 MOBILE COMMUNICATION PP 100 40 26 48 74 P 404182 COMPUTER NETWORKS PP 100 40 22 49 71 P 404190 BROADBAND COMMUNICATION SYS. PP 100 40 22 47 69 P 404183 MICROWAVE ENGINEERING PP 100 40 23 45 68 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 20 50 70 P 404184A DIGITAL IMAGE PROCESSING PP 100 40 19 67 86 P C 404192D WIRELESS NETWORKS PP 100 40 21 42 63 P 404185C PLCS AND AUTOMATION PP 100 40 17 37 54 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 39 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 41 P C 404193 LAB PRACTICE III(MC & BCS) OR 50 20 41 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 39 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 37 P 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 39 P C 404195 PROJECT PHASE II TW 100 40 50 P 404188 PROJECT PHASE II OR 50 20 25 P														
GRAND TOTAL = 1031/1500 , RESULT:FIRST CLASS WITH DISTINCTION														
B120053223 SANYASI SARTHAK DILIP SHUBHANGI ,71527570E ,PICT ,B120053223	• • •													
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 14 49 63 P C 404189 MOBILE COMMUNICATION PP 100 40 23 49 72 P 404182 COMPUTER NETWORKS PP 100 40 10 42 52 P 404190 BROADBAND COMMUNICATION SYS. PP 100 40 26 45 71 P 404183 MICROWAVE ENGINEERING PP 100 40 25 44 69 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 17 54 71 P 404184A DIGITAL IMAGE PROCESSING PP 100 40 22 52 74 P C 404192D WIRELESS NETWORKS PP 100 40 28 48 76 P 404185C PLCS AND AUTOMATION PP 100 40 13 46 59 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 37 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 39 P C 404193 LAB PRACTICE III(MC & BCS) OR 50 20 40 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 38 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 38 P 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 38 P C 404195 PROJECT PHASE II TW 100 40 93 P 404188 PROJECT PHASE I OR 50 20 38 P C 404195 PROJECT PHASE II PR 50 20 43 P GRAND TOTAL = 1080/1500 , RESULT:FIRST CLASS WITH DISTINCTION														

GRAND TOTAL = 1080/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PU	NE INSTIT	UTE	OF CO	MPUTER	TEC	HNOLOG	GY, PUNE.	PAGE	NO. 74	4					
NOTE: F	IRST LINE : SEAT NO., NAME OF	THE CAND	IDAT	Έ, Μ	OTHER,	PER	MANENT	REG. NO.	, PREVIOUS SEAT NO., CO	OLLEGE	, SEAT	NO.				
OTHER L	NES: HEAD OF PASSING, MAX. M	ARKS, MIN	.PAS	S MAR	KS, IN	IT. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED,	P/F:PA	ASS/FAII	, C	:PREVI	OUS C	ARRY	OVER
B1200532	224 SAOJI AADITI VIVEK			A	NAGHA			,7152	7571C ,PICT ,B	3120053	3224			• • • • •		
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY	PP 100	40	10	53	63	P C	404189 404190	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION					53 50		
404183	COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING	PP 100	40	23	47	70	P C	404191C	AUDIO VIDEO ENGINEERING		PP 100	40	21	48	69	P
404184A 404185D	DIGITAL IMAGE PROCESSING ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100	40 40	23 19	60 60	83 79	P C P C	404192A 404193	BIOMEDICAL SIGNAL PROCES LAB PRACTICE III(MC & BC	SSING CS)	PP 100 TW 50	40 20	26	46	72 45	P P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			41	P C	404193	LAB PRACTICE III(MC & BC	CS)	OR 50	20			45	P
404186 404187	LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI &ELEC. I)	OR 50 TW 50	20			42 45	P C	404194	LAB PRAC. IV(ELECTIVE II LAB PRAC. IV(ELECTIVE II	LI)	TW 50 PR 50	20			45 45	P P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			47	P C	404195	PROJECT PHASE II		TW 100	40			97 47	P
						4 /	PC	404195	PROJECT PHASE II		PR 50	20			4/	P
GRAND TOTAL	_ = 1203/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	ION											
B1200532	225 SARDA YASH RAMANKUMAR			Α	RCHANA		• • • • •	,7152	7573K ,PICT ,B	3120053	3225					
	SEM.:1								SEM.:2							
404181 404182	VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS	PP 100 PP 100	40 40	15 23	44 29	59 52	P C P C	404189 404190	MOBILE COMMUNICATION BROADBAND COMMUNICATION	SYS.	PP 100 PP 100	40 40	23 27	49 46	72 73	P P
404183	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100	40	22	52	74	РC	404191C	AUDIO VIDEO ENGINEERING		PP 100	40	16	48	64	P
404184A 404185C	PLCS AND AUTOMATION	PP 100 PP 100	40 40	20 17	5 <i>3</i> 50	67	P C	404192D 404193	WIRELESS NETWORKS LAB PRACTICE III(MC & BC	CS)	PP 100 TW 50	40 20	28	39	67 43	P P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			39	РC	404193	LAB PRACTICE III(MC & BC	CS)	OR 50	20			40	P
404186 404187	LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I)	OR 50 TW 50	20 20			39 43	P C P C	404194 404194	LAB PRAC. IV(ELECTIVE II	II) II)	TW 50 PR 50	20 20			42 36	P P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			34	РC	404195	PROJECT PHASE II	/	TW 100	40			85	P
404188	PROJECT PHASE I	OR 50	20			42	РC	404195	PROJECT PHASE II		PR 50	20			40	P
GRAND TOTAL	L = 1084/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	ION											
B1200532	226 SHAH YASH NARESH			S	UREKHA			,7152	7583G ,PICT ,B	3120053	3226					
404101	SEM.:1	DD 100	4.0	1.0	4.2	60	D G	404100	SEM.:2		DD 100	4.0	2.5	4.0	65	D
404181	COMPUTER NETWORKS	PP 100 PP 100	40	20	43 31	6∠ 51	P C	404189	MOBILE COMMUNICATION BROADBAND COMMUNICATION					40 42		
404183	MICROWAVE ENGINEERING	PP 100	40	23	40	63	РC	404191D	SOFT COMPUTING					37	61	
404184A 404185D	DIGITAL IMAGE PROCESSING ARTIFICIAL INTELLIGENCE	PP 100 PP 100	40 40	15 15	52 50	6 ⁻ /	P C P C	404192A 404193	BIOMEDICAL SIGNAL PROCES	SSING CS)	PP 100 TW 50	40 20	21	36	57 35	P P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20		3 0	42	P C	404193	LAB PRACTICE III(MC & BC	CS)	OR 50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			37 31	P C	404194	LAB PRAC. IV(ELECTIVE II	II)	TW 50	20			31 35	P D
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			45	P C	404195	LAB PRACTICE III(MC & BC LAB PRACTICE III(MC & BC LAB PRAC. IV(ELECTIVE II LAB PRAC. IV(ELECTIVE II PROJECT PHASE II PROJECT PHASE II	/	TW 100	40			96	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			40	P C	404195	PROJECT PHASE II		PR 50	20			46	P
	_ = 1037/1500 , RESULT:FIRST C															

GRAND TOTAL = 1037/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH: 71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 75													
NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, MAX. M	THE CANDIDAT	E, MOTHER S MARKS, I	, PERMANENT NT. MARKS,	reg. No. Th. MARKS	, PREVIOUS SEAT NO., COLLEG S, TOTAL MARKS OBTAINED, P/F:	E, SEAT NO. PASS/FAIL, C:PREV	IOUS CARRY OVER						
B120053227 SHAIKH AMIR AQUEEL		IFFAT		,7152	27584E ,PICT ,B1200	53227							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	12 46 26 34 17 48 22 53 20 53	58 P C 60 P C 65 P C 75 P C 73 P C 44 P C 38 P C 43 P C 35 P C 40 P C	404189 404190 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACT IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 23 PP 100 40 29 PP 100 40 19 PP 100 40 28 TW 50 20 OR 50 20 TW 50 20 TW 100 40 PR 50 20 PR 50 20 TW 100 40 PR 50 20	46 69 P 41 70 P 51 70 P 47 75 P 44 P 40 P 40 P 37 P 88 P 40 P						
GRAND TOTAL = 1104/1500 , RESULT:FIRST C	THIS WITH DIE) I IIVC I I OIV											
B120053228 SHANU KUMAR		MANJU	DEVI	,7152	27590K ,PICT ,B1200	53228							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	13 59 26 39 22 53 22 51 19 57	72 P C 65 P C 75 P C 73 P C 76 P C 42 P C 43 P C 38 P C 48 P C 42 P C	404189 404190 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACT IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 25 PP 100 40 26 PP 100 40 22 PP 100 40 30 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	49 74 P 52 78 P 52 74 P 46 76 P 40 P 41 P 35 P 41 P 96 P 45 P						
GRAND TOTAL = 1174/1500 , RESULT:FIRST C	CLASS WITH DIS	STINCTION											
B120053229 SHEFALI GARG		VANDNA		,7152	27595L ,PICT ,B1200	53229							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I GRAND TOTAL = 1136/1500 RESULT: FIRST OF	PP 100 40 PP 50 20 OR 50 20 OR 50 20 OR 50 20	15 48 17 42 21 45 25 54 17 54	63 P C 59 P 66 P C 79 P C 71 P C 41 P C 43 P C 39 P C 38 P C 40 P C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 29 PP 100 40 22 PP 100 40 19 PP 100 40 23 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	47 76 P 53 75 P 50 69 P 41 64 P 46 P 42 P 44 P 44 P 94 P 43 P						
GRAND TOTAL = 1136/1500 , RESULT:FIRST C	CLASS WITH DIS	STINCTION											

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH DATE :	BRANCH: /1-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 76													
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	IACI	E, N	MOTHER,	PER	MANENT	REG. NO.	PREVIOUS SEAT NO., COLLEGE, SEAT NO.					
OTHER LI	NES: HEAD OF PASSING, MAX. MAX. MAX. MAX. MAX. MAX. MAX. MAX.	ARKS, MIN	I.PAS	SS MAI	RKS, IN	JT. M	IARKS,	TH. MARKS	TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARR	Y OVER				
B1200532	30 SHEJULE RUPALI ARJUNRAO		• • • •	1	NANDA	• • • •		,7163	657L ,PICT ,B120053230	• • • • •				
	SEM.:1								SEM.:2 MOBILE COMMUNICATION PP 100 40 22 46 68 BROADBAND COMMUNICATION SYS. PP 100 40 26 48 74 AUDIO VIDEO ENGINEERING PP 100 40 21 53 74 WIRELESS NETWORKS PP 100 40 26 43 69 LAB PRACTICE III(MC & BCS) TW 50 20 40 LAB PRACTICE III(MC & BCS) OR 50 20 42 LAB PRAC. IV(ELECTIVE III) TW 50 20 34 LAB PRAC. IV(ELECTIVE III) PR 50 20 37 PROJECT PHASE II TW 100 40 93 PROJECT PHASE II PR 50 20 45					
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	16	57	73	P C	404189	MOBILE COMMUNICATION PP 100 40 22 46 68	P				
404182 404183	MICROWAVE ENGINEERING	PP 100	40 40	⊿6 18	3 U 4 R	56 66	P C	404190 404191C	BROADBAND COMMUNICATION SYS. PP 100 40 26 48 74 AUDIO VIDEO ENGINEERING DD 100 40 21 53 74	P D				
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	21	49	70	РC	404192D	WIRELESS NETWORKS PP 100 40 26 43 69	P				
404185C	PLCS AND AUTOMATION	PP 100	40	12	58	70	РC	404193	LAB PRACTICE III(MC & BCS) TW 50 20 40	P				
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			46	P C	404193	LAB PRACTICE III (MC & BCS) OR 50 20 42	P				
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			39	PC	404194	LAB PRAC. IV(ELECTIVE III) IW 50 20 34	P P				
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			37	РC	404195	PROJECT PHASE II TW 100 40 93	P				
404188	PROJECT PHASE I	OR 50	20			43	P C	404195	PROJECT PHASE II PR 50 20 45	P				
GIVAND IOIAL	1 - IIZU/IJUU , NEBULI:FINBI CI	THOS MIII	L DIG) T TINC.	LTOIN									
B1200532	231 SHEJWAL SAYALT VINAYAK			7	 ATTNA				658T .PTCT .B120053231	• • • • • •				
B120053231 SHEJWAL SAYALI VINAYAK ANITA ,71633658J ,PICT ,B120053231														
404101	SEM.:1	100	4.0			6.5	_ ~	404100	SEM.: 2	_				
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	16 27	51 4Ω	67 75	PC	404189	MOBILE COMMUNICATION PP 100 40 25 53 78	P				
404183	MICROWAVE ENGINEERING	PP 100	40	25	49	74	PC	404191C	AUDIO VIDEO ENGINEERING PP 100 40 21 56 77	P				
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	23	46	69	РC	404192D	WIRELESS NETWORKS PP 100 40 29 44 73	P				
404185C	PLCS AND AUTOMATION	PP 100	40	15	54	69	P C	404193	LAB PRACTICE III(MC & BCS) TW 50 20 41	P				
404186	LAB PRACTICE I (CN & MWE)	J.M 20	20			45 41	PC	404193	LAB PRACTICE III (MC & BCS) OR 50 20 42	P				
404187	LAB PRACTICE I (CN & MWE)	TW 50	20			44	PC	404194	LAB PRAC. IV(ELECTIVE III) IW 50 20 42	P				
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			$\overline{44}$	РC	404195	PROJECT PHASE II TW 100 40 93	P				
404188	PROJECT PHASE I	OR 50	20			40	P C	404195	SEM.:2 MOBILE COMMUNICATION PP 100 40 25 53 78 BROADBAND COMMUNICATION SYS. PP 100 40 28 50 78 AUDIO VIDEO ENGINEERING PP 100 40 21 56 77 WIRELESS NETWORKS PP 100 40 29 44 73 LAB PRACTICE III(MC & BCS) TW 50 20 41 LAB PRACTICE III(MC & BCS) OR 50 20 42 LAB PRAC. IV(ELECTIVE III) TW 50 20 42 LAB PRAC. IV(ELECTIVE III) PR 50 20 39 PROJECT PHASE II TW 100 40 93 PROJECT PHASE II PR 50 20 45	P				
GRAND TOTAL	= 1176/1500 , RESULT:FIRST C	LASS WITH	DIS	STINCT	CION									
B1200532	32 SHELKE AMIT KIRAN			I	KANCHAN	1	• • • • • •	,7152						
	SEM.:1								SEM.:2					
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ELECTRONIC PRODUCT DESIGN	PP 100	40	13	46	59	РC	404189	MOBILE COMMUNICATION PP 100 40 22 48 70	P				
404182	COMPUTER NETWORKS	PP 100	40	17	34	51	P C	404190		P				
404183	MICROWAVE ENGINEERING	PP 100	40	21	45	66	PC	404191C	AUDIO VIDEO ENGINEERING PP 100 40 26 45 71 WIRELESS NETWORKS PP 100 40 27 39 66	D				
404184B	ELECTRONIC PRODUCT DESIGN	PP 100	40	16	46	62	PC	4041920	TAB PRACTICE III(MC & BCS) TW 50 20 48	P				
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			36	РC	404193	LAB PRACTICE III(MC & BCS) OR 50 20 40	P				
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			36	P C	404194	LAB PRAC. IV(ELECTIVE III) TW 50 20 45	P				
404187	LAB PRAC. II(VLSI & ELEC. I)	TW 50	20			41	P C	404194	LAB PRAC. IV(ELECTIVE III) PR 50 20 40	P				
404187 404188	LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PK 50 OR 50	⊿0 20			3⊥ 35	P C	404195 404195	WIRELESS NEIWORKS	P P				
	= 1050/1500 , RESULT:FIRST C					23	- 0	_0 110 0	11. 55 25	-				

GRAND TOTAL = 1050/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PU	NE INSTIT	UTE	OF COMP	UTER	TEC	HNOLOG	SY, PUNE.	PAG	GE NO. 7	7					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	 IDAT	E, MOT	HER	PER	MANENT	REG. NO.	PREVIOUS SEAT NO.,	 COLLEGE	SEAT	NO.	• • • • • •		• • • •	• • • • •
OTHER LI	INES: HEAD OF PASSING. MAX. M	ARKS. MIN	. PAS	S MARKS	. TN	Т. М	IARKS.	TH. MARKS	. TOTAL MARKS OBTAINED.	P/F:P	ASS/FATI	. C	:PREVI	OUS C	'ARRY	OVER
B1200532	233 SHETI SWAPNIL KAILAS			PUS	HPA			,7152	7601J ,PICT	,B12005	3233	• • • •				
	SEM.:1								SEM.:2							
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) PROJECT PHASE I	PP 100	40	12	48	60	P C	404189	MOBILE COMMUNICATION		PP 100	40	22	29	51	P
404182	COMPUTER NETWORKS	PP 100	40	22	32	54	P C	404190	BROADBAND COMMUNICATIO	ON SYS.	PP 100	40	25	48	73	P
404183 404184B	MICKOMAVE ENGINEERING	PP 100	40 40	⊥ / 11	4 Z	59 45	P C	404191D 404192D	MIDELESS NETWORKS		PP 100	40 40	24 25	43	6 / 57	P D
404185D	ARTIFICIAL INTELLIGENCE	PP 100	40	11	49	60	PC	404193	LAB PRACTICE III(MC &	BCS)	TW 50	20	23	32	37	P P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			37	РC	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			39	РC	404194	LAB PRAC. IV(ELECTIVE	III)	TW 50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			44	РC	404194	LAB PRAC. IV(ELECTIVE	III)	PR 50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			36	PC	404195	PROJECT PHASE II		TW 100	40			93	P
404188	PROJECT PHASE I	OR 50	20			40	PC	404195	PROJECT PHASE II		PR 50	20			42	Р
GRAND TOTAL	_ = 1009/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCITO	IN											
	234 SHINDE PRANALI SURYAKANT															
B1200532	234 SHINDE PRANALI SURYAKANT			SAR	OJIN:	I		,7163	3659G ,PICT	,B12005	3234					
	SEM.:1								SEM.:2							
404181	VIST DESIGN & TECHNOLOGY	PP 100	40	13	43	56	РC	404189	MOBILE COMMUNICATION		PP 100	40	18	45	63	P
404182	COMPUTER NETWORKS	PP 100	40	26	35	61	РC	404190	BROADBAND COMMUNICATIO	ON SYS.	PP 100	40	26	54	80	P
404183	MICROWAVE ENGINEERING	PP 100	40	15	55	70	РC	404191C	AUDIO VIDEO ENGINEERIN	1G	PP 100	40	20	50	70	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	22	43	65	P C	404192D	WIRELESS NETWORKS		PP 100	40	28	48	76	P
404185C	PLCS AND AUTOMATION	DD 100	40	19	51	70	P C	404193	LAB PRACTICE III (MC &	BCS)	J.M 20	20			38 40	Р
404100	TAB DRACTICE I (CN & MWE)	1W 50	20			42 41	PC	404193	TAR DRACTICE III (MC &	TTT)	UR 50	20			40 41	P D
404187	LAB PRAC. II(VIST & ELEC. I)	TW 50	20			40	PC	404194	LAB PRAC. IV(ELECTIVE	TTT)	PR 50	20			40	P P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			40	РC	404195	PROJECT PHASE II	,	TW 100	40			90	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			33	P C	404195	PROJECT PHASE II		PR 50	20			42	P
GRAND TOTAL	_ = 1098/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCTIO	N											
	235 SHINDE SHAMAL VIKRAM							7163	2.6601	D1000F			• • • • •			
B1200532	33 SHINDE SHAMAL VIKRAM			P00	UΑ			, /163	3660L ,PICI	,BIZ005	3435					
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	17	51	68	РC	404189	MOBILE COMMUNICATION		PP 100	40	25	49	74	P
404182	COMPUTER NETWORKS	PP 100	40	28	43	71	P C	404190	BROADBAND COMMUNICATIO	ON SYS.	PP 100	40	29	59	88	P
404183	MICROWAVE ENGINEERING	PP 100	40	26	51	7/	P C	404191C	AUDIO VIDEO ENGINEERIN	NG	PP 100	40	23	52	75	Р
404164A 404185C	DIGITAL IMAGE PROCESSING DICS AND AUTOMATION	PP 100 PP 100	40	23	54	7 4 76	PC	4041920	TAR DRACTICE TIT (MC &	RCS)	TW 50	20	49	40	40	P D
404186	LAB PRACTICE I (CN & MWE)	TW 50	20		J 1	45	P C	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			43	РC	404194	LAB PRAC. IV(ELECTIVE	III)	TW 50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			40	P C	404194	LAB PRAC. IV(ELECTIVE	III)	PR 50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			40	P C	404195	PROJECT PHASE II		TW 100	40			95	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OK 50	20			43	ЬC	404195	PROJECT PHASE II		PK 50	20			44	Ь
	- 1104/1500 PECHT TIPCT C															

GRAND TOTAL = 1194/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH DATE :	BRANCH: 71-B.E. (2012 PAT.)(ELECTRONICS & TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 78														
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	 TACI	 Е, М	OTHER,	PER	 MANENT	REG. NO.	, PREVIOUS SEAT NO., COLLEGE	 E, SEAT	NO.				• • • • •
OTHER LI	NES: HEAD OF PASSING, MAX. M	ARKS, MIN	.PAS	S MAR	KS, IN	JT. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED, P/F:I	PASS/FAIL	, C	::PREV	IOUS C	CARRY	OVER
B1200532	36 SHINDE SNEHAL RAMESH			N	ANDA			,7152	7604C ,PICT ,B12005	 53236		• • • • • •			• • • • •
	SEM.:1								SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	18	44	62	РC	404189	MOBILE COMMUNICATION	PP 100	40	24	54	78	P
404182	COMPUTER NETWORKS	PP 100	40	22	53	75	PC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	28	59 47	8.7	P
404163 404184R	EMBEDDED SASLEMS & BLOS	PP 100 PP 100	40	19 21	40	75 61	PC	4041910	MIRELEGS NETWORKS	PP 100 PP 100	40	28	47	75	P D
404185C	PLCS AND AUTOMATION	PP 100	40	14	49	63	PC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20	20	1,	38	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			38	РC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			42	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			43	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			42	PC	404195	PROJECT PHASE II	TW 100	40			95 46	P
404188	PROJECT PHASE I	OR 50	20			40	PC	404195	MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACT. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50	20			40	Р
GRAND TOTAL	. = 1146/1500 . RESULT:FIRST C	LASS WITH	DIS	TINCT	ION										
B1200532	37 SHINDE SUMEET CHANDRAKAN	 Т		Δ	 NTTA	• • • •		7152	7606K PICT B1200					• • • •	
B120053237 SHINDE SUMEET CHANDRAKANT SEM.:1 SEM.:2 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 15 48 63 P C 404189 MOBILE COMMUNICATION PP 100 40 20 47 67 P 404182 COMPUTER NETWORKS PP 100 40 19 29 48 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 21 44 65 P 404183 MICROWAVE ENGINEERING PP 100 40 AA 49 49 P 404191D SOFT COMPUTING PP 100 40 20 37 57 P 404184A DIGITAL IMAGE PROCESSING PP 100 40 24 43 67 P C 404192D WIRELESS NETWORKS PP 100 40 22 36 58 P 404185D ARTIFICIAL INTELLIGENCE PP 100 40 18 41 59 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 41 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 40 40 P C 404193 LAB PRACTICE III(MC & BCS) OR 50 20 38 P 404186 LAB PRACTICE I (CN & MWE) OR 50 20 43 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 40 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 28 P C 404195 PROJECT PHASE II TW 100 40 95 P 404188 PROJECT PHASE I OR 50 20 42 P C 404195 PROJECT PHASE II PR 50 20 466 P															
404101	SEM.:1	DD 100	4.0	1 -	4.0	<i>c</i> 2	D 0	404100	SEM.:2	DD 100	4.0	2.0	47	67	ъ
404181	ATSI DESIGN & LECHNOLOGI	PP 100	40 40	15 10	29	03 48	PC	404189 404190	RECADRAND COMMUNICATION SVS	PP 100	40	20 21	4/	67 65	P D
404183	MICROWAVE ENGINEERING	PP 100	40	AA	49	49	P	404191D	SOFT COMPUTING	PP 100	40	20	37	57	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	24	43	67	PС	404192D	WIRELESS NETWORKS	PP 100	40	22	36	58	P
404185D	ARTIFICIAL INTELLIGENCE	PP 100	40	18	41	59	РC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			40	РC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			43	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			30	P
404187	LAB PRAC. II(VLSI &ELEC. I)	J.M 20	20			28	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			40	Ъ
404187 404188	DROJECT DHASE T	OR 50	20 20			30 42	PC	404195	PROJECT PHASE II	DR 50	20			95 46	P D
							1 0	101133	TROOLET TIMES II	110 50	20			10	-
GRAND TOTAL	= 1012/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	ION										
B1200532	38 SHIRALKAR APURVA ASHOK			S	UNITA			,7163	3661J ,PICT ,B12009	53238			, 		
	SEM.:1								SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	19	51	70	РC	404189	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS)	PP 100	40	21	44	65	P
404182	COMPUTER NETWORKS	PP 100	40	28	46	74	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	25	51	76	P
404183	MICROWAVE ENGINEERING	PP 100	40	21	53	74	РC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	19	51	70	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	22	43	65	PC	404192D	WIRELESS NETWORKS	PP 100	40	30	49	./9	P
4041858	ELECTRONIC PRODUCT DESIGN	TW 50	20	22	52	/4 44	PC	404193	LAB PRACTICE III(MC & BCS)	1W 50	20			38	P P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			40	PC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			40	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			35	РC	404195	PROJECT PHASE II	TW 100	40			80	P
404188	PROJECT PHASE I	OR 50	20			37	P C	404195	LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50	20			32	P
	= 1104/1500 , RESULT:FIRST C														

GRAND TOTAL = 1104/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

71-B.E. (2012 PAT.)(ELECTRONI 04 JUL 2018	.CS &TELE(E INSTIT)	COMU. UTE	NICAT. OF CO	LON) MPUTER	TEC	HNOLOG	Y, PUNE.	PAGE NO.	79					
ST LINE : SEAT NO., NAME OF ES: HEAD OF PASSING, MAX. MA	THE CAND	IDAT .PAS	E, MO S MARI	OTHER,	PER T. M	MANENT ARKS,	REG. NO. TH. MARKS	, PREVIOUS SEAT NO., COLLEG., TOTAL MARKS OBTAINED, P/F:	E, SEAT PASS/FAIL	NO.	:PREVI	OUS C	ARRY	OVER
9 SHIRWAR GLOBAL NAVINKUMAR		• • • •		 STHER			,7163	3662G ,PICT ,B1200	 53239	• • • •			• • • •	• • • • •
VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I)	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 PR 50	40 40 40 40 20 20 20 20	21 27 22 19 20	52 32 53 48 54	73 59 75 67 74 43 38 40 36	P C C C C C C C C C C C C C C C C C C C	404189 404190 404191C 404192D 404193 404193 404194 404194 404195	MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50	40 40 40 20 20 20 20 40	22 26 22 30	49 50 55 49	71 76 77 79 39 40 24 39 96	P P P P P P P
1124/1500 PROVED OF	700 55	DIG	m TNI OM:		13	1 0	101123	IROUTET TIMES IT	110 30	20			13	_
SHITOLE ANMOL SUNIL			V	AISHAL	I		,7163	3663E ,PICT ,B1200	53240					
SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20 20	16 20 18 14 21	51 46 55 44 57	67 66 73 58 78 42 36 37 33 40	P C P C P C P C P C P C P C P C	404189 404190 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 20	24 19 25 29	49 54 49 34	73 73 74 63 40 38 42 39 95 44	P P P P P P P P
= 1111/1500 RESULT:FIRST CI	ASS WITH	DTS	ттист.	TON										
 1 SHIYALE SACHIT SHRAWANJI		• • • •	CI	 HHABBU	 TAI		,7163	3664C ,PICT ,B1200	 53241	• • • •		• • • • •	• • • •	• • • • •
SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 20 20 20 20 20	14 25 20 18 13	31 49 43 43 53					PP 100 PP 100 PP 100	40 40 40	22 21 17	44 38 50	66 59 67	P P P
CONTRACTOR OF STATE O	OH JUL 2018 CENTRE: PUNCT. CET LINE: SEAT NO., NAME OF CENTRE SEAT NO. CENTRE SEAT NO., NAME OF CENTRE SEAT NO., NAME OF CENTRE SEAT NO. CENTRE SEAT NO. CENTRE SEAT NO. CENTRE SEAT NO., NAME OF CENTRE SEAT NO. CENTRE SEAT NO. CENTRE SEAT NO. CENTRE SE	A JUL 2018 CENTRE: PUNE INSTITED TO THE CAND CONTROL OF THE CAND CAND CAND CAND CAND CAND CAND CAND	CENTRE: PUNE INSTITUTE CIT LINE: SEAT NO., NAME OF THE CANDIDAT SE: HEAD OF PASSING, MAX. MARKS, MIN.PAS SHIRWAR GLOBAL NAVINKUMAR SEM.:1 ZUSI DESIGN & TECHNOLOGY PP 100 40 COMPUTER NETWORKS PP 100 40 CIGITAL IMAGE PROCESSING PP 100 40 CIGITAL IMAGE PROCESSING PP 100 40 CIGITAL IMAGE PROCESSING PP 100 40 CIAB PRACTICE I (CN & MWE) TW 50 20 ABB PRACTICE I (CN & MWE) TW 50 20 ABB PRAC. II(VLSI & ELEC. I) PR 50 20 CROJECT PHASE I OR 50 20 CROJECT PHASE I OR 50 20 COMPUTER NETWORKS PP 100 40 CLAS AND AUTOMATION PP 100 40 CLAS AND AUTOMATION PP 100 40 CLAS PRAC. II(VLSI & ELEC. I) TW 50 20 CLAS PRACTICE I (CN & MWE) TW 50 20 CLAS PRACTICE I (CN & MWE) TW 50 20 CLAS PRACTICE I (CN & MWE) OR 50 20 CLAS AND AUTOMATION PP 100 40 CLAS AND AUTOMATION PP 100 40 CLAS PRACTICE I (CN & MWE) OR 50 20 CLAS PRACTICE I (CN & MWE) OR 50 20 CLAS PRACTICE I (CN & MWE) OR 50 20 CLAS PRACTICE I (CN & MWE) OR 50 20 CLAS PRACTICE I (CN & MWE) OR 50 20 CROJECT PHASE I OR 50 20 CROJECT PHASE I OR 50 20 CROJECT PHASE I OR 50 20 COMPUTER NETWORKS PP 100 40 CLOS AND AUTOMATION PP 100 40 ALB PRACTICE I (CN & MWE) OR 50 20 CROMPUTER NETWORKS PP 100 40 COMPUTER NETWORKS PP 100 40 CLOS AND AUTOMATION PP 100 40 CLOS AND AUTOMA	## JUL 2018 CENTRE: PUNE INSTITUTE OF COLUMN	ET LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, S: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, IN. SHIRWAR GLOBAL NAVINKUMAR SEM.:1 ZLSI DESIGN & TECHNOLOGY PP 100 40 21 52 COMPUTER NETWORKS PP 100 40 27 32 IICROWAVE ENGINEERING PP 100 40 19 48 ELECTRONIC PRODUCT DESIGN AB PRACTICE I (CN & MWE) AB PRACTICE I (CN & MWE) AB PRACTICE I (CN & MWE) AB PRACTICE I (CN & WWE) AB PRACTICE I (CN & WHE) AB PRACTICE I (CN & MWE) AB PRACTICE I (CN & MWE) AB PRACTICE I (CN & MWE) COMPUTER NETWORKS PP 100 40 16 51 COMPUTER NETWORKS PP 100 40 16 51 COMPUTER NETWORKS PP 100 40 18 55 COMPUTER NETWORKS PP 100 40 14 44 PLCS AND AUTOMATION PP 100 40 21 57 AB PRACTICE I (CN & MWE) AB PRACTICE I (CN &	A JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TEC IT LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PER SS: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. M SHIRWAR GLOBAL NAVINKUMAR SEM.:1 LISI DESIGN & TECHNOLOGY PP 100 40 21 52 73 COMPUTER NETWORKS PP 100 40 27 32 59 ILICROWAVE ENGINEERING PP 100 40 19 48 67 LLECTRONIC PRODUCT DESIGN PP 100 40 19 48 67 LLECTRONIC PRODUCT DESIGN PP 100 40 19 48 67 LLECTRONIC PRODUCT DESIGN PP 100 40 19 48 67 LLECTRONIC PRODUCT DESIGN PP 100 40 19 48 67 LLECTRONIC PRODUCT DESIGN PP 100 40 19 48 67 LLECTRONIC PRODUCT DESIGN PP 100 40 19 48 67 LLECTRONIC PRODUCT DESIGN PP 100 40 19 48 67 LLECTRONIC PRODUCT DESIGN PP 100 40 19 48 67 LLECTRONIC PRODUCT DESIGN PP 100 40 19 48 67 LLECTRONIC PRODUCT DESIGN PP 100 40 19 48 67 LLECTRONIC PRODUCT DESIGN PP 100 40 19 48 67 LLECTRONIC PRODUCT PRODUC	## JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY FILINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT SE: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, SHIRWAR GLOBAL NAVINKUMAR SEM.:1 **LISI DESIGN & TECHNOLOGY** PP 100 40 21 52 73 P C **INCROWAVE ENGINEERING** PP 100 40 27 32 59 P C **ILCROWAVE ENGINEERING** PP 100 40 22 53 75 P C **ILCROWAVE ENGINEERING** PP 100 40 19 48 67 P C **ILCROWAVE ENGINEERING** PP 100 40 19 48 67 P C **ILCROWAVE ENGINEERING** PP 100 40 20 54 74 P C **IAB PRACTICE I (CN & MWE)	A JUL 2018	MAY JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO.	MAY 1 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 79	MARTIC SEAT NO. NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO. PREVIOUS SEAT NO. COLLEGE. SEAT NO.	A JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 79	MAY 1 2018 CENTRE: PURE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 79	M JUL 2018 CENTRE: FUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 79 TILNE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, DEFMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. SHERAD OF PASSING, MAA, MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OSTAINED, PP:PASS/FAIL, C:PREVIOUS CARRY SHIRMAR GLOBAL NAVINKUMAR ESTHER 7,16336620 , PLCT , B120053239 SEN: 1 LICENIES STATE OF THE CANDIDATE, MOTHER DEFMANENT REG. NO., PREVIOUS SEAT NO. COLLEGE, SEAT NO. SHIRMAR GLOBAL NAVINKUMAR ESTHER 7,16336620 , PLCT , B120053239 SEN: 2 LICENIES STATE OF THE COMPUTER NETWORKS OF THE

GRAND TOTAL = 1031/1500 , RESULT:FIRST CLASS WITH DISTINCTION

DATE :	04 JUL 2018 CENTRE: PU	NE INST	'ITUTE	OF C	OMPUTER	R TEC	CHNOLO	GY, PUNE.	PA	GE NO. 8	30					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CA	NDIDA'	ΓE,	MOTHER,	, PER	MANEN'	T REG. NO.	, PREVIOUS SEAT NO.,	COLLEGE	l, SEAT	NO.				
OTHER LI	NES: HEAD OF PASSING, MAX. M	ARKS, M	IN.PA	SS MA	RKS, IN	JT. M	MARKS,	TH. MARKS	, TOTAL MARKS OBTAINED	, P/F:P	PASS/FAII	J, C	:PREV	IOUS (CARRY	OVER
B1200532	42 SHREYAS HEMANT NAZARE				KALPANA	<i>A</i>	• • • • •	,7152	7613B ,PICT	,B12005	3242					
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 10	0 40	15	60	75	РC	404189	MOBILE COMMUNICATION		PP 100	40	27	54	81	P
404182	COMPUTER NETWORKS	PP 10	0 40	26	44	70	РC	404190	BROADBAND COMMUNICATION	ON SYS.	PP 100	40	28	49	77	P
404183	MICROWAVE ENGINEERING	PP 10	0 40	23	44	67	РC	404191C	AUDIO VIDEO ENGINEERI	NG	PP 100	40	25	53	78	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 10	0 40	22	44	66	P C	404192D	WIRELESS NETWORKS	DGG)	PP 100	40	27	50	77	P
404185D	ARTIFICIAL INTELLIGENCE	DD TO	0 40	18	56	/4 /E	P C	404193	LAB PRACTICE III (MC &	BCS)	J.M 20	20			44	Ъ
404186	LAB PRACITCE I (CN & MWE)	OP 5	0 20			45	P C	404193	TAB PRACTICE III (MC &	BCS)	UK 50	20			2/	P D
404180	LAB PRACTICE I (CN & MWE)	TW 5	0 20			43	P C	404194	IAB PRAC. IV(ELECTIVE	TTT)	DR 50	20			40	D
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 5	0 20			34	PC	404195	PROJECT PHASE IT	/	TW 100	40			96	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 5	0 20			46	РC	404195	PROJECT PHASE II		PR 50	20			44	P
	= 1178/1500 , RESULT:FIRST C															
B1200532	43 SHRUTI ATUL MALI				JAYSHRE	EE MA	LI	,7152	7615J ,PICT	,B12005	3243					
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 10	0 40	13	43	56	P C	404189	MOBILE COMMUNICATION		PP 100	40	25	48	73 67	P
404182	COMPUTER NETWORKS	PP 10	0 40	24	5 I	/5	P C	404190	BROADBAND COMMUNICATIO	ON SYS.	PP 100	40	28	39 54		
404183	MICROWAVE ENGINEERING	PP 10	0 40	13 1Ω	30	48 57	P C	4041910	DIOMEDICAL CICNAL DDO	NG TECCTNO	PP 100	40	19 27	57	Q /I	D
404185C	DIGITAL IMAGE PROCESSING	DD 10	0 40	25	44	69	РC	404192A 404193	IAR DRACTICE III (MC &	BCS)	TW 50	20	۷ /	57	40	D
404186	LAB PRACTICE I (CN & MWE)	TW 5	0 20	23		38	PC	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR 5	0 20			36	PC	404194	LAB PRAC. IV(ELECTIVE	III)	TW 50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 5	0 20			32	РC	404194	LAB PRAC. IV(ELECTIVE	III)	PR 50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 5	0 20			34	РC	404195	PROJECT PHASE II		TW 100	40			94	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 5	0 20			41	РC	404195	PROJECT PHASE II		PR 50	20	27		45	P
GRAND TOTAL	= 1081/1500 , RESULT:FIRST C	LASS WI	TH DI	STINC	TION											
D1000F30	44 SHUBHAM JAKHOTIYA		• • • • •						7.61.63						· · · ·	
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 10	0 40	10	41	51	РC	404189	MOBILE COMMUNICATION		PP 100	40	18	28	46	P
404182	COMPUTER NETWORKS	PP 10	0 40	05	41	46	РC	404190	BROADBAND COMMUNICATION	ON SYS.	PP 100	40	12	36	48	P
404183	MICROWAVE ENGINEERING	PP 10	0 40	15	29	44	PC	404191C	AUDIO VIDEO ENGINEERI	NG	PP 100	40	12	51	63	P
404184A	DIGITAL IMAGE PROCESSING	PP 10	0 40	1/	4∠ 21	59 4 E	P C	404192D	WIRELESS NETWORKS	DGG \	DD IOO	20	22	3 L	20	P
404185C 404186	TAR DRACTICE T (CN & MWE)	LM E	0 40	⊥ 4	5 L	45 42	D C	404193	TAB DRACTICE III(MC &	BCG)	UB EU	∠∪ 2∩			∡α 20	P D
404186	LAR PRACTICE I (CN & MWE)	OR 5	0 20			35	P C	404194	TAR PRACTICE III (MC &	TTT)	TW 50	20			28	Þ
404187	LAB PRAC. II(VLSI &ELEC. T)	TW 5	0 20			32	PC	404194	LAB PRAC. IV(ELECTIVE	III)	PR 50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 5	0 20			28	PC	404195	PROJECT PHASE II	,	TW 100	40			90	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 5	0 20			35	РĈ	404195	PROJECT PHASE II		PR 50	20			40	P
	= 885/1500 , RESULT:HIGHER S															

SAVITRIBAI PHULE PUNE UNIVERSITY, B.E.(2012 COURSE) EXAMINATION, MAY 2018 BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE	TUTE OF COMPUTER	R TECHNOLOGY, PUNE.	PAGE NO. 8	31
NOTE: FIRST LINE : SEAT NO., NAME OF THE CAN OTHER LINES: HEAD OF PASSING, MAX. MARKS, MI	DIDATE, MOTHER, N.PASS MARKS, IN	, PERMANENT REG. NO. VT. MARKS, TH. MARKS	., PREVIOUS SEAT NO., COLLEGE S, TOTAL MARKS OBTAINED, P/F:F	C, SEAT NO. PASS/FAIL, C:PREVIOUS CARRY OVER
B120053245 SHUKLA MAMTA RAMENDRA		,7152		3245
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 404182 COMPUTER NETWORKS PP 100 404183 MICROWAVE ENGINEERING PP 100 404184B EMBEDDED SYSTEMS & RTOS PP 100 404185B ELECTRONIC PRODUCT DESIGN PP 100 404186 LAB PRACTICE I (CN & MWE) TW 50 404186 LAB PRACTICE I (CN & MWE) OR 50 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 404188 PROJECT PHASE I OR 50	40 14 55 40 26 43 40 19 37 40 25 49 40 23 49 20 20 20 20 20	69 P C 404189 69 P C 404190 56 P C 404191C 74 P C 404193 36 P C 404193 45 P C 404194 37 P C 404194 42 P C 404195 44 P C 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING BIOMEDICAL SIGNAL PROCESSING LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 29 37 66 P PP 100 40 27 54 81 P PP 100 40 22 48 70 P PP 100 40 28 54 82 P TW 50 20 43 P OR 50 20 43 P TW 50 20 43 P PR 50 20 42 P TW 100 40 95 P PR 50 20 45 P
GRAND TOTAL = 1151/1500 . RESULT:FIRST CLASS WITH	H DISTINCTION			
B120053246 SHWETA IYER	GITA	,7152	27621C ,PICT ,B12005	33246
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 404182 COMPUTER NETWORKS PP 100 404183 MICROWAVE ENGINEERING PP 100 404184A DIGITAL IMAGE PROCESSING PP 100 404185C PLCS AND AUTOMATION PP 100 404186 LAB PRACTICE I (CN & MWE) TW 50 404186 LAB PRACTICE I (CN & MWE) OR 50 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 404188 PROJECT PHASE I OR 50				
CDAND TOTAL - 1110/1500 DECLITEDED OF ACCUMENT	II DICUTNICUTONI			
B120053247 SINGH NAVIN BACHCHAN SINGH	LALSA	,7152	27623K ,PICT ,B12005	33247
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 404182 COMPUTER NETWORKS PP 100 404183 MICROWAVE ENGINEERING PP 100 404184A DIGITAL IMAGE PROCESSING PP 100 404185C PLCS AND AUTOMATION PP 100 404186 LAB PRACTICE I (CN & MWE) TW 50 404186 LAB PRACTICE I (CN & MWE) OR 50 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 404188 PROJECT PHASE I OR 50	40 19 40 40 21 38 40 18 34 40 16 43 40 19 39 20 20 20 20 20	59 P C 404189 59 P C 404190 52 P C 404191C 59 P C 404193 40 P C 404193 36 P C 404194 36 P C 404194 38 P C 404195 31 P C 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACT IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 18 42 60 P PP 100 40 24 50 74 P PP 100 40 19 41 60 P PP 100 40 28 36 64 P TW 50 20 38 P OR 50 20 39 P TW 50 20 40 P PR 50 20 37 P TW 100 40 50 P PR 50 20 25 P
GRAND TOTAL = 955/1500 , RESULT:FIRST CLASS				

BRANCH DATE :	BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 82															
NOTE: FI	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER															
OTHER LI	NES: HEAD OF PASSING, MAX. M.	ARKS, MIN	.PAS	S MAR	KS, IN	IT. M	ARKS,	TH. MARKS	S, TOTAL MARKS OBTAIN	IED, P/F:	PASS/FAII	, C	PREV	IOUS (ARRY	OVER
B1200532	48 SINGH VIKAS RAJESH				UNITA			,7152	.7624H ,PICT	,B1200	53248				• • • •	• • • • •
	SEM.:1								SEM.:2							
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (ULSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100	40	12	41	53	РC	404189	MOBILE COMMUNICATION	N	PP 100	40	20	40	60	P
404182	COMPUTER NETWORKS	PP 100	40	24	38	62	P C	404190	BROADBAND COMMUNICA	ATION SYS.	PP 100	40	27	41	68	P
404183 404184A	DIGITAL IMAGE PROCESSING	PP 100 PP 100	40	19 14	3 <i>3</i>	5∠ 48	PC	404191C 404192D	WIRELESS NETWORKS	ERING	PP 100	40	20 25	43 33	58	P
404185C	PLCS AND AUTOMATION	PP 100	40	12	39	51	PC	404193	LAB PRACTICE III(MO	C & BCS)	TW 50	20	23	33	43	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			38	РC	404193	LAB PRACTICE III (MC	C & BCS)	OR 50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			37	РC	404194	LAB PRAC. IV(ELECT	VE III)	TW 50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			42	PC	404194	LAB PRAC. IV(ELECT)	IVE III)	PR 50	20 40			39	P
404188	PROJECT PHASE I	OR 50	20			42	PC	404195	PROJECT PHASE II		PR 50	20			44	P
																_
GRAND TOTAL	= 1002/1500 RESULT:FIRST C	LASS WITH	DIS	TINCT	'ION											
D1200522	40 COMMANCHT KETAN MICHMANA		• • • •				• • • • • •	7150	76201 DIOT	D1200		• • • •	• • • • •		• • • •	• • • • •
B120053249 SOMVANSHI KETAN VISHVANATH SUNITA ,71527628L ,PICT ,B120053249																
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 12 50 62 P C 404189 MOBILE COMMUNICATION PP 100 40 29 45 74 P 404182 COMPUTER NETWORKS PP 100 40 26 40 66 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 23 46 69 P 404183 MICROWAVE ENGINEERING PP 100 40 21 41 62 P C 404191 SOFT COMPUTING PP 100 40 22 45 67 P 404184B EMBEDDED SYSTEMS & RTOS PP 100 40 18 38 56 P C 404192D WIRELESS NETWORKS PP 100 40 28 45 73 P 404185D ARTIFICIAL INTELLIGENCE PP 100 40 19 52 71 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 41 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 34 P C 404193 LAB PRACTICE III(MC & BCS) OR 50 20 42 P 404186 LAB PRACTICE I (CN & MWE) OR 50 20 42 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 34 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 37 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 41 P 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 43 P C 404195 PROJECT PHASE II TW 100 40 97 P 404188 PROJECT PHASE I OR 50 20 43 P C 404195 PROJECT PHASE II PR 50 20 48 P																
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	12	50	62	РC	404189	MOBILE COMMUNICATION	N	PP 100	40	29	45	74	P
404182	COMPUTER NETWORKS	PP 100	40	26	40	66	PC	404190	BROADBAND COMMUNICA	ATION SYS.	PP 100	40	23	46	69	P
404183 404184R	MICKOWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS	PP 100 PP 100	40 40	∠⊥ 18	38	6∠ 56	PC	404191D 404192D	WIRELESS NETWORKS		PP 100	40	22 28	45 45	0 / 73	P D
404185D	ARTIFICIAL INTELLIGENCE	PP 100	40	19	52	71	PC	404193	LAB PRACTICE III(MO	C & BCS)	TW 50	20	20	15	41	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			34	РC	404193	LAB PRACTICE III (MC	C & BCS)	OR 50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			42	РC	404194	LAB PRAC. IV(ELECT	IVE III)	TW 50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			37	P C	404194	LAB PRAC. IV(ELECT)	IVE III)	PR 50	20			41	P
404187	PROJECT PHASE T	OR 50	20			39 43	PC	404195	PROJECT PHASE II		TW 100	20			9 / 4 8	P
101100	IROUTET TIMBE I	on 50	20			13	1 0	10 11 2 3	TROOLET THISE IT		110 50	20			10	-
GRAND TOTAL	= 1098/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCT	ION											
B1200532	50 SREERADH SREEKUMAR				ADHIKA	4		,7152	7634E ,PICT	,B1200	53250	• • • •				
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	04	32	36#	P	404189	MOBILE COMMUNICATION	ON	PP 100	40	19	33	52	P
404182	COMPUTER NETWORKS	PP 100	40	21	35	56	РC	404190	BROADBAND COMMUNICA	ATION SYS.	PP 100	40	16	35	51	P
404183	MICROWAVE ENGINEERING	PP 100	40	13	28	41	P C	404191C	AUDIO VIDEO ENGINE	ERING	PP 100	40	12	38	50	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	04	40	44	P C	404192D	WIRELESS NETWORKS	a c Daa)	PP 100	40	21	31	52	P
4041650	LAR PRACTICE I (CN & MWE)	TW 50	20	0.6	34	36	PC	404193	TAB PRACTICE III (MC	& BCS)	OR 50	20			30	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			37	P C	404194	LAB PRAC. IV(ELECT)	IVE III)	TW 50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			38	РC	404194	LAB PRAC. IV(ELECT	VE III)	PR 50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			36	P C	404195	PROJECT PHASE II		TW 100	40			43	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (ULSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OK 50	20			29	ЬC	404195	PROJECT PHASE II		PR 50	20			21	Ъ
GRAND TOTAL	= 800/1500 , RESULT:SECOND C	LASS [#	0.4]												
,				-			R	RESULT RES	SERVED FOR BACKLOGS.							

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCE DATE :	BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 83														
NOTE: F	RST LINE : SEAT NO., NAME OF	THE CAND	IDAT	Έ,	MOTHER,	, PER	RMANENT	reg. No.	, PREVIOUS SEAT NO., COLLI	GE, SEAT	ΓNO.				
O'T'HER L	NES: HEAD OF PASSING, MAX. M	ARKS, MIN	. PAS	SS MA	RKS, IN	ν.Τ. * Ι _ν	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED, P/I	: PASS/FAII	., C	PREV	TOUS (CARRY	OVER
B1200532	251 SRINJAY SOUMITRA SARKAR				BHAKTI			,7152	7635C ,PICT ,B120	053251					
	SEM.:1								SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	17	44	61	РC	404189	MOBILE COMMUNICATION	PP 100	40	17	32		
404182 404183	COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE	PP 100	40 40	19 17	41 37	60 54	ъс Р	404190 404191n	BROADBAND COMMUNICATION SYS	DD 100	40 40	21 14	40 28	61 42	P P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	16	36	52	PC	404192A	BIOMEDICAL SIGNAL PROCESSIN	G PP 100	40	21	2 E	E 6	_ D
404185D	ARTIFICIAL INTELLIGENCE	PP 100	40	15	43	58	РC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			32	P
404186 404186	LAB PRACTICE I (CN & MWE)	TW 50	20			33	P C	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			40	P
404186	TAB PRACTICE I (CN & MWE)	OR 50	20			38 35	PC	404194	IAB PRAC. IV(ELECTIVE III)	PR 50	20			∠8 33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			32	РC	404195	PROJECT PHASE II	TW 100	40			96	P
404188	ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			41	РC	404195	PROJECT PHASE II	PR 50	20			46	P
GRAND TOTAL	_ = 947/1500 . RESULT:FIRST CL	ASS													
B1200533	D52 SIIDADKAD NATRIITT MANO.T	• • • • • • • •		• • • •	DARCHAN	 .T.Z\			7640K DICT R120	053252			• • • • •		• • • • •
B1200552	B120053252 SURADKAR NAIRUTI MANOJ DARSHANA ,71527640K ,PICT ,B120053252														
SEM.:1 SEM.:2 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 14 40 54 P C 404189 MOBILE COMMUNICATION PP 100 40 24 44 68 P 404182 COMPUTER NETWORKS PP 100 40 27 28 55 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 26 50 76 P 404183 MICROWAVE ENGINEERING PP 100 40 23 33 56 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 24 51 75 P 404184B EMBEDDED SYSTEMS & RTOS PP 100 40 22 42 64 P C 404192D WIRELESS NETWORKS PP 100 40 28 36 64 P 404185C PLCS AND AUTOMATION PP 100 40 18 43 61 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 34 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 41 P C 404194 LAB PRACTICE III(MC & BCS) OR 50 20 41 P 404187 LAB PRACT. IV(ELECTIVE III) TW 50 20 36 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 36 P 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 39 P 404188 PROJECT PHASE II TW 100 40 93 P 404188 PROJECT PHASE II OR 50 20 44 P															
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	14	40	54	PC	404189	MOBILE COMMUNICATION CV	PP 100	40	24	44 50	68 76	P
404183	MICROWAVE ENGINEERING	PP 100 PP 100	40	23	⊿o 33	56	P C	404190 404191C	AUDIO VIDEO ENGINEERING	PP 100	40	24	50 51	75	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	22	42	64	РC	404192D	WIRELESS NETWORKS	PP 100	40	28	36	64	P
404185C	PLCS AND AUTOMATION	PP 100	40	18	43	61	РC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			34	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			34	P C	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			41	P
404186	TAB PRACTICE I (CN & MWE)	OR 50	20			36	PC	404194	IAB PRAC. IV(ELECTIVE III)	PR 50	20			30 39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			37	РC	404195	PROJECT PHASE II	TW 100	40			93	P
404188	PROJECT PHASE I	OR 50	20			39	РC	404195	PROJECT PHASE II	PR 50	20			44	P
GRAND TOTAL	$_{i}$ = 1047/1500 . RESULT:FIRST C	LASS WITH	DTS	TTNC	TTON										
B1200532	253 SURUDWAD MANOJKUMAR BALA	JIRAO	• • • •	• • • •	ARUNABA	 AI			7642F .PICT .B120	053253	· · · · ·				• • • • •
								,							
101121	SEM.:1	חח 100	40	1 2	5.2	6.1	рС	101189	SEM.:2 MOBILE COMMUNICATION	חח 100	40	20	42	62	D
404182	VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING	PP 100	40	19	33	52	PC	404190	BROADBAND COMMUNICATION SYS	. PP 100	40	26	41	67	
404183	MICROWAVE ENGINEERING	PP 100	40	12	32	44	РC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	20	45		P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	23	49	72	P C	404192D	WIRELESS NETWORKS	PP 100	40	29	37	66	P
404185B 404186	ELECTRONIC PRODUCT DESIGN	PP 100	20	13	46	59 42	P C	404193	LAB PRACTICE III (MC & BCS)	UM 50	20			34	P P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			39	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			32	РĈ	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			38	P C	404195	LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	TW 100	40			94	P
404188	PROJECT PHASE I	OR 50	20			40	ЪС	404195	PROJECT PHASE II	PR 50	20			45	P
GRAND TOTAL	VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACT II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I = 1022/1500 , RESULT:FIRST C	LASS WITH	DIS	STINC	TION										

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH: 71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMORICATION) DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 84														
NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, MAX. I	NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER													
B120053254 SUSHIL PATIL		MA	NJUSHA		,7152	7643D ,PICT ,B1200	53254							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 TW 50 20 PR 50 20 OR 50 20	13 21 18 28 20	45 58 31 52 28 46 44 72 48 68 33 37 31 42 47	P C C C C C C C C C C C C C C C C C C C	404189 404190 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACT IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 TW 50 20 TW 50 20 TW 50 20 TW 100 40 PR 50 20	22 23 18 25	40 50 51 46	62 P 73 P 69 P 71 P 35 P 39 P 24 P 37 P 97 P 47 P				
GRAND TOTAL - 1040/1300 , RESULTITIEST N	THOS WIII DI	DITMCIT	OIV											
B120053255 SUSHIL S. KHAIRNAR KIRAN ,71527644B ,PICT ,B120053255														
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOS 404185C PLCS AND AUTOMATION 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I				P C C C C C C C C C C C C C C C C C C C	404189 404190 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACT IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 PP 100 40 PP 100 40 PP 100 40 TW 50 20 TW 50 20 TW 50 20 TW 100 40 PR 50 20	25 27 18 28	52	81 P 70 P				
GRAND TOTAL = 1150/1500 , RESULT:FIRST (
B120053256 SUTKAR ADITYA SUBHASH		VA	RSHA		,7152	7645L ,PICT ,B1200	53256							
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSING 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MWE) 404186 LAB PRACTICE I (CN & MWE) 404187 LAB PRAC. II(VLSI & ELEC. I) 404187 LAB PRAC. II(VLSI & ELEC. I) 404188 PROJECT PHASE I				P C C C C C C C C C C C C C C C C C C C	404189 404190 404191C 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACT. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 PP 100 40	22 16	28	72 P 44 P				
GRAND TOTAL = 1025/1500 , RESULT:FIRST (CLASS WITH DI	STINCTI	ON											

SAVITRIBAI PHULE PUNE UNIVERSITY, B.E.(2012 COURSE) EXAMINATION, MAY 2018 BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PU	NE INSTIT	UTE	OF C	OMPUTER	R TEC	CHNOLOG	GY, PUNE.	P.	AGE NO.	85					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	IDAT	 E. 1	 MOTHER,	PER	MANEN	 I REG. NO.	PREVIOUS SEAT NO.,	 COLLEG	E. SEZ	ON TA				• • • • •
OTHER LI	NES: HEAD OF PASSING, MAX. M.	ARKS, MIN	.PAS	S MA	RKS, IN	IT. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED), P/F:	PASS/FA	L, (C:PREV	IOUS C	CARRY	OVER
B1200532	57 SUTRAVE SUSHANT SUDHIR				SARITA		• • • • •	,7152	7646J ,PICT	,B1200	53257	• • • •				• • • • •
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	11	35	46	РC	404189	MOBILE COMMUNICATION		PP 100	40	18	34	52	P
404182	COMPUTER NETWORKS	PP 100	40	13	34	47	P	404190	BROADBAND COMMUNICATI	ION SYS.	PP 100	40	21	41	62	P
404183	MICROWAVE ENGINEERING	PP 100	40	18	31	49	РC	404191C	AUDIO VIDEO ENGINEERI	ING	PP 100	40	14	37	51	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	21	35	56	РC	404192D	WIRELESS NETWORKS		PP 100) 40	17	34	51	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	20	36	56	P C	404193	LAB PRACTICE III (MC &	BCS)	TW 50	20			28	P
404186	LAB PRACTICE I (CN & MWE)	.I.M 20	20			42	PC	404193	LAB PRACTICE III (MC &	BCS)	OR 50	20			43	Ь
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			36	PC	404194	LAB PRAC. IV(ELECTIVE	5 111)	J.M 20	20			25	Р
404187	LAB PRAC. II(VLSI & ELEC. I)	IM 20	20			20	PC	404194	DROTECT DIAGE IT	7 TTT)	PK 50) 40			35	P
404107	DROTECT DUNCE T	OD 50	20			⊿o ⊿1	P C	404195	DROTECT DUACE II		IM IO	1 20			92	P D
404100	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			41	PC	404195	PROJECT PHASE II		PR 50) 20			44	Р
	= 914/1500 , RESULT:FIRST CL															
B120053258 TAKALIKAR SUPRIYA SUBHASH KALAVATI ,71527647G ,PICT ,B120053258																
404181	SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 15 44 59 P C 404189 MOBILE COMMUNICATION PP 100 40 18 47 65 P 404182 COMPUTER NETWORKS PP 100 40 24 30 54 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 29 47 76 P 404183 MICROWAVE ENGINEERING PP 100 40 18 39 57 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 17 39 56 P 404184B EMBEDDED SYSTEMS & RTOS PP 100 40 22 37 59 P C 404192D WIRELESS NETWORKS PP 100 40 24 29 53 P 404185B ELECTRONIC PRODUCT DESIGN PP 100 40 17 46 63 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 38 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 34 P C 404193 LAB PRACTICE III(MC & BCS) OR 50 20 43 P 404186 LAB PRACTICE I (CN & MWE) OR 50 20 42 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 40 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 37 P C 404195 PROJECT PHASE II TW 100 40 95 P 404188 PROJECT PHASE I OR 50 20 41 P C 404195 PROJECT PHASE II PR 50 20 466 P															
404182	COMPUTER NETWORKS	PP 100	40	24	30	54	РC	404190	BROADBAND COMMUNICATI	ON SYS.	PP 100	40	29	47	76	P
404183	MICROWAVE ENGINEERING	PP 100	40	18	39	57	РC	404191C	AUDIO VIDEO ENGINEERI	ING	PP 100	40	17	39	56	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	22	37	59	РC	404192D	WIRELESS NETWORKS		PP 100	40	24	29	53	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	17	46	63	РC	404193	LAB PRACTICE III(MC &	BCS)	TW 50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			34	РC	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			42	РC	404194	LAB PRAC. IV(ELECTIVE	E III)	TW 50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			38	РC	404194	LAB PRAC. IV(ELECTIVE	E III)	PR 50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			37	РC	404195	PROJECT PHASE II		TW 100	40			95	P
404188	PROJECT PHASE I	OR 50	20			41	РC	404195	PROJECT PHASE II		PR 50	20			46	P
GRAND TOTAL	= 1033/1500 . RESULT:FIRST C	LASS WITH	DIS	TINC'	TION											
B1200532	59 TAMBADE PRANAV PRAFULLA		• • • •	• • • • •		• • • •	• • • • •	7152	7649C DICT	R1200	53259	• • • •				• • • • •
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	8 0	41	49	РC	404189	MOBILE COMMUNICATION		PP 100	40	10	30\$	40	P
404182	COMPUTER NETWORKS	PP 100	40	16	34	50	P	404190	BROADBAND COMMUNICATI	ION SYS.	PP 100) 40	23	34	57	P
404183	MICROWAVE ENGINEERING	PP 100	40	17	39	56	РC	404191D	SOFT COMPUTING		PP 100) 40	19	30	49	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	06	44	50	PC	404192D	WIRELESS NETWORKS	, Daa'	PP 100	40	18	28	46	Ь
404185C	PLCS AND AUTOMATION	LL TO 0	40	14	38	52	ь с	404193	LAB PRACTICE III (MC 8	RCS)	.I.M 2(20			36 41	F.
404186 404186	LAB PRACTICE I (CN & MWE)	T.M 20	20			38 27	P C	404193	LAB PRACTICE III (MC &	K BCS)	UK 50	. ∠0			41 25	P.
4U4186 404189	LAB PRACITOR I (CN & MWE)	UK 50	∠U			3 / 20	b G	404194	LAB PRAC. IV(ELECTIVE	2 TTT/	TM 2(. ∠U			35 20	D P
404107	IND DDAC II(VIDI GELEC. I)	IM 20	20			3 U 2 Q	P C	404194	DDOTECT DUNCE IT	7 TTT)	וכ אם יינו זויי	, <u>4</u> 0			20	D
404188	PROJECT PHASE T	OR 50	20			30 35	PC	404195	PROJECT PHASE IT		DR 50) 20			42	P D
104100	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	010 30	20			55	1 (101175	INCOLCI IMPE II		110 30	, 40				-
	= 912/1500 , RESULT:FIRST CL															

BRANCH: 71-B.E. (2012 PAT.)(ELE DATE: 04 JUL 2018 CENTR	CTRONICS &TELECOMUNI RE: PUNE INSTITUTE OF	CATION) F COMPUTER TECHNOLOG	GY, PUNE. PAG	E NO. 86
NOTE: FIRST LINE : SEAT NO., NA OTHER LINES: HEAD OF PASSING, N	ME OF THE CANDIDATE, MAX. MARKS, MIN.PASS	, MOTHER, PERMANENT MARKS, INT. MARKS,	r reg. no., previous seat no., Th. Marks, total marks obtained,	COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
B120053260 TAMBE RUSHIKESH DII		SHAILA	,71633665M ,PICT	,B120053260
				PP 100 40 20 47 67 P ON SYS. PP 100 40 27 56 83 P OG PP 100 40 22 52 74 P PP 100 40 27 47 74 P BCS) TW 50 20 44 P BCS) OR 50 20 38 P III) TW 50 20 40 P III) PR 50 20 41 P TW 100 40 94 P PR 50 20 46 P
GRAND TOTAL = 1159/1500 , RESULT:FI	RST CLASS WITH DIST	INCTION		
B120053261 TANAY SUDARSHAN CHO	OUGULE		,71527652C ,PICT	,B120053261
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184A DIGITAL IMAGE PROCESSIN 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MW 404186 LAB PRACTICE I (CN & MW 404187 LAB PRAC. II(VLSI & ELECT 404187 LAB PRAC. II(VLSI & ELECT 404188 PROJECT PHASE I	FY PP 100 40 1 PP 100 40 2 PP 100 40 2 PP 100 40 2 PP 100 40 2 PP 100 40 1 FE TW 50 20 PF 50 20 PF 100 40 1 FE TW 50 20 PF 50 20 PF 50 20 OR 50 20	14 37 51 P C 22 33 55 P C 20 31 51 P C 24 48 72 P C 15 43 58 P C 45 P C 39 P C 30 P C 41 P C 42 P C		PP 100 40 25 40 65 P PP 100 40 25 40 65 P PP 100 40 22 34 56 P PP 100 40 21 28 49 P BCS) TW 50 20 35 P BCS) OR 50 20 36 P III) TW 50 20 28 P III) PR 50 20 39 P TW 100 40 95 P PR 50 20 46 P
GRAND TOTAL = 998/1500 , RESULT:FIF				
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY 404182 COMPUTER NETWORKS 404183 MICROWAVE ENGINEERING 404184B EMBEDDED SYSTEMS & RTOSY 404185D ARTIFICIAL INTELLIGENCE 404186 LAB PRACTICE I (CN & MW 404186 LAB PRACTICE I (CN & MW 404187 LAB PRAC. II(VLSI & ELECT 404187 LAB PRAC. II(VLSI & ELECT 404188 PROJECT PHASE I GRAND TOTAL =/1500 , RESULT:FAIL	PP 100 40 0 PP 100 40 1 PP 50 20 PP 100 40 1 PP 50 20			PP 100 40 16 18 F ON SYS. PP 100 40 23 37 60 P ON SYS. PP 100 40 16 50 66 P PP 100 40 18 33 51 P BCS) TW 50 20 36 P BCS) OR 50 20 39 P III) TW 50 20 30 P III) PR 50 20 34 P TW 100 40 95 P PR 50 20 45 P

DATE	1:71-B.E. (2012 PAT.)(ELECTRON. : 04 JUL 2018 CENTRE: PUI	NE INSTITUTE	OF CO	ION) MPUTER'	TECHNOL	OGY, PUNE.	PAGE NO.	87							
NOTE: FI	IRST LINE : SEAT NO., NAME OF INES: HEAD OF PASSING. MAX. MA	THE CANDIDA	TE, M	OTHER, I	PERMANE: . MARKS	NT REG. NO. . TH. MARKS	, PREVIOUS SEAT NO., COLLEG S. TOTAL MARKS OBTAINED. P/F:	E, SEAT NO. PASS/FAIL. C:PREV	/IOUS C	ARRY OVER					
B1200532	263 THAKARE BHAGYASHREE RAJEI	 NDRA	s	UNITA		,7104		 53263		• • • • • • • •					
404181 404182 404183 404184A 404185C 404186 404186 404187 404187 404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 40 PP 50 20 TW 50 20 PR 50 20 OR 50 20 OR 50 20	07 17 03 14 14	AA AA AA 32 AA	F F 46 P C F 29 P 25 P AA F AA F	404189 404191D 404191D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. SOFT COMPUTING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 15 PP 100 40 16 PP 100 40 20 PP 100 40 21 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	AA AA AA AA	F F F AA F AA F 26 P 24 P 70 P 35 P					
GRAND TOTAL	L =/1500 , RESULT:FAILS					RESULT RES	SERVED FOR BACKLOGS.								
P120053	RESULT RESERVED FOR BACKLOGS. B120053264 THAKUR NUPUR SHEKHAR MEENAL ,71527665E ,PICT ,B120053264														
404181 404182 404183 404184A 404185B 404186 404186 404187 404187 404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100 40 TW 50 20 OR 50 20 PR 50 20 OR 50 20 OR 50 20	16 25 25 26 18	52 38 36 49 49	68 P C 63 P C 61 P C 67 P C 67 P C 36 P C 36 P C 37 P C 47 P C 38 P C	404189 404190 404191D 404192D 404193 404193 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. SOFT COMPUTING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 26 PP 100 40 25 PP 100 40 17 PP 100 40 25 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	44 52 44 40	70 P 77 P 61 P 65 P 35 P 41 P 28 P 40 P 91 P 40 P					
GRAND TOTAL	L = 1080/1500 , RESULT:FIRST C	LASS WITH DI	STINCT	ION											
B1200532	265 THATTE SNEHA UDAY		 T.	ANVI		,7152		53265		• • • • • • • • •					
	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I L = 1207/1500 , RESULT:FIRST CI				83 P C 67 P C 67 P C 683 P C 642 P C 641 P C 644 P C 644 P C	404189 404191C 404191C 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATION BROADBAND COMMUNICATION SYS. AUDIO VIDEO ENGINEERING WIRELESS NETWORKS LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PP 100 40 23 PP 100 40 29 PP 100 40 17 PP 100 40 29 TW 50 20 OR 50 20 TW 50 20 PR 50 20 TW 100 40 PR 50 20	46 58 61 59	69 P 87 P 78 P 88 P 42 P 42 P 40 P 40 P 94 P 44 P					

DATE :	04 JUL 2018 CENTRE: PU	NE INSTITUTE	OF C	TTON) OMPUTER	R TEC	HNOLOG	SY, PUNE.	PAGE NO.	88				
NOTE: FI OTHER LI	04 JUL 2018 CENTRE: PU. CRST LINE: SEAT NO., NAME OF CONTROL OF PASSING, MAX. M.	THE CANDIDA ARKS, MIN.PA	ATE, I ASS MAI	MOTHER, RKS, IN	, PER NT. M	MANENT IARKS,	reg. No. Th. MARKS	, PREVIOUS SEAT NO., COLLEG 5, TOTAL MARKS OBTAINED, P/F:	E, SEAT PASS/FAIL	NO. ., C:Pi	REVIOUS	CARRY	OVER
	266 THOKE SHREYAS ANIRUDHA											• • • • •	
	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACT II (VLSI & ELEC. I) LAB PRAC. II (VLSI & ELEC. I) PROJECT PHASE I									40 2 40 2 40 2 40 2 20 20 20 20 20 20 20	2 46 3 60 5 56 4 48	68 83 81 72 45 42 40 92	P P P P P P P P
GRAND TOTAL	L = 1179/1500 , RESULT:FIRST C	LASS WITH DI	STINC'	TION									
B1200532				SANGITA	 A	• • • • • •	,7152		 53267	• • • • • •		• • • • •	
404181 404182 404183 404184A 404185B 404186 404187 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I									40 1 40 1 40 1 40 0 20 20 20 20 40 20	0 31 3 15 0 39 9 14	41 49 36 38 34 36 90 42	P F P F P P P P P
GRAND TOTAL	=/1500 , RESULT:FAILS					F	RESIILT RES	SERVED FOR BACKLOGS					
B1200532	268 THOMBRE UTKARSHA SANJAY			LEENA			,7163	::::::::::::::::::::::::::::::::::::::	 53268				
404181 404182 404183 404184A 404185C 404186 404186 404187 404187	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I = 1125/1500 , RESULT:FIRST C	PP 100 40 TW 50 20 OR 50 20 PR 50 20 OR 50 20	19 29 25 20 19	49 36 42 54 48						40 2 40 2 40 1 40 2 20 20 20 20 20 20 20	4 48 8 54 7 54 9 46	72 82 71 75 39 37 42 38 86 40	P P P P P P P P

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

BRANCH DATE :	04 JUL 2018 CENTRE: PUR	ICS &TELEC	JOMU UTE	OF CC	TON) MPUTEF	R TEC	HNOLOG	Y, PUNE.	P.	AGE NO. 8	39					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND:	IDAT	E, M	IOTHER	, PER	MANENT	REG. NO.	, PREVIOUS SEAT NO.,	COLLEGE	E, SEAT	NO.				
OTHER LI	NES: HEAD OF PASSING, MAX. MAX.	ARKS, MIN	.PAS	S MAR	RKS, IN	NT. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED), P/F:I	PASS/FAIL	, C	:PREVI	OUS C	'ARRY	OVER
B1200532	69 THORAT RUGVED DEVENDRA			s	UREKHA	A		,7152	7670M ,PICT	,B12005	 53269					
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	11	41	52	P C	404189	MOBILE COMMUNICATION		PP 100	40	15	28	43	P
404182	COMPUTER NETWORKS	PP 100	40	17	29	46	P D G	404190	BROADBAND COMMUNICATI	ON SYS.	PP 100	40	25	31	56	P
404183 404184a	MICROWAVE ENGINEERING DIGITAL IMAGE DEOCESSING	PP 100	40 40	15 11	∠8 35	43 46	P C	404191D 404192D	MIDELESS METWORKS		PP 100	40 40	12 17	∠8 28	40 45	P D
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	10	42	52	P	4041920	LAB PRACTICE III (MC &	BCS)	TW 50	20	1 /	20	30	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			42	РC	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			39	РC	404194	LAB PRAC. IV(ELECTIVE	C III)	TW 50	20			28	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			31	РC	404194	LAB PRAC. IV(ELECTIVE	E III)	PR 50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			32	P C	404195	PROJECT PHASE II		TW 100	40			85	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			40	ЪС	404195	PROJECT PHASE II		PR 50	20			40	Ь
CDAND HOHAT	- 064/1500 PROTESTITATION OF	TOOME OF A	a a													
	RESULT RESERVED FOR BACKLOGS. B120053270 TIWARI MITHUN RAMESHCHANDRA SANTOSH ,71527672H ,PICT ,B120053270															
В1200532																
404181	SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 13 42 55 P C 404189 MOBILE COMMUNICATION PP 100 40 24 39 63 P 404182 COMPUTER NETWORKS PP 100 40 27 31 58 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 27 42 69 P 404183 MICROWAVE ENGINEERING PP 100 40 21 40 61 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 24 35 59 P 404184A DIGITAL IMAGE PROCESSING PP 100 40 17 52 69 P C 404192D WIRELESS NETWORKS PP 100 40 28 43 71 P 404185C PLCS AND AUTOMATION PP 100 40 14 39 53 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 42 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 39 P C 404193 LAB PRACTICE III(MC & BCS) OR 50 20 39 P 404186 LAB PRACTICE I (CN & MWE) OR 50 20 41 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 36 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 41 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 40 P 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 42 P 404188 PROJECT PHASE I TW 100 40 93 P P 404188 PROJECT PHASE I OR 50 20 42 P P 50 20 42 P 404188 PROJECT PHASE I PR 50 20 42 P 404188 PROJECT PHASE I PR 50 20 42 P 404188 PROJECT PHASE II PR 50 20 42 P 404188 PROJECT PHASE II PR 50 20 42 P 404188 PROJECT PHASE I PR 50 20 42 P 404188 PROJECT PHASE I PR 50 20 42 P 404188 PROJECT PHASE I PR 50 20 42 P 404188 PROJECT PHASE II PR 50															
404182	COMPUTER NETWORKS	PP 100	40	27	31	58	P C	404190	BROADBAND COMMUNICATI	ON SYS.	PP 100	40	27	42	69	P
404183	MICROWAVE ENGINEERING	PP 100	40	21 17	40	6 T	P C	404191C	AUDIO VIDEO ENGINEERI	LNG	PP 100	40	24	35 42	59 71	Р
404164A 404185C	DIGITAL IMAGE PROCESSING DI.CS AND AHTOMATION	PP 100 PP 100	40	14	39	53	PC	4041920	TAR DRACTICE TIT(MC &	RCS)	TW 50	20	∠0	43	42	P D
404186	LAB PRACTICE I (CN & MWE)	TW 50	20		3,7	39	PC	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			41	РC	404194	LAB PRAC. IV(ELECTIVE	III)	TW 50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			41	РC	404194	LAB PRAC. IV ELECTIVE	C III)	PR 50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			40	РC	404195	PROJECT PHASE II		TW 100	40			93	P
404188	PROJECT PHASE I	OR 50	20			45	РC	404195	PROJECT PHASE II		PR 50	20			42	P
GRAND TOTAL	= 1056/1500 , RESULT:FIRST CI	LASS WITH	DIS	TINCT	ION											
B1200532	71 UDIT MENDIRATTA			R	EENU N	MENDI	RATTA	,7152	7677J ,PICT	,B12005	 53271	• • • •			• • • •	
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	07	34	41	P	404189	MOBILE COMMUNICATION		PP 100	40	20	37	57	P
404182	COMPUTER NETWORKS	PP 100	40	15	41	56	РC	404190	BROADBAND COMMUNICATI	ON SYS.	PP 100	40	25	39	64	P
404183	MICROWAVE ENGINEERING	PP 100	40	16	45	61	РC	404191C	AUDIO VIDEO ENGINEERI	NG	PP 100	40	18	37	55	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	18	34	52	P C	404192D	WIRELESS NETWORKS		PP 100	40	21	36	57	P
404185C	PLCS AND AUTOMATION	DD 100	40	18	34	52	P C	404193	LAB PRACTICE III (MC &	BCS)	J.M 20	20			35	Ъ
404186	IAB PRACTICE I (CN & MWE)	OR 50	20			30 41	PC	404193	TAB PRACTICE III (MC &	E BCS)	UR 50	20			39 34	P D
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			28	PC	404194	LAB PRAC. IV(ELECTIVE	III)	PR 50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			30	РĈ	404195	PROJECT PHASE II	- /	TW 100	40			96	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			48	P C	404195	PROJECT PHASE II		PR 50	20			47	P
	= 970/1500 , RESULT:FIRST CLA															

GRAND TOTAL = 970/1500 , RESULT:FIRST CLASS

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)
DATE: 04 JUL 2018 CENTRE: PINE INSTITUTE OF COMPUTER TECHNOLOGY, PINE.

BRANCH DATE :	H:71-B.E. (2012 PAT.)(ELECTRON : 04 JUL 2018 CENTRE: PU	ICS &TELE NE INSTIT	COMU UTE	NICAT OF CO	CION) OMPUTER	R TEC	'HNOLOG	Y, PUNE.	PAGE NO.	90					
NOTE: FI	IRST LINE : SEAT NO., NAME OF	THE CAND	IDAT	E, N	MOTHER,	PER	MANENT	REG. NO.	, PREVIOUS SEAT NO., COLLEG	E, SEAT	NO.				
	INES: HEAD OF PASSING, MAX. M														
	272 UTKARSH				SARITA			,7152	7680J ,PICT ,B1200	53272	• • • •	• • • • •			
	SEM.:1								SEM.:2						
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100	40	08	39	47	РC	404189	MOBILE COMMUNICATION	PP 100	40	17	41	58	P
404182	COMPUTER NETWORKS	PP 100	40	20	35	55	PC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	23	48	71	P
404183 404194D	MICKOWAVE ENGINEERING	PP 100	40	∠3 16	46	69	PC	4041910	AUDIO VIDEO ENGINEERING	PP 100	40	7.4 T.0	3 b 4 o	5Z	P
4041846	PI.CS AND AUTOMATION	PP 100 PP 100	40	12	39	51	PC	404192A 404193	IAB PRACTICE III (MC & RCS)	TW 50	20	24	40	38	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20		3,7	38	PC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			45	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			35	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			31	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			35	P C	404195	PROJECT PHASE II	TW 100	40			93	P
404188	PROJECT PHASE I	OR 50	20			38	РC	404195	PROJECT PHASE II	PR 50	20			45	Р
GRAND TOTAL	; = 1010/1500	LASS WITH	DTS	TINC	rton										
B1200532	273 VALECHA SAKSHI SATISH				RCHANZ			.7152	7683C PTCT B1200			• • • • •			
	B120053273 VALECHA SAKSHI SATISH ARCHANA ,71527683C ,PICT ,B120053273 SEM.:1														
404181	404181 VLSI DESIGN & TECHNOLOGY PP 100 40 13 35 48 P C 404189 MOBILE COMMUNICATION PP 100 40 22 36 58 P 404182 COMPUTER NETWORKS PP 100 40 18 42 60 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 24 42 66 P														
404182	COMPUTER NETWORKS	PP 100	40	T8	42	60	P C	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	24	42	66	Ъ
404183	MICKOWAVE ENGINEERING	PP 100	40	∠∠ 21	45 53	7/	P C	404191D	SOFT COMPUTING WIRELESS NETWORKS	PP 100	40	20	30 21	58 51	P D
404185D	ARTIFICIAL INTELLIGENCE	PP 100	40	20	45	65	PC	404193	LAB PRACTICE III (MC & BCS)	TW 50	20	20	21	43	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			34	РC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			37	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			39	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			40	РC	404195	PROJECT PHASE II	TW 100	40			92	P
404188	PROJECT PHASE I	OR 50	20			41	РC	404195	LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50	20			44	P
GRAND TOTAL	L = 1031/1500 , RESULT:FIRST C	LASS WITH	DIS	TINC	CION										
B1200532				I	PUNJABA		• • • • • •	,7163	3667H ,PICT ,B1200	 53274	• • • •	• • • • •			• • • • •
	SEM.:1								SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING	PP 100	40	14	38	52	РC	404189	MOBILE COMMUNICATION	PP 100	40	14	42	56	P
404182	COMPUTER NETWORKS	PP 100	40	25	48	73	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	26		79	
404183	MICROWAVE ENGINEERING	PP 100	40	22	51	73	РC	404191C	AUDIO VIDEO ENGINEERING WIRELESS NETWORKS	PP 100	40	14	44		_
404184A	DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN	PP 100	40	16	50	66	РC	404192D	WIRELESS NETWORKS	PP 100	40	20	34	54	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	19	48	67	PC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			38	P
404186 404186	LAB PRACTICE I (CN & MWE)	UB EU	20			43	ь с ь с	404193 404194	LAB PRACTICE III(MC & BCS)	OR 50	20			44 30	P P
404186	TAB PRACITOR I (CN & MME)	UK 50	20			33 34	P C	404194	IAB PRAC. IV(ELECTIVE III)	DB 20	⊿∪ 2∩			3 Q	P P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			35	PC	404195	PROJECT PHASE II	TW 100	40			95	P
404188	LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			35	РĈ	404195	LAB PRACTICE III(MC & BCS) LAB PRACTICE III(MC & BCS) LAB PRAC. IV(ELECTIVE III) LAB PRAC. IV(ELECTIVE III) PROJECT PHASE II PROJECT PHASE II	PR 50	20			42	P
	L = 1054/1500 , RESULT:FIRST C														

GRAND TOTAL = 1054/1500 , RESULTIFIEST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PU	NE INSTITU	UTE	OF COMPUTE	R TEC	CHNOLOG	BY, PUNE.	PAGE NO.	91					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	 IDAT	 E, MOTHER	PEI	 RMANENT	REG. NO.	, PREVIOUS SEAT NO., COLLEG	E. SEAT	NO.	• • • • • •	• • • • •		• • • • •
OTHER LI	NES: HEAD OF PASSING, MAX. M	ARKS, MIN	.PAS	S MARKS, I	NT. I	MARKS,	TH. MARKS	. TOTAL MARKS OBTAINED, P/F:	PASS/FAIL	. C	PREVI	OUS C	ARRY	OVER
B1200532	75 VANGARI MAMTA GOVIND			NAGMAN	II		,7163	3668F ,PICT ,B1200	53275					• • • • •
	SEM.:1							SEM.:2						
404181	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRACTICE I (VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	PP 100	40	16 42	58	P C	404189	MOBILE COMMUNICATION	PP 100	40	23	50	73	P
404182	COMPUTER NETWORKS	PP 100	40	29 37	66 61	PC	404190 404101C	BROADBAND COMMUNICATION SYS.	PP 100	40	24 17	6 L	85 60	Ъ
404183 404184B	EMBEDDED SYSTEMS & RTOS	PP 100 PP 100	40	21 57	78	PC	404191C	WIRELESS NETWORKS	PP 100	40	28	49	77	P
404185C	PLCS AND AUTOMATION	PP 100	40	23 50	73	РC	404193	LAB PRACTICE III(MC & BCS)	TW 50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20		38	РC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20		40	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			30	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20		3.7	PC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			38	P
404187 404188	DROJECT DHASE T	OR 50	20		33	PC	404195 404195	PROJECT PHASE II	TW 100	20			79 37	P D
101100	TROOLET THISE T	010 30	20		30	1 0	101173	TROOLET TIMES II	110 30	20			5,	-
GRAND TOTAL	. = 1092/1500 , RESULT:FIRST C.	LASS WITH	DIS	T.TNC.T.TON										
D1000F30	276 VARMA SHRUTI SANJAY	• • • • • • • • •					7150	7 DIGE DIGE		• • • •	• • • • •		• • • •	
B1200532	276 VARMA SHRUII SANJAY			SANGEE	ıΙΑ		,/152	7685K ,PICI ,BI200	532/6					
	SEM.:1							SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	14 30	44	РC	404189	MOBILE COMMUNICATION	PP 100	40	20	47	67	P
404182	COMPUTER NETWORKS	PP 100	40	20 42	62	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	24	54	78	P
404183	MICROWAVE ENGINEERING	PP 100	40	26 49	75	PC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	24	47	71	P
404184A 404185B	DIGITAL IMAGE PROCESSING FIRCTRONIC DRODUCT DESIGN	PP 100	40 40	21 50	/ 1 6 7	P C	404192A 404193	IAR DRACTICE III (MC & RCS)	TW 50	20	21	56	/ / 4 ∩	P D
404186	LAB PRACTICE I (CN & MWE)	TW 50	20	22 43	38	PC	404193	LAB PRACTICE III(MC & BCS)	OR 50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20		41	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20		41	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20		32	ΡC	404195	PROJECT PHASE II	TW 100	40			93	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20		40	ЬC	404195	PROJECT PHASE II	PR 50	20			45	Ь
GRAND TOTAL	= 1099/1500 , RESULT:FIRST C	LASS WITH	DIS	TINCTION										
B1200532	277 VASAGADE SUMEDHA SUNIL			SHWETA	٠		,7163	3669D ,PICT ,B1200	53277	• • • •			• • • •	• • • • •
	SEM.:1							SEM.:2						
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	16 34	50	РC	404189	MOBILE COMMUNICATION	PP 100	40	25	55	80	P
404182	COMPUTER NETWORKS	PP 100	40	27 39	66	РC	404190	BROADBAND COMMUNICATION SYS.	PP 100	40	24	59	83	P
404183	MICROWAVE ENGINEERING	PP 100	40	22 47	69	ΡC	404191C	AUDIO VIDEO ENGINEERING	PP 100	40	18	49	67	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	17 52	69	P C	404192D	WIRELESS NETWORKS	DD 100	40	27	44	./T	Ъ
404186	LAB PRACTICE T (CN & MWF)	TW 50	20	19 4/	44	PC	404193	LAB PRACTICE III (MC & BCS)	1W 5U	20			39 43	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20		42	PC	404194	LAB PRAC. IV(ELECTIVE III)	TW 50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20		41^{-}	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR 50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20		39	РC	404195	PROJECT PHASE II	TW 100	40			95	P
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20		41	РC	404195	PROJECT PHASE II	PR 50	20			45	P
	- 1132/1500 PECHT T. ETPCT C													

GRAND TOTAL = 1132/1500 , RESULT:FIRST CLASS WITH DISTINCTION

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PUI	ICS &TELE NE INSTIT	UTE	OF C	TION) OMPUTEF	R TEC	HNOLO	GY, PUNE.		PAGE NO.	92					
NOTE: FI	04 JUL 2018 CENTRE: PUI	THE CAND	 TATI	E	MOTHER.	PER	MANEN	 Г REG. NO.		COLLEG	 E. SEAT	·····	• • • • •	• • • • •		• • • • •
OTHER LI	NES: HEAD OF PASSING, MAX. MA	ARKS, MIN	.PAS	S MA	RKS, IN	JT. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAI	NED, P/F:	PASS/FAII	, c	:PREV	IOUS (CARRY	OVER
B1200532	78 VELANKAR SIDDHI SANJAY			• • • •	SANDHYA	Α	• • • • •	,7152	7687F ,PICT	,B1200	 53278		• • • • •			• • • • •
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	15	30	45	РC	404189	MOBILE COMMUNICATI	ON	PP 100	40	24	50	74	P
404182	COMPUTER NETWORKS	PP 100	40	24	43	67	РC	404190	BROADBAND COMMUNIC	ATION SYS.	PP 100	40	25	54	79	P
404183	MICROWAVE ENGINEERING	PP 100	40	24	44	68	РC	404191C	AUDIO VIDEO ENGINE	ERING	PP 100	40	13	39	52	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	19	53	72	РC	404192A	BIOMEDICAL SIGNAL	PROCESSING	PP 100	40	25	54	79	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	21	40	61	PC	404193	LAB PRACTICE III(M	C & BCS)	TW 50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	.I.M 20	20			34	PC	404193	LAB PRACTICE III(M	C & BCS)	OR 50	20			44	Ъ
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			42	PC	404194	LAB PRAC. IV(ELECT	TAR TIT)	J.M 20	20			42	Ъ
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			38	P C	404194	LAB PRAC. IV(ELECT	TAR TIT)	PR 50	20			41	P
404187	DROTECT DUNCE T	PR 50	20			42	P C	404195	PROJECT PHASE II		IM IOO	20			92	P
404100	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			42	PC	404193	PROUECT PHASE II		PK 50	20			44	P
GRAND TOTAL	= 1097/1500 , RESULT:FIRST C	LASS WITH	DIS	TINC'	TION											
B120053279 WADIKAR PINAK SUHAS SMITA ,71527693L ,PICT ,B120053279																
B120053279 WADIKAR PINAK SUHAS SMITA ,71527693L ,PICT ,B120053279																
404181 VLSI DESIGN & TECHNOLOGY PP 100 40 16 36 52 P C 404189 MOBILE COMMUNICATION PP 100 40 26 47 73 P																
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	16	36	52	PC	404189							73 78	_
404182	MICDONAVE ENGINEEDING	PP 100	40	∠o 23	44 E0	70	PC	404190	BROADBAND COMMUNIC	AIION SYS.	PP 100	40	27		78 72	
404183 404104D	MICKOWAVE ENGINEERING	PP 100	40	∠3 21	50 60	/ 3 0 1	P C	4041910	SOFT COMPUTING WIRELESS NETWORKS		PP 100	40	20		80	
4041046	APTIFICIAL INTFLLICENCE	DD 100	40	10	54	73	P C	4041920	TAR DRACTICE TIT/M	C & BCG)	TW 50	20	20	34	43	D
4041055	I.AR PRACTICE I (CN & MWE)	TW 50	20	17	31	44	РC	404193	IAB PRACTICE III(M	C & BCS)	OR 50	20			45	D
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			42	PC	404194	LAB PRAC TV(ELECT	TVE TIT)	TW 50	20			35	P
404187	LAB PRAC. II(VLSI & ELEC. I)	TW 50	20			42	PC	404194	LAB PRAC. IV(ELECT	TVE TIT)	PR 50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			42	PC	404195	PROJECT PHASE II		TW 100	40			97	P
404188	PROJECT PHASE I	OR 50	20			40	РC	404195	LAB PRACTICE III(M LAB PRACTICE III(M LAB PRAC. IV(ELECT LAB PRAC. IV(ELECT PROJECT PHASE II PROJECT PHASE II		PR 50	20			48	P
GRAND TOTAL	= 1173/1500 . RESULT:FIRST C	LASS WITH	DIS	TINC'	TION											
B1200532	80 WAGHMARE ASHLESHA NAMDEV			•	VINAYA			,7152	7696E ,PICT	,B1200	 53280		• • • • •			
	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING ELECTRONIC PRODUCT DESIGN LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	12	34	46	РC	404189	MOBILE COMMUNICATI	ON	PP 100	40	23	44	67	P
404182	COMPUTER NETWORKS	PP 100	40	13	38	51	РC	404190	BROADBAND COMMUNIC			40	26		77	_
404183	MICROWAVE ENGINEERING	PP 100	40	20	48	68	РC	404191C	AUDIO VIDEO ENGINE	ERING	PP 100				60	
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	23	56	79	РC	404192A	BIOMEDICAL SIGNAL	PROCESSING	PP 100	40	23	48	71	P
404185B	ELECTRONIC PRODUCT DESIGN	PP 100	40	18	41	59	РC	404193	LAB PRACTICE III(M	C & BCS)	TW 50	20			45	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			32	РC	404193	LAB PRACTICE III(M	C & BCS)	OR 50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			39	P C	404194	LAB PRAC. IV(ELECT	IVE III)	TW 50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			41	РC	404194	LAB PRAC. IV(ELECT	IVE III)	PR 50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			39	P C	404195	PROJECT PHASE II		TW 100	40			95	P
404188	PROJECT PHASE I	OR 50	20			32	ЬС	404195	LAB PRACTICE III(M LAB PRACTICE III(M LAB PRAC. IV(ELECT LAB PRAC. IV(ELECT PROJECT PHASE II PROJECT PHASE II		PR 50	20			44	Ъ
GRAND TOTAL	= 1065 + 10/1500 , RESULT:FIR	RST CLASS	riw 3	H DI	STINCTI	ON	[& (0.163]								

PAGE NO. 93

SAVITRIBAI PHULE PUNE UNIVERSITY, B.E.(2012 COURSE) EXAMINATION, MAY 2018 BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04 JUL 2018 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

NOTE: PIRST LINE: SEAT NO. NAME OF THE CANDIDATE. WOTHER, PERNAMENT RSG. NO. PREVIOUS SEAT NO. COLLEGE SEAT NO. COLLEGE SEAT NO. TOTAL MARKS OFFINIARIES INTO ON MAX. MARKS, THE MARKS, THE MARKS, THE MARKS, TOTAL MARKS OFFINIARIES, DISCUSSIVE SEAT NO. COLLEGE SEAT NO. COLLEGE SEAT NO. COLLEGE SEAT NO. COLLEGE SEAT NO. TOTAL MARKS OFFINIARIES NOTE: A MAX. MARKS, THE MARKS,	DAIE · U4 C	UUL 2016 CENTRE: PUN									PAGE NO.	9.3					
## ALIGNOMER SAKSHI RAJESH	NOTE: FIRST DOTHER LINES:	LINE : SEAT NO., NAME OF THEAD OF PASSING, MAX. MAX.	THE CANDI	DATI	E, MC S MARK	THER,	PER	MANENT ARKS,	REG. NO. TH. MARKS	, PREVIOUS SEAT NO , TOTAL MARKS OBTAI	., COLLEGI	E, SEAT PASS/FAII	NO.	:PREV	COUS C	ARRY	OVER
## 404181 VLGI DESIGNS TECHNOLOGY DP 100 40 06 35 41 D C 404189 MOBILE COMMUNICATION DP 100 40 18 45 64 D 404182 COMPUTER NETWORKS DP 100 40 08 43 51 P C 4041910 BROADBAND COMMUNICATION SYS. DP 100 40 12 4 48 72 P 404183 MICROWANE RENINIBERING DP 100 40 17 43 60 P C 4041910 BROADBAND COMMUNICATION SYS. DP 100 40 09 45 54 P 404184 DIGITAL IMAGE PROCESSING DP 100 40 13 52 65 P C 4041910 BROADBAND COMMUNICATION SYS. DP 100 40 16 37 53 P 404185 DECS AND AUTOMATION DP 100 40 12 49 61 P C 4041910 BROADBAND COMMUNICATION SYS. DP 100 40 16 37 53 P 404185 LAB PRACTICE I (IV. MICRO DP 100 40 12 40 61 P C 404191 BROADBAND COMMUNICATION DP 100 40 16 37 53 P 404185 LAB PRACTICE I (IV. MICRO DP 100 40 12 40 61 P C 404191 BROADBAND COMMUNICATION DP 100 40 16 37 53 P 404185 LAB PRACTICE I (IV. MICRO DP 100 40 18 40 41 P C 404191 BROADBAND COMMUNICATION DR 50 20 37 P C 404191 BROADBAND COMMUNICATION DR 50 20 37 P C 404192 BROADBAND COMMUNICATION DR 50 20 37 P C 404192 BROADBAND COMMUNICATION DR 50 20 37 P C 404192 BROADBAND COMMUNICATION DR 50 20 37 P C 404193 BROADBAND COMMUNICATION DR 50 20 37 P C 404193 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P C 404195 BROADBAND COMMUNICATION DR 50 20 37 P	D1200E2201	WATCAONVAD CAVCUT DATECU			 M7	 MTTDT		• • • • •	7150	7600M DTCT	P12001			• • • • •		• • • •	• • • • •
SEM. 1																	
SEM. 1	404181 VLS: 404182 COMI 404183 MICI 404184A DIG: 404185C PLCS 404186 LAB 404187 LAB 404187 LAB 404187 LAB 404188 PROS	SEM.:1 SI DESIGN & TECHNOLOGY IPUTER NETWORKS ROWAVE ENGINEERING SITAL IMAGE PROCESSING S AND AUTOMATION B PRACTICE I (CN & MWE) B PRACTICE I (CN & MWE) B PRAC. II(VLSI & ELEC. I) DJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 OR 50	40 40 40 40 40 20 20 20 20 20	06 08 17 13 12	35 43 43 52 49	41 51 60 65 61 38 35 34 37 47	P P P P C C C C C C P P P P P P P P P P	404189 404190 404191D 404192D 404193 404193 404194 404194 404195 404195	SEM.:2 MOBILE COMMUNICATI BROADBAND COMMUNIC. SOFT COMPUTING WIRELESS NETWORKS LAB PRACTICE III(M. LAB PRACTICE III(M. LAB PRAC. IV(ELECT LAB PRAC. IV(ELECT PROJECT PHASE II PROJECT PHASE II	ON ATION SYS. C & BCS) C & BCS) IVE III) IVE III)	PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 TW 100 PR 50	40 40 40 20 20 20 20 40 20	19 24 09 16	45 48 45 37	64 72 54 53 39 37 34 32 96 47	P P P P P P P
SEM.:1 SEM.:1 SEM.:2 SEM.:1 SEM.:2 S	CD A N D T C T A T = 0.0	107/1500 DECITEDED OF A	CC WITTH D	יד כידי	TNOTTO	TAT											
SEM.:1 SEM.:2 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 15 28 43 P C 404189 MOBILE COMMUNICATION PP 100 40 29 47 76 P 404182 COMPUTER NETWORKS PP 100 40 18 43 61 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 24 35 59 P 404184 MIGROWAVE ENGINEERING PP 100 40 20 53 73 P C 4041912 MIRELESS NETWORKS PP 100 40 18 44 62 P 404184 EMBEDDED SYSTEMS & RTOS PP 100 40 18 52 70 P C 4041912 MIRELESS NETWORKS PP 100 40 18 44 62 P 404185 LAB PRACTICE II (CN & MWE) TW 50 20 42 P C 404193 LAB PRACTICE II (MC & BCS) OR 50 20 42 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 34 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 41 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 40 P C 404188 PROJECT PHASE II TW 100 40 85 P A04188 PROJECT PHASE II TW 100 40 85 P A04188 PROJECT PHASE II TW 100 40 85 P A04188 PROJECT PHASE II TW 100 40 85 P A04189 MIRCHESS NETWORKS PP 100 40 22 43 65 P A04188 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 40 P C 404189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P C 404195 PROJECT PHASE II TW 100 40 22 43 65 P A04188 PROJECT PHASE II TW 100 40 85 P A04188 PROJECT PHASE II TW 100 40 85 P A04188 PROJECT PHASE II TW 100 40 22 43 65 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P C 404195 PROJECT PHASE II TW 100 40 22 43 65 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P A04189 MIRCOWAVE ENGINEERING PP 100 40 18 32 50 P																	
GRAND TOTAL = 1038/1500 , RESULT:FIRST CLASS WITH DISTINCTION B120053283 WIDHATE RAHUL ARUNA ,71527705H ,PICT ,B120053283 SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 07 36 43 P 404189 MOBILE COMMUNICATION PP 100 40 22 43 65 P 404182 COMPUTER NETWORKS PP 100 40 18 32 50 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 28 36 64 P 404183 MICROWAVE ENGINEERING PP 100 40 22 42 64 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 18 32 50 P 404184A DIGITAL IMAGE PROCESSING PP 100 40 25 50 75 P C 404192D WIRELESS NETWORKS PP 100 40 20 35 55 P 404186 LAB PRACTICE I (CN & MME) TW 50 20 33 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 34 P 404186 LAB PRACTICE I (CN & MME) OR 50 20 37 P C 404193 LAB PRACTICE III(MC & BCS) OR 50 20 30 P 404187 LAB PRACT. II(VLSI & ELEC. I) TW 50 20 28 P C 404193 LAB PRACT. IV(ELECTIVE III) TW 50 20 30 P 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 36 P C 404195 PROJECT PHASE II TW 100 40 92 P 404188 PROJECT PHASE I OR 50 20 42 P C 404195 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P P C 404188 PROJECT PHASE II PR 50 20 42 P P C 404188 PROJECT PHASE II PR 50 20 42 P P P CANNO TOTAL PR 505/1500 PR 505/1500 P C 404189 PROJECT PHASE II PR 50 20 42 P P P CANNO TOTAL PR 505/1500 PR 505/1500 P C 404189 PROJECT PHASE II PR 50 20 42 P P P CANNO TOTAL PR 505/1500 P CANNO TOTAL P	B120053282	WAKODKAR VARAD SANJEEV			SU	NITA			,7152	7701E ,PICT	,B1200	53282					
SEM.:1 SEM.:2 S	404181 VLS: 404182 COMI 404183 MICI 404184B EMBI 404185C PLC: 404186 LAB 404186 LAB 404187 LAB 404187 LAB 404187 PROS	SEM.:1 SI DESIGN & TECHNOLOGY IPUTER NETWORKS ROWAVE ENGINEERING SEDDED SYSTEMS & RTOS S AND AUTOMATION B PRACTICE I (CN & MWE) B PRACTICE I (CN & MWE) S PRAC. II(VLSI & ELEC. I) DIECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 TW 50 OR 50 TW 50 PR 50 OR 50	40 40 40 40 40 20 20 20 20 20	15 18 20 18 10	28 43 53 52 39											P P P P P P P P P
SEM.:1 SEM.:2 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 07 36 43 P 404189 MOBILE COMMUNICATION PP 100 40 22 43 65 P 404182 COMPUTER NETWORKS PP 100 40 18 32 50 PC 404190 BROADBAND COMMUNICATION SYS. PP 100 40 28 36 64 P 404183 MICROWAVE ENGINEERING PP 100 40 22 42 64 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 18 32 50 P 404184A DIGITAL IMAGE PROCESSING PP 100 40 25 50 75 P C 404192D WIRELESS NETWORKS PP 100 40 20 35 55 P 404185C PLCS AND AUTOMATION PP 100 40 16 36 52 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 34 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 33 P C 404194 LAB PRACTICE III(MC & BCS) OR 50 20 39 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 36 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 28 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 36 P 404187 LAB PRAC. II(VLSI & ELEC. I) PR 50 20 40 P C 404195 PROJECT PHASE II TW 100 40 92 P 404188 PROJECT PHASE I OR 50 20 42 P P C 404195 PROJECT PHASE II PR 50 20 42 P P C 404188 PROJECT PHASE I PR 50 20 42 P P C 404195 PROJECT PHASE II PR 50 20 42 P P C 404188 PROJECT PHASE II PR 50 20 42 P P C 404195 PROJECT PHASE II PR 50 20 42 P P C 404188 PROJECT PHASE II PR 50 20 42 P P C 404195 PROJECT PHASE II PR 50 20 42 P P C 404198 PROJECT PHASE II PR 50 20 42 P P P TOTAL = 965/1500 , RESULT:FIRST CLASS	GRAND TOTAL = 10	.038/1500 , RESULT:FIRST CL	ASS WITH	DIST	FINCTI	ON											
SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 07 36 43 P 404189 MOBILE COMMUNICATION PP 100 40 22 43 65 P 404182 COMPUTER NETWORKS PP 100 40 18 32 50 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 28 36 64 P 404183 MICROWAVE ENGINEERING PP 100 40 22 42 64 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 18 32 50 P 404184A DIGITAL IMAGE PROCESSING PP 100 40 25 50 75 P C 404192D WIRELESS NETWORKS PP 100 40 20 35 55 P 404185 PLCS AND AUTOMATION PP 100 40 16 36 52 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 34 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 33 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 39 P 404186 LAB PRACTICE I (CN & MWE) OR 50 20 37 P C 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 30 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 36 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 36 P 404188 PROJECT PHASE I TW 100 40 92 P 404188 PROJECT PHASE I OR 50 20 42 P C 404195 PROJECT PHASE II PR 50 20 42 P C 404188 PROJECT PHASE II PR 50 20 42 P P C 404188 PROJECT PHASE II PR 50 20 42 P P C 404188 PROJECT PHASE II PR 50					 A R	 IINA	• • • •	• • • • • •	7152	7705H PICT	B1200		• • • •	• • • • •		• • • •	• • • • •
	404181 VLS: 404182 COM 404183 MICI 404184A DIG: 404185C PLC: 404186 LAB 404186 LAB 404187 LAB 404187 LAB 404187 LAB	SEM.:1 SI DESIGN & TECHNOLOGY IPUTER NETWORKS ROWAVE ENGINEERING SITAL IMAGE PROCESSING S AND AUTOMATION S PRACTICE I (CN & MWE) S PRACTICE I (CN & MWE) S PRAC. II(VLSI & ELEC. I) S PRAC. II(VLSI & ELEC. I) DJECT PHASE I	PP 100 PP 100 PP 100 PP 100 PP 100 PP 50 OR 50 TW 50 PR 50 OR 50										40 40 40 20 20 20 20 20 20	22 28 18 20	43 36 32 35	65 64 50 55 34 39 30 36 92 42	P P P P P P P P
	GRAND IOIAL = 90	OS/ISOU , RESULTIFIEST CLA	υ 0					R	ESULT RES	ERVED FOR BACKLOGS.							

SAVITRIBAI PHULE PUNE UNIVERSITY, B.E.(2012 COURSE) EXAMINATION, MAY 2018 BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE :	04 JUL 2018 CENTRE: PUI	NE INSTIT	UTE	OF C	OMPUTER	TEC	CHNOLOG	SY, PUNE.	PA	GE NO. 9	94					
NOTE: FI	RST LINE : SEAT NO., NAME OF	THE CAND	IAGI	E,	MOTHER,	PER	RMANENT	reg. No.	, PREVIOUS SEAT NO.,	COLLEGE	E, SEAT	NO.				
OTHER LI	NES: HEAD OF PASSING, MAX. MAX. MAX. MAX.	ARKS, MIN	I.PAS	S MA	RKS, IN	T. M	IARKS,	TH. MARKS	, TOTAL MARKS OBTAINED), P/F:I	PASS/FAIL	, (PREV	IOUS (CARRY	OVER
B1200532	84 YENGE SAURAV NIVRUTTI				SUNITA		•••••	,7152	7711B ,PICT	,B12005	53284		• • • • •		••••	• • • • •
	SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP 100	40	13	32	45	РC	404189	MOBILE COMMUNICATION		PP 100	40	20		66	
404182	COMPUTER NETWORKS MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING	PP 100	40	19 10	32 31	51 40	P C	404190 404191	BROADBAND COMMUNICATI	ON SYS.	PP 100	40	24 17	36 31	60 45	P
404184A	DIGITAL IMAGE PROCESSING	PP 100	40	25	44	69	PC	404192D	WIRELESS NETWORKS	.IVG	PP 100	40	15	33	48	P
404185C	PLCS AND AUTOMATION	PP 100	40	14	28	42	РC	404193	LAB PRACTICE III(MC &	BCS)	TW 50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			38	P C	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			41	P
404186 404187	LAB PRACTICE I (CN & MWE)	OR 50	20			38 39	P C	404194	TAB PRAC. IV(ELECTIVE	:	TW 50	20 20			3∠ 38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			31	PC	404195	PROJECT PHASE II		TW 100	40			85	P
404188	MICROWAVE ENGINEERING DIGITAL IMAGE PROCESSING PLCS AND AUTOMATION LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & MWE) LAB PRAC. II(VLSI & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	OR 50	20			41	РC	404195	PROJECT PHASE II		PR 50	20			38	P
GRAND TOTAL	_ = 932/1500 , RESULT:FIRST CL	ASS														
									<u></u>							
B120053285 ZAMBARE ANKUR GOVIND SUSHMA ,71527714G ,PICT ,B120053285																
404181	404181 VLSI DESIGN & TECHNOLOGY PP 100 40 12 29 41 P C 404189 MOBILE COMMUNICATION PP 100 40 19 51 70 P 404182 COMPUTER NETWORKS PP 100 40 23 38 61 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 16 35 51 P															
404182 404183	404181 VLSI DESIGN & TECHNOLOGY PP 100 40 12 29 41 P C 404189 MOBILE COMMUNICATION PP 100 40 19 51 70 P 404182 COMPUTER NETWORKS PP 100 40 23 38 61 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 16 35 51 P 404183 MICROWAVE ENGINEERING PP 100 40 17 38 55 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 15 33 48 P															
404184B	SEM.:1 404181 VLSI DESIGN & TECHNOLOGY PP 100 40 12 29 41 P C 404189 MOBILE COMMUNICATION PP 100 40 19 51 70 P 404182 COMPUTER NETWORKS PP 100 40 23 38 61 P C 404190 BROADBAND COMMUNICATION SYS. PP 100 40 16 35 51 P 404183 MICROWAVE ENGINEERING PP 100 40 17 38 55 P C 404191C AUDIO VIDEO ENGINEERING PP 100 40 15 33 48 P 404184B EMBEDDED SYSTEMS & RTOS PP 100 40 16 51 67 P C 404192D WIRELESS NETWORKS PP 100 40 21 32 53 P 404185C PLCS AND AUTOMATION PP 100 40 15 37 52 P C 404193 LAB PRACTICE III(MC & BCS) TW 50 20 39 P 404186 LAB PRACTICE I (CN & MWE) TW 50 20 38 P C 404193 LAB PRACTICE III(MC & BCS) OR 50 20 42 P 404187 LAB PRAC. II(VLSI & ELEC. I) TW 50 20 41 P C 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 38 P 404188 PROJECT PHASE I OR 50 20 41 P C 404195 PROJECT PHASE II TW 100 40 94 P 404188 PROJECT PHASE I OR 50 20 41 P C 404195 PROJECT PHASE II PR 50 20 45 P															
404185C	PLCS AND AUTOMATION	PP 100	40	15	37	52	P C	404193	LAB PRACTICE III(MC &	BCS)	TW 50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	TW 50	20			38	P C	404193	LAB PRACTICE III(MC &	BCS)	OR 50	20			42	P
404186 404187	LAB PRACTICE I (CN & MWE)	OR 50	20			41 41	P C	404194	TAB PRAC. IV(ELECTIVE	:	TW 50	20 20			40 38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			35	P C	404195	PROJECT PHASE II		TW 100	40			94	P
404188	PROJECT PHASE I	OR 50	20			41	P C	404195	PROJECT PHASE II		PR 50	20			45	P
GRAND TOTAL	. = 992/1500 PESHITT: FIRST CL.	HTTW 224	רפדת	ידאכיד	TON											
B1200532	286 ZAWARE PRASAD ASHOK			• • • •	MADHIIR T				3670H .PTCT	B1200			• • • • •	• • • • •	• • • •	• • • • •
										,						
404181	SEM.:1	DD 100	40	12	28	40	рС	404189	SEM.:2 MOBILE COMMUNICATION		DD 100	40	15	46	61	D
404182	COMPUTER NETWORKS	PP 100	40	15	35	50	PC	404190	BROADBAND COMMUNICATI	ON SYS.	PP 100	40	2.7		58	
404183	MICROWAVE ENGINEERING	PP 100	40	20	47	67	РC	404191D	SOFT COMPUTING WIRELESS NETWORKS		PP 100	40	22	36	58	P
404184B	EMBEDDED SYSTEMS & RTOS	PP 100	40	12	50	62	P C	404192D	WIRELESS NETWORKS	DGG)	PP 100	40	23	40	63	P
404185D 404186	AKILFICIAL INTELLIGENCE LAR PRACTICE I (CN & MWF)	TM 20	40 20	Τ2	36	48 45	ъс ъс	404193 404193	LAB PRACTICE III (MC &	BCS)	OR 50	∠0 20			46 43	Þ
404186	LAB PRACTICE I (CN & MWE)	OR 50	20			39	P C	404194	LAB PRAC. IV(ELECTIVE	: III)	TW 50	20			45	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW 50	20			46	P C	404194	LAB PRAC. IV(ELECTIVE	III)	PR 50	20			35	P
404187 404188	LAB PRAC. II(VLSI &ELEC. I)	PR 50	20			37 42	P C	404195	LAB PRACTICE III(MC & LAB PRACTICE III(MC & LAB PRAC. IV(ELECTIVE LAB PRAC. IV(ELECTIVE PROJECT PHASE II PROJECT PHASE II		TW 100	40			92 42	P D
404188	SEM.:1 VLSI DESIGN & TECHNOLOGY COMPUTER NETWORKS MICROWAVE ENGINEERING EMBEDDED SYSTEMS & RTOS ARTIFICIAL INTELLIGENCE LAB PRACTICE I (CN & MWE) LAB PRACTICE I (CN & ELEC. I) LAB PRAC. II(VLSI & ELEC. I) PROJECT PHASE I	UK 50	∠∪			43	PC	404132	PROUBCI PHASE II		PK 50	۷۷			43	P
GRAND TOTAL	= 1021/1500 , RESULT:FIRST C	LASS WITH	DIS	TINC	TION											

BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)

DATE: 04	JUL 2018 CENTRE: PUN	E INSTIT	UTE O	F COMPUTER	R TECH	HNOLOGY	, PUNE.	PAGE NO. 9	5						
-	•							, PREVIOUS SEAT NO., COLLEGE, TOTAL MARKS OBTAINED, P/F:E	•	SEAT FAIL		······ :PREVI	OUS C	ARRY	OVER
B120053287	ABHISHEK USHEKWAR			PREETI	· • • • •	• • • • • •	,7140)687E ,PICT ,B12005	3287						
	SEM.:1							SEM.:2							
404181 VLS	I DESIGN & TECHNOLOGY	PP 100	40	12 32	44	PС	404189	MOBILE COMMUNICATION	PP	100	40	08	32\$	40	PС
404182 COM	IPUTER NETWORKS	PP 100	40	13 34	47	РC	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	10	38	48	P C
404183 MIC	ROWAVE ENGINEERING	PP 100	40	06 17		F	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	11	43	54	P C
404184A DIG	SITAL IMAGE PROCESSING	PP 100	40	12 46	58	РC	404192D	WIRELESS NETWORKS	PP	100	40	04	36	40	P C
404185C PLC	S AND AUTOMATION	PP 100	40	13 34	47	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			31	P C
404186 LAB	PRACTICE I (CN & MWE)	TW 50	20		36	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P C
404186 LAB	PRACTICE I (CN & MWE)	OR 50	20		24	РC	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			24	P C
404187 LAB	PRAC. II(VLSI &ELEC. I)	TW 50	20		25	РC	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			31	P C
404187 LAB	PRAC. II(VLSI &ELEC. I)	PR 50	20		32	P C	404195	PROJECT PHASE II	TW	100	40			88	P C
404188 PRO	JECT PHASE I	OR 50	20		36	P C	404195	PROJECT PHASE II	PR	50	20			42	P C

GRAND TOTAL = --/1500 , RESULT: FAILS [\$ 0.1]

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