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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053001   AAROHI VAIDYA                       SNEHAL              ,71527080L   ,PICT        ,B120053001

            SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100 40 17    37    54  P C    404189   MOBILE COMMUNICATION           PP 100 40 21    33    54  P
      404182   COMPUTER NETWORKS             PP 100 40 26    41    67  P C    404190   BROADBAND COMMUNICATION SYS.   PP 100 40 23    57    80  P
      404183   MICROWAVE ENGINEERING         PP 100 40 26    56    82  P C    404191C  AUDIO VIDEO ENGINEERING        PP 100 40 25    56    81  P
      404184B  EMBEDDED SYSTEMS & RTOS       PP 100 40 22    54    76  P C    404192D  WIRELESS NETWORKS              PP 100 40 27    51    78  P
      404185D  ARTIFICIAL INTELLIGENCE       PP 100 40 21    47    68  P C    404193   LAB PRACTICE III(MC & BCS)     TW  50 20             33  P
      404186   LAB PRACTICE I (CN & MWE)     TW  50 20             36  P C    404193   LAB PRACTICE III(MC & BCS)     OR  50 20             41  P
      404186   LAB PRACTICE I (CN & MWE)     OR  50 20             40  P C    404194   LAB PRAC. IV(ELECTIVE III)     TW  50 20             39  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50 20             36  P C    404194   LAB PRAC. IV(ELECTIVE III)     PR  50 20             39  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50 20             33  P C    404195   PROJECT PHASE II               TW 100 40             94  P
      404188   PROJECT PHASE I               OR  50 20             40  P C    404195   PROJECT PHASE II               PR  50 20             45  P

   GRAND TOTAL = 1116/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053002   AARYAN KUMAR                        ANJU                ,71400679D   ,PICT        ,B120053002

            SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100 40 AA    36    --  F      404189   MOBILE COMMUNICATION           PP 100 40 15    28    43  P
      404182   COMPUTER NETWORKS             PP 100 40 18    29    47  P      404190   BROADBAND COMMUNICATION SYS.   PP 100 40 12    33    45  P
      404183   MICROWAVE ENGINEERING         PP 100 40 05    19    --  F      404191C  AUDIO VIDEO ENGINEERING        PP 100 40 18    45    63  P
      404184A  DIGITAL IMAGE PROCESSING      PP 100 40 24    39    63  P C    404192D  WIRELESS NETWORKS              PP 100 40 22    43    65  P
      404185C  PLCS AND AUTOMATION           PP 100 40 06    38    44  P      404193   LAB PRACTICE III(MC & BCS)     TW  50 20             35  P
      404186   LAB PRACTICE I (CN & MWE)     TW  50 20             37  P C    404193   LAB PRACTICE III(MC & BCS)     OR  50 20             32  P
      404186   LAB PRACTICE I (CN & MWE)     OR  50 20             22  P C    404194   LAB PRAC. IV(ELECTIVE III)     TW  50 20             24  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50 20             30  P C    404194   LAB PRAC. IV(ELECTIVE III)     PR  50 20             36  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50 20             35  P C    404195   PROJECT PHASE II               TW 100 40             94  P
      404188   PROJECT PHASE I               OR  50 20             43  P C    404195   PROJECT PHASE II               PR  50 20             45  P

   GRAND TOTAL = --/1500 , RESULT:FAILS
                                                            RESULT RESERVED FOR BACKLOGS.
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      B120053003   ABHISHEK KUMAR SINGH                SHUKWANTI           ,71527083E   ,PICT        ,B120053003

            SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100 40 11    53    64  P      404189   MOBILE COMMUNICATION           PP 100 40 21    34    55  P
      404182   COMPUTER NETWORKS             PP 100 40 24    44    68  P C    404190   BROADBAND COMMUNICATION SYS.   PP 100 40 26    50    76  P
      404183   MICROWAVE ENGINEERING         PP 100 40 27    55    82  P C    404191C  AUDIO VIDEO ENGINEERING        PP 100 40 24    51    75  P
      404184A  DIGITAL IMAGE PROCESSING      PP 100 40 28    55    83  P C    404192D  WIRELESS NETWORKS              PP 100 40 28    56    84  P
      404185D  ARTIFICIAL INTELLIGENCE       PP 100 40 15    41    56  P C    404193   LAB PRACTICE III(MC & BCS)     TW  50 20             AA  F
      404186   LAB PRACTICE I (CN & MWE)     TW  50 20             20  P C    404193   LAB PRACTICE III(MC & BCS)     OR  50 20             AA  F
      404186   LAB PRACTICE I (CN & MWE)     OR  50 20             30  P      404194   LAB PRAC. IV(ELECTIVE III)     TW  50 20             AA  F
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50 20             21  P C    404194   LAB PRAC. IV(ELECTIVE III)     PR  50 20             AA  F
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50 20             32  P      404195   PROJECT PHASE II               TW 100 40             90  P
      404188   PROJECT PHASE I               OR  50 20             29  P C    404195   PROJECT PHASE II               PR  50 20             42  P

   GRAND TOTAL = --/1500 , RESULT:FAILS
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053004   AITISKHEM SYNGKON                        PROSSILA              ,71400700F   ,PICT        ,B120053004

              SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  12     40   52  P       404189   MOBILE COMMUNICATION         PP 100  40  12     29   41  P
     404182   COMPUTER NETWORKS            PP 100  40  15     38   53  P       404190   BROADBAND COMMUNICATION SYS. PP 100  40  15     35   50  P
     404183   MICROWAVE ENGINEERING        PP 100  40  02     39   41  P       404191C  AUDIO VIDEO ENGINEERING      PP 100  40  14     41   55  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  10     36   46  P       404192D  WIRELESS NETWORKS            PP 100  40  18     36   54  P
     404185C  PLCS AND AUTOMATION          PP 100  40  05     44   49  P       404193   LAB PRACTICE III(MC & BCS)   TW  50  20              21  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              32  P       404193   LAB PRACTICE III(MC & BCS)   OR  50  20             15# P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              22  P       404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              24  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              25  P       404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              30  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              22  P       404195   PROJECT PHASE II             TW 100  40              41  P
     404188   PROJECT PHASE I              OR  50  20              35  P C     404195   PROJECT PHASE II             PR  50  20              21  P

GRAND TOTAL = 729/1500 , RESULT:PASS CLASS   [# O.4]
                                                            RESULT RESERVED FOR BACKLOGS.
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     B120053005   AKSHAY SHRIKANT DESHMUKH                 SEEMA                 ,71527103C   ,PICT        ,B120053005

              SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  09     35   44  P C     404189   MOBILE COMMUNICATION         PP 100  40  15     32   47  P
     404182   COMPUTER NETWORKS            PP 100  40  25     41   66  P C     404190   BROADBAND COMMUNICATION SYS. PP 100  40  28     32   60  P
     404183   MICROWAVE ENGINEERING        PP 100  40  17     47   64  P C     404191C  AUDIO VIDEO ENGINEERING      PP 100  40  16     49   65  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  16     46   62  P C     404192D  WIRELESS NETWORKS            PP 100  40  24     49   73  P
     404185C  PLCS AND AUTOMATION          PP 100  40  08     45   53  P C     404193   LAB PRACTICE III(MC & BCS)   TW  50  20              36  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              31  P C     404193   LAB PRACTICE III(MC & BCS)   OR  50  20              22  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              28  P C     404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              36  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              40  P C     404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              36  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              36  P C     404195   PROJECT PHASE II             TW 100  40              95  P
     404188   PROJECT PHASE I              OR  50  20              30  P C     404195   PROJECT PHASE II             PR  50  20              45  P

GRAND TOTAL = 969/1500 , RESULT:FIRST CLASS
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     B120053006   AMALE ADYA RAJENDRA                      POURNIMA              ,71300769K   ,PICT        ,B120053006

              SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  11     34   45  P       404189   MOBILE COMMUNICATION         PP 100  40  19     AA   --  F
     404182   COMPUTER NETWORKS            PP 100  40  16     29   45  P C     404190   BROADBAND COMMUNICATION SYS. PP 100  40  17     AA   --  F
     404183   MICROWAVE ENGINEERING        PP 100  40  21     AA   --  F       404191D  SOFT COMPUTING               PP 100  40  15     AA   --  F
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  16     AA   --  F       404192A  BIOMEDICAL SIGNAL PROCESSING PP 100  40  24     34   58  P
     404185C  PLCS AND AUTOMATION          PP 100  40  15     AA   --  F       404193   LAB PRACTICE III(MC & BCS)   TW  50  20              28  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              27  P C     404193   LAB PRACTICE III(MC & BCS)   OR  50  20              26  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              32  P C     404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              34  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              26  P       404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              25  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              25  P       404195   PROJECT PHASE II             TW 100  40              70  P
     404188   PROJECT PHASE I              OR  50  20              36  P C     404195   PROJECT PHASE II             PR  50  20              35  P

GRAND TOTAL = --/1500 , RESULT:FAILS
                                                            RESULT RESERVED FOR BACKLOGS.
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  NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
  OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053007   ANDURKAR SHUBHAM VYANKATESH              VAISHNAVI            ,71527110F   ,PICT        ,B120053007

              SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  14      29   43  P C  404189   MOBILE COMMUNICATION          PP 100  40  07      28   35# P
      404182   COMPUTER NETWORKS            PP 100  40  23      44   67  P C  404190   BROADBAND COMMUNICATION SYS.  PP 100  40  23      28   51  P
      404183   MICROWAVE ENGINEERING        PP 100  40  20      38   58  P C  404191C  AUDIO VIDEO ENGINEERING       PP 100  40  21      46   67  P
      404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  15      39   54  P C  404192D  WIRELESS NETWORKS             PP 100  40  24      35   59  P
      404185C  PLCS AND AUTOMATION          PP 100  40  12      43   55  P C  404193   LAB PRACTICE III(MC & BCS)    TW  50  20                 35  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20               36  P C  404193   LAB PRACTICE III(MC & BCS)    OR  50  20                 30  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20               24  P C  404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                 33  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20               38  P C  404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                 39  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20               34  P C  404195   PROJECT PHASE II              TW 100  40                 94  P
      404188   PROJECT PHASE I              OR  50  20               44  P C  404195   PROJECT PHASE II              PR  50  20                 45  P

  GRAND TOTAL = 941/1500 , RESULT:FIRST CLASS   [# O.4]
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      B120053008   ANIKET ASHOK BHALERAO                    SUNITA              ,71527111D   ,PICT        ,B120053008

              SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  15      36   51  P C  404189   MOBILE COMMUNICATION          PP 100  40  22      41   63  P
      404182   COMPUTER NETWORKS            PP 100  40  27      47   74  P C  404190   BROADBAND COMMUNICATION SYS.  PP 100  40  25      36   61  P
      404183   MICROWAVE ENGINEERING        PP 100  40  23      50   73  P C  404191C  AUDIO VIDEO ENGINEERING       PP 100  40  25      49   74  P
      404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  23      49   72  P C  404192D  WIRELESS NETWORKS             PP 100  40  24      49   73  P
      404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  14      40   54  P C  404193   LAB PRACTICE III(MC & BCS)    TW  50  20                 45  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20               39  P C  404193   LAB PRACTICE III(MC & BCS)    OR  50  20                 43  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20               40  P C  404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                 41  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20               41  P C  404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                 36  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20               42  P C  404195   PROJECT PHASE II              TW 100  40                 95  P
      404188   PROJECT PHASE I              OR  50  20               45  P C  404195   PROJECT PHASE II              PR  50  20                 44  P

  GRAND TOTAL = 1106/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053009   ANUSHKA NARAYAN                          PUSHPA              ,71527118M   ,PICT        ,B120053009

              SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  13      32   45  P C  404189   MOBILE COMMUNICATION          PP 100  40  18      42   60  P
      404182   COMPUTER NETWORKS            PP 100  40  27      49   76  P C  404190   BROADBAND COMMUNICATION SYS.  PP 100  40  21      46   67  P
      404183   MICROWAVE ENGINEERING        PP 100  40  17      50   67  P C  404191C  AUDIO VIDEO ENGINEERING       PP 100  40  20      51   71  P
      404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  20      50   70  P C  404192D  WIRELESS NETWORKS             PP 100  40  29      46   75  P
      404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  24      31   55  P C  404193   LAB PRACTICE III(MC & BCS)    TW  50  20                 29  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20               35  P C  404193   LAB PRACTICE III(MC & BCS)    OR  50  20                 45  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20               44  P C  404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                 37  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20               33  P C  404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                 39  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20               35  P C  404195   PROJECT PHASE II              TW 100  40                 93  P
      404188   PROJECT PHASE I              OR  50  20               38  P C  404195   PROJECT PHASE II              PR  50  20                 44  P

  GRAND TOTAL = 1058/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053010   ARPIT BALDWA                          DARSHANA              ,71527121M   ,PICT        ,B120053010

               SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 12   28   40  P C    404189   MOBILE COMMUNICATION           PP 100 40 22   37   59  P
    404182   COMPUTER NETWORKS           PP 100 40 24   39   63  P C    404190   BROADBAND COMMUNICATION SYS.   PP 100 40 18   47   65  P
    404183   MICROWAVE ENGINEERING       PP 100 40 16   38   54  P C    404191C  AUDIO VIDEO ENGINEERING        PP 100 40 16   41   57  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100 40 22   52   74  P C    404192D  WIRELESS NETWORKS              PP 100 40 22   50   72  P
    404185C  PLCS AND AUTOMATION         PP 100 40 18   29   47  P C    404193   LAB PRACTICE III(MC & BCS)     TW  50 20             46  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50 20            44  P C    404193   LAB PRACTICE III(MC & BCS)     OR  50 20             43  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50 20            34  P C    404194   LAB PRAC. IV(ELECTIVE III)     TW  50 20             41  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20            41  P C    404194   LAB PRAC. IV(ELECTIVE III)     PR  50 20             38  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20            28  P C    404195   PROJECT PHASE II               TW 100 40             70  P
    404188   PROJECT PHASE I             OR  50 20            31  P C    404195   PROJECT PHASE II               PR  50 20             30  P

  GRAND TOTAL = 977/1500 , RESULT:FIRST CLASS
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    B120053011   ARYA K SHAJI SHAJI KOLOTH             KUMARI                ,71527124F   ,PICT        ,B120053011

               SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 15   32   47  P C    404189   MOBILE COMMUNICATION           PP 100 40 15   29   44  P
    404182   COMPUTER NETWORKS           PP 100 40 26   52   78  P C    404190   BROADBAND COMMUNICATION SYS.   PP 100 40 23   47   70  P
    404183   MICROWAVE ENGINEERING       PP 100 40 20   42   62  P C    404191C  AUDIO VIDEO ENGINEERING        PP 100 40 22   48   70  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100 40 24   41   65  P C    404192D  WIRELESS NETWORKS              PP 100 40 28   43   71  P
    404185C  PLCS AND AUTOMATION         PP 100 40 20   34   54  P C    404193   LAB PRACTICE III(MC & BCS)     TW  50 20             40  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50 20            32  P C    404193   LAB PRACTICE III(MC & BCS)     OR  50 20             46  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50 20            40  P C    404194   LAB PRAC. IV(ELECTIVE III)     TW  50 20             42  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20            39  P C    404194   LAB PRAC. IV(ELECTIVE III)     PR  50 20             38  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20            35  P C    404195   PROJECT PHASE II               TW 100 40             91  P
    404188   PROJECT PHASE I             OR  50 20            44  P C    404195   PROJECT PHASE II               PR  50 20             45  P

  GRAND TOTAL = 1053/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053012   AVHAD TEJAL VIPIN                     NAYANA                ,71527129G   ,PICT        ,B120053012

               SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 07   36   43  P C    404189   MOBILE COMMUNICATION           PP 100 40 11   31   42  P
    404182   COMPUTER NETWORKS           PP 100 40 24   42   66  P C    404190   BROADBAND COMMUNICATION SYS.   PP 100 40 16   42   58  P
    404183   MICROWAVE ENGINEERING       PP 100 40 13   44   57  P C    404191D  SOFT COMPUTING                 PP 100 40 04   36$  40  P
    404184B  EMBEDDED SYSTEMS & RTOS     PP 100 40 22   36   58  P C    404192D  WIRELESS NETWORKS              PP 100 40 18   35   53  P
    404185D  ARTIFICIAL INTELLIGENCE     PP 100 40 12   43   55  P C    404193   LAB PRACTICE III(MC & BCS)     TW  50 20             29  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50 20            35  P C    404193   LAB PRACTICE III(MC & BCS)     OR  50 20             32  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50 20            38  P C    404194   LAB PRAC. IV(ELECTIVE III)     TW  50 20             28  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20            33  P C    404194   LAB PRAC. IV(ELECTIVE III)     PR  50 20             34  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20            32  P C    404195   PROJECT PHASE II               TW 100 40             90  P
    404188   PROJECT PHASE I             OR  50 20            40  P C    404195   PROJECT PHASE II               PR  50 20             45  P

  GRAND TOTAL = 908/1500 , RESULT:FIRST CLASS   [$ O.1]
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  NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
  OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    B120053013  AWATE NETRA NANDKUMAR                 MEENAKSHI           ,71527131J   ,PICT        ,B120053013

            SEM.:1                                                     SEM.:2
    404181  VLSI DESIGN & TECHNOLOGY    PP 100  40  06    28    34# P      404189  MOBILE COMMUNICATION        PP 100  40  19    35    54  P
    404182  COMPUTER NETWORKS           PP 100  40  15    28    43  P C    404190  BROADBAND COMMUNICATION SYS. PP 100  40  23    41    64  P
    404183  MICROWAVE ENGINEERING       PP 100  40  10    41    51  P      404191C AUDIO VIDEO ENGINEERING     PP 100  40  13    43    56  P
    404184B EMBEDDED SYSTEMS & RTOS     PP 100  40  13    31    44  P C    404192D WIRELESS NETWORKS           PP 100  40  15    41    56  P
    404185D ARTIFICIAL INTELLIGENCE     PP 100  40  14    38    52  P C    404193  LAB PRACTICE III(MC & BCS)  TW  50  20                35  P
    404186  LAB PRACTICE I (CN & MWE)   TW  50  20                32  P C    404193  LAB PRACTICE III(MC & BCS)  OR  50  20                32  P
    404186  LAB PRACTICE I (CN & MWE)   OR  50  20                32  P C    404194  LAB PRAC. IV(ELECTIVE III)  TW  50  20                40  P
    404187  LAB PRAC. II(VLSI &ELEC. I) TW  50  20                35  P C    404194  LAB PRAC. IV(ELECTIVE III)  PR  50  20                40  P
    404187  LAB PRAC. II(VLSI &ELEC. I) PR  50  20                30  P C    404195  PROJECT PHASE II            TW 100  40                92  P
    404188  PROJECT PHASE I             OR  50  20                44  P C    404195  PROJECT PHASE II            PR  50  20                45  P

  GRAND TOTAL = 911/1500 , RESULT:FIRST CLASS   [# O.4]
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    B120053014  AYUSH BHADAURIA                       SUMAN              ,71527132G   ,PICT        ,B120053014

            SEM.:1                                                     SEM.:2
    404181  VLSI DESIGN & TECHNOLOGY    PP 100  40  16    34    50  P C    404189  MOBILE COMMUNICATION        PP 100  40  19    29    48  P
    404182  COMPUTER NETWORKS           PP 100  40  26    51    77  P C    404190  BROADBAND COMMUNICATION SYS. PP 100  40  19    38    57  P
    404183  MICROWAVE ENGINEERING       PP 100  40  22    47    69  P C    404191C AUDIO VIDEO ENGINEERING     PP 100  40  20    48    68  P
    404184A DIGITAL IMAGE PROCESSING    PP 100  40  18    43    61  P C    404192D WIRELESS NETWORKS           PP 100  40  27    45    72  P
    404185C PLCS AND AUTOMATION         PP 100  40  12    40    52  P C    404193  LAB PRACTICE III(MC & BCS)  TW  50  20                27  P
    404186  LAB PRACTICE I (CN & MWE)   TW  50  20                43  P C    404193  LAB PRACTICE III(MC & BCS)  OR  50  20                42  P
    404186  LAB PRACTICE I (CN & MWE)   OR  50  20                33  P C    404194  LAB PRAC. IV(ELECTIVE III)  TW  50  20                27  P
    404187  LAB PRAC. II(VLSI &ELEC. I) TW  50  20                33  P C    404194  LAB PRAC. IV(ELECTIVE III)  PR  50  20                36  P
    404187  LAB PRAC. II(VLSI &ELEC. I) PR  50  20                34  P C    404195  PROJECT PHASE II            TW 100  40                70  P
    404188  PROJECT PHASE I             OR  50  20                30  P C    404195  PROJECT PHASE II            PR  50  20                30  P

  GRAND TOTAL = 959/1500 , RESULT:FIRST CLASS
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    B120053015  BAGDARE ABHISHEK PRASHANT             SHILPA             ,71527134C   ,PICT        ,B120053015

            SEM.:1                                                     SEM.:2
    404181  VLSI DESIGN & TECHNOLOGY    PP 100  40  14    29    43  P C    404189  MOBILE COMMUNICATION        PP 100  40  17    28    45  P
    404182  COMPUTER NETWORKS           PP 100  40  26    41    67  P C    404190  BROADBAND COMMUNICATION SYS. PP 100  40  16    47    63  P
    404183  MICROWAVE ENGINEERING       PP 100  40  09    35    44  P C    404191C AUDIO VIDEO ENGINEERING     PP 100  40  20    42    62  P
    404184B EMBEDDED SYSTEMS & RTOS     PP 100  40  19    39    58  P C    404192D WIRELESS NETWORKS           PP 100  40  25    39    64  P
    404185D ARTIFICIAL INTELLIGENCE     PP 100  40  17    34    51  P C    404193  LAB PRACTICE III(MC & BCS)  TW  50  20                38  P
    404186  LAB PRACTICE I (CN & MWE)   TW  50  20                32  P C    404193  LAB PRACTICE III(MC & BCS)  OR  50  20                45  P
    404186  LAB PRACTICE I (CN & MWE)   OR  50  20                32  P C    404194  LAB PRAC. IV(ELECTIVE III)  TW  50  20                39  P
    404187  LAB PRAC. II(VLSI &ELEC. I) TW  50  20                35  P C    404194  LAB PRAC. IV(ELECTIVE III)  PR  50  20                40  P
    404187  LAB PRAC. II(VLSI &ELEC. I) PR  50  20                38  P C    404195  PROJECT PHASE II            TW 100  40                93  P
    404188  PROJECT PHASE I             OR  50  20                44  P C    404195  PROJECT PHASE II            PR  50  20                45  P

  GRAND TOTAL = 978/1500 , RESULT:FIRST CLASS
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053016   BAGUL OMKAR SANJAY                   VAISHALI              ,71527135M   ,PICT       ,B120053016

            SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 13    29    42  P C   404189   MOBILE COMMUNICATION         PP 100 40 19    29    48  P
    404182   COMPUTER NETWORKS           PP 100 40 18    40    58  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 21    42    63  P
    404183   MICROWAVE ENGINEERING       PP 100 40 11    36    47  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100 40 13    45    58  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100 40 24    44    68  P C   404192D  WIRELESS NETWORKS            PP 100 40 26    41    67  P
    404185D  ARTIFICIAL INTELLIGENCE     PP 100 40 18    40    58  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50 20              35  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50 20              26  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50 20              32  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50 20              30  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50 20              37  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20              28  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50 20              35  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20              38  P C   404195   PROJECT PHASE II             TW 100 40              60  P
    404188   PROJECT PHASE I             OR  50 20              40  P C   404195   PROJECT PHASE II             PR  50 20              22  P

 GRAND TOTAL = 892 + 08/1500 , RESULT:FIRST CLASS   [O.2]
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    B120053017   BAHETI BALAJI NAVALKISHOR            HEMA                  ,71527137H   ,PICT       ,B120053017

            SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 12    54    66  P C   404189   MOBILE COMMUNICATION         PP 100 40 23    36    59  P
    404182   COMPUTER NETWORKS           PP 100 40 24    42    66  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 23    47    70  P
    404183   MICROWAVE ENGINEERING       PP 100 40 28    39    67  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100 40 19    52    71  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100 40 26    48    74  P C   404192A  BIOMEDICAL SIGNAL PROCESSING PP 100 40 19    45    64  P
    404185B  ELECTRONIC PRODUCT DESIGN   PP 100 40 20    45    65  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50 20              23  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50 20              42  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50 20              22  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50 20              32  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50 20              24  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20              41  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50 20              38  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20              32  P C   404195   PROJECT PHASE II             TW 100 40              95  P
    404188   PROJECT PHASE I             OR  50 20              43  P C   404195   PROJECT PHASE II             PR  50 20              44  P

 GRAND TOTAL = 1038/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053018   BAIRAGI CHINTAMANI DATTATRAY         USHA                  ,71400739M   ,PICT       ,B120053018

            SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 16    56    72  P C   404189   MOBILE COMMUNICATION         PP 100 40 15    40    55  P
    404182   COMPUTER NETWORKS           PP 100 40 23    48    71  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 24    40    64  P
    404183   MICROWAVE ENGINEERING       PP 100 40 22    48    70  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100 40 20    52    72  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100 40 26    50    76  P C   404192A  BIOMEDICAL SIGNAL PROCESSING PP 100 40 26    46    72  P
    404185C  PLCS AND AUTOMATION         PP 100 40 21    43    64  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50 20              41  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50 20              43  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50 20              44  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50 20              36  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50 20              30  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20              37  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50 20              44  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20              32  P     404195   PROJECT PHASE II             TW 100 40              85  P
    404188   PROJECT PHASE I             OR  50 20              44  P C   404195   PROJECT PHASE II             PR  50 20              42  P

 GRAND TOTAL = 1094 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION   [& O.163]
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    B120053019   BAND VIPIN DILIP                         CHANDA              ,71527141F   ,PICT        ,B120053019

            SEM.:1                                                             SEM.:2
    404181  VLSI DESIGN & TECHNOLOGY      PP 100  40  09    38    47  P C   404189  MOBILE COMMUNICATION          PP 100  40  12    31    43  P
    404182  COMPUTER NETWORKS            PP 100  40  24    39    63  P C   404190  BROADBAND COMMUNICATION SYS.  PP 100  40  09    33    42  P
    404183  MICROWAVE ENGINEERING        PP 100  40  16    36    52  P C   404191C AUDIO VIDEO ENGINEERING       PP 100  40  12    43    55  P
    404184A DIGITAL IMAGE PROCESSING     PP 100  40  15    34    49  P C   404192D WIRELESS NETWORKS             PP 100  40  22    41    63  P
    404185C PLCS AND AUTOMATION          PP 100  40  10    39    49  P     404193  LAB PRACTICE III(MC & BCS)    TW  50  20              34  P
    404186  LAB PRACTICE I (CN & MWE)    TW  50  20              24  P C   404193  LAB PRACTICE III(MC & BCS)    OR  50  20              42  P
    404186  LAB PRACTICE I (CN & MWE)    OR  50  20              22  P C   404194  LAB PRAC. IV(ELECTIVE III)    TW  50  20              27  P
    404187  LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              21  P C   404194  LAB PRAC. IV(ELECTIVE III)    PR  50  20              33  P
    404187  LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              25  P     404195  PROJECT PHASE II              TW 100  40              80  P
    404188  PROJECT PHASE I              OR  50  20              34  P C   404195  PROJECT PHASE II              PR  50  20              35  P

    GRAND TOTAL = 840/1500 , RESULT:HIGHER SECOND CLASS
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    B120053020   BANDAL NEHAL ROHIDAS                     SUREKHA             ,71527142D   ,PICT        ,B120053020

            SEM.:1                                                             SEM.:2
    404181  VLSI DESIGN & TECHNOLOGY      PP 100  40  16    41    57  P C   404189  MOBILE COMMUNICATION          PP 100  40  18    44    62  P
    404182  COMPUTER NETWORKS            PP 100  40  25    34    59  P C   404190  BROADBAND COMMUNICATION SYS.  PP 100  40  22    49    71  P
    404183  MICROWAVE ENGINEERING        PP 100  40  17    49    66  P C   404191C AUDIO VIDEO ENGINEERING       PP 100  40  19    51    70  P
    404184A DIGITAL IMAGE PROCESSING     PP 100  40  15    51    66  P C   404192D WIRELESS NETWORKS             PP 100  40  28    44    72  P
    404185C PLCS AND AUTOMATION          PP 100  40  13    33    46  P C   404193  LAB PRACTICE III(MC & BCS)    TW  50  20              44  P
    404186  LAB PRACTICE I (CN & MWE)    TW  50  20              43  P C   404193  LAB PRACTICE III(MC & BCS)    OR  50  20              42  P
    404186  LAB PRACTICE I (CN & MWE)    OR  50  20              42  P C   404194  LAB PRAC. IV(ELECTIVE III)    TW  50  20              40  P
    404187  LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              32  P C   404194  LAB PRAC. IV(ELECTIVE III)    PR  50  20              42  P
    404187  LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              34  P C   404195  PROJECT PHASE II              TW 100  40              94  P
    404188  PROJECT PHASE I              OR  50  20              42  P C   404195  PROJECT PHASE II              PR  50  20              45  P

    GRAND TOTAL = 1069/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053021   BANDE NAYAN SHRIRAM                      SINDHU              ,71500143E   ,PICT        ,B120053021

            SEM.:1                                                             SEM.:2
    404181  VLSI DESIGN & TECHNOLOGY      PP 100  40  11    42    53  P C   404189  MOBILE COMMUNICATION          PP 100  40  08    21    --  F
    404182  COMPUTER NETWORKS            PP 100  40  16    35    51  P C   404190  BROADBAND COMMUNICATION SYS.  PP 100  40  15    33    48  P
    404183  MICROWAVE ENGINEERING        PP 100  40  07    33    40  P C   404191C AUDIO VIDEO ENGINEERING       PP 100  40  13    31    44  P
    404184A DIGITAL IMAGE PROCESSING     PP 100  40  17    34    51  P C   404192D WIRELESS NETWORKS             PP 100  40  18    42    60  P
    404185C PLCS AND AUTOMATION          PP 100  40  16    28    44  P     404193  LAB PRACTICE III(MC & BCS)    TW  50  20              36  P
    404186  LAB PRACTICE I (CN & MWE)    TW  50  20              39  P C   404193  LAB PRACTICE III(MC & BCS)    OR  50  20              40  P
    404186  LAB PRACTICE I (CN & MWE)    OR  50  20              25  P C   404194  LAB PRAC. IV(ELECTIVE III)    TW  50  20              30  P
    404187  LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              35  P C   404194  LAB PRAC. IV(ELECTIVE III)    PR  50  20              39  P
    404187  LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              25  P C   404195  PROJECT PHASE II              TW 100  40              42  P
    404188  PROJECT PHASE I              OR  50  20              38  P C   404195  PROJECT PHASE II              PR  50  20              22  P

    GRAND TOTAL = --/1500 , RESULT:FAILS
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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     B120053022   BARAPATRE VRUNAL NARENDRA              VAISHALI              ,71527143B   ,PICT        ,B120053022

              SEM.:1                                                   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  21   59   80  P C   404189   MOBILE COMMUNICATION          PP 100  40  18   43   61  P
     404182   COMPUTER NETWORKS            PP 100  40  28   50   78  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  21   49   70  P
     404183   MICROWAVE ENGINEERING        PP 100  40  25   43   68  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  25   58   83  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  22   57   79  P C   404192D  WIRELESS NETWORKS             PP 100  40  27   53   80  P
     404185C  PLCS AND AUTOMATION          PP 100  40  18   35   53  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20            40  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20            39  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20            42  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20            42  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20            32  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20            40  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20            36  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20            32  P C   404195   PROJECT PHASE II              TW 100  40            95  P
     404188   PROJECT PHASE I              OR  50  20            30  P C   404195   PROJECT PHASE II              PR  50  20            45  P

   GRAND TOTAL = 1125/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053023   BHADGAONKAR SARANG SUBHASH             SHAMALA              ,71527149M   ,PICT        ,B120053023

              SEM.:1                                                   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  11   49   60  P C   404189   MOBILE COMMUNICATION          PP 100  40  20   37   57  P
     404182   COMPUTER NETWORKS            PP 100  40  26   45   71  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  23   46   69  P
     404183   MICROWAVE ENGINEERING        PP 100  40  18   54   72  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  20   53   73  P
     404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  22   47   69  P C   404192D  WIRELESS NETWORKS             PP 100  40  23   51   74  P
     404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  16   45   61  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20            43  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20            37  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20            46  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20            41  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20            40  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20            39  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20            39  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20            42  P C   404195   PROJECT PHASE II              TW 100  40            92  P
     404188   PROJECT PHASE I              OR  50  20            43  P C   404195   PROJECT PHASE II              PR  50  20            43  P

   GRAND TOTAL = 1111/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053024   BHIMANI JINKAL MAHENDRAKUMAR           REKHA                ,71633605H   ,PICT        ,B120053024

              SEM.:1                                                   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  17   52   69  P C   404189   MOBILE COMMUNICATION          PP 100  40  18   40   58  P
     404182   COMPUTER NETWORKS            PP 100  40  27   50   77  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  24   47   71  P
     404183   MICROWAVE ENGINEERING        PP 100  40  23   47   70  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  20   53   73  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  20   48   68  P C   404192D  WIRELESS NETWORKS             PP 100  40  27   54   81  P
     404185C  PLCS AND AUTOMATION          PP 100  40  20   44   64  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20            41  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20            40  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20            44  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20            30  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20            34  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20            39  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20            40  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20            38  P C   404195   PROJECT PHASE II              TW 100  40            85  P
     404188   PROJECT PHASE I              OR  50  20            42  P C   404195   PROJECT PHASE II              PR  50  20            40  P

   GRAND TOTAL = 1104/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053025   BHIRUD AKSHAY PRADIP                   ANITA              ,71527155F    ,PICT        ,B120053025

                SEM.:1                                                     SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  12    43    55  P C   404189   MOBILE COMMUNICATION         PP 100  40  13   29   42  P
      404182   COMPUTER NETWORKS           PP 100  40  18    35    53  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  21   36   57  P
      404183   MICROWAVE ENGINEERING       PP 100  40  16    33    49  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  19   46   65  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  16    43    59  P C   404192D  WIRELESS NETWORKS            PP 100  40  26   53   79  P
      404185C  PLCS AND AUTOMATION         PP 100  40  11    35    46  P     404193   LAB PRACTICE III(MC & BCS)   TW  50  20             47  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20             39  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20             46  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20             30  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20             38  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             45  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20             32  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             25  P C   404195   PROJECT PHASE II             TW 100  40             93  P
      404188   PROJECT PHASE I             OR  50  20             40  P C   404195   PROJECT PHASE II             PR  50  20             47  P

      GRAND TOTAL = 987/1500 , RESULT:FIRST CLASS
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      B120053026   BHISE AKSHAY UDDHAV                    MEERA              ,71527156D    ,PICT        ,B120053026

                SEM.:1                                                     SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  12    48    60  P C   404189   MOBILE COMMUNICATION         PP 100  40  15   39   54  P
      404182   COMPUTER NETWORKS           PP 100  40  25    40    65  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  29   49   78  P
      404183   MICROWAVE ENGINEERING       PP 100  40  29    48    77  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  14   48   62  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  22    50    72  P C   404192D  WIRELESS NETWORKS            PP 100  40  27   45   72  P
      404185C  PLCS AND AUTOMATION         PP 100  40  20    40    60  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20             35  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20             31  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20             32  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20             32  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20             40  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             35  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20             33  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             29  P C   404195   PROJECT PHASE II             TW 100  40             70  P
      404188   PROJECT PHASE I             OR  50  20             35  P C   404195   PROJECT PHASE II             PR  50  20             30  P

      GRAND TOTAL = 1002/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053027   BHOITE ANIKET DEEPAK                   NIKITA             ,71633606F    ,PICT        ,B120053027

                SEM.:1                                                     SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  10    51    61  P C   404189   MOBILE COMMUNICATION         PP 100  40  20   28   48  P
      404182   COMPUTER NETWORKS           PP 100  40  27    43    70  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  18   53   71  P
      404183   MICROWAVE ENGINEERING       PP 100  40  26    47    73  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  25   50   75  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  21    45    66  P C   404192D  WIRELESS NETWORKS            PP 100  40  29   50   79  P
      404185C  PLCS AND AUTOMATION         PP 100  40  23    51    74  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20             43  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20             43  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20             43  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20             38  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20             39  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             42  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20             38  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             36  P C   404195   PROJECT PHASE II             TW 100  40             90  P
      404188   PROJECT PHASE I             OR  50  20             40  P C   404195   PROJECT PHASE II             PR  50  20             45  P

      GRAND TOTAL = 1114/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,  SEAT NO.
     OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053028   BHUJBAL AJINKYA RAVINDRA            VIJAYA              ,71527160B   ,PICT         ,B120053028

              SEM.:1                                                  SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  10    32    42  P C   404189   MOBILE COMMUNICATION        PP 100  40  20    17    --  F
     404182   COMPUTER NETWORKS             PP 100  40  23    32    55  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  14    33    47  P
     404183   MICROWAVE ENGINEERING         PP 100  40  13    39    52  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  16    30    46  P
     404184A  DIGITAL IMAGE PROCESSING      PP 100  40  01    34    --  F     404192D  WIRELESS NETWORKS           PP 100  40  15    29    44  P
     404185C  PLCS AND AUTOMATION           PP 100  40  18    33    51  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20                42  P
     404186   LAB PRACTICE I (CN & MWE)     TW  50  20              39  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20                41  P
     404186   LAB PRACTICE I (CN & MWE)     OR  50  20              36  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20                36  P
     404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              43  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20                36  P
     404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              34  P C   404195   PROJECT PHASE II            TW 100  40                91  P
     404188   PROJECT PHASE I               OR  50  20              45  P C   404195   PROJECT PHASE II            PR  50  20                42  P

GRAND TOTAL = --/1500 , RESULT:FAILS
                                                         RESULT RESERVED FOR BACKLOGS.
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     B120053029   BIDKAR SHUBHAM MANOJ                MANASI              ,71527162J   ,PICT         ,B120053029

              SEM.:1                                                  SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  11    51    62  P C   404189   MOBILE COMMUNICATION        PP 100  40  17    33    50  P
     404182   COMPUTER NETWORKS             PP 100  40  20    45    65  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  26    45    71  P
     404183   MICROWAVE ENGINEERING         PP 100  40  22    53    75  P C   404191D  SOFT COMPUTING              PP 100  40  21    41    62  P
     404184B  EMBEDDED SYSTEMS & RTOS       PP 100  40  20    47    67  P C   404192A  BIOMEDICAL SIGNAL PROCESSING PP 100  40  21    41    62  P
     404185D  ARTIFICIAL INTELLIGENCE       PP 100  40  17    46    63  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20                40  P
     404186   LAB PRACTICE I (CN & MWE)     TW  50  20              40  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20                46  P
     404186   LAB PRACTICE I (CN & MWE)     OR  50  20              45  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20                37  P
     404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              40  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20                38  P
     404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              36  P C   404195   PROJECT PHASE II            TW 100  40                97  P
     404188   PROJECT PHASE I               OR  50  20              44  P C   404195   PROJECT PHASE II            PR  50  20                48  P

GRAND TOTAL = 1088/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053030   BIRAJDAR RUSHABH UMAKANT            RANJANA             ,71527163G   ,PICT         ,B120053030

              SEM.:1                                                  SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  12    47    59  P C   404189   MOBILE COMMUNICATION        PP 100  40  11    40    51  P
     404182   COMPUTER NETWORKS             PP 100  40  20    42    62  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  23    36    59  P
     404183   MICROWAVE ENGINEERING         PP 100  40  21    35    56  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  10    40    50  P
     404184A  DIGITAL IMAGE PROCESSING      PP 100  40  22    34    56  P C   404192A  BIOMEDICAL SIGNAL PROCESSING PP 100  40  16    38    54  P
     404185C  PLCS AND AUTOMATION           PP 100  40  13    30    43  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20                37  P
     404186   LAB PRACTICE I (CN & MWE)     TW  50  20              47  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20                37  P
     404186   LAB PRACTICE I (CN & MWE)     OR  50  20              28  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20                39  P
     404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              37  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20                40  P
     404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              23  P C   404195   PROJECT PHASE II            TW 100  40                84  P
     404188   PROJECT PHASE I               OR  50  20              42  P C   404195   PROJECT PHASE II            PR  50  20                42  P

GRAND TOTAL = 946 + 10/1500 , RESULT:FIRST CLASS    [& O.163]
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,  SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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       B120053031   BOBADE SRUSHTI PRAKASH                DARSHANA            ,71527164E   ,PICT        ,B120053031

              SEM.:1                                                    SEM.:2
       404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  15    55   70  P C   404189   MOBILE COMMUNICATION         PP 100  40  17    38   55  P
       404182   COMPUTER NETWORKS            PP 100  40  27    33   60  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  27    42   69  P
       404183   MICROWAVE ENGINEERING        PP 100  40  22    45   67  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  23    50   73  P
       404184A  DIGITAL IMAGE PROCESSING     PP 100  40  21    46   67  P C   404192D  WIRELESS NETWORKS            PP 100  40  29    38   67  P
       404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  18    45   63  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              36  P
       404186   LAB PRACTICE I (CN & MWE)    TW  50  20              29  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              44  P
       404186   LAB PRACTICE I (CN & MWE)    OR  50  20              35  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              42  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              40  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              40  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              44  P C   404195   PROJECT PHASE II             TW 100  40              95  P
       404188   PROJECT PHASE I              OR  50  20              43  P C   404195   PROJECT PHASE II             PR  50  20              45  P

    GRAND TOTAL = 1084/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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       B120053032   BOMALE ADITYA SANJAY                  YASHODA             ,71527166M   ,PICT        ,B120053032

              SEM.:1                                                    SEM.:2
       404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  16    53   69  P C   404189   MOBILE COMMUNICATION         PP 100  40  23    38   61  P
       404182   COMPUTER NETWORKS            PP 100  40  26    41   67  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  26    37   63  P
       404183   MICROWAVE ENGINEERING        PP 100  40  14    36   50  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  21    48   69  P
       404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  21    40   61  P C   404192D  WIRELESS NETWORKS            PP 100  40  26    47   73  P
       404185C  PLCS AND AUTOMATION          PP 100  40  13    38   51  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              41  P
       404186   LAB PRACTICE I (CN & MWE)    TW  50  20              38  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              44  P
       404186   LAB PRACTICE I (CN & MWE)    OR  50  20              38  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              44  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              41  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              39  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              33  P C   404195   PROJECT PHASE II             TW 100  40              94  P
       404188   PROJECT PHASE I              OR  50  20              42  P C   404195   PROJECT PHASE II             PR  50  20              43  P

    GRAND TOTAL = 1061/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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       B120053033   BORA HARSHAL MAHESH                   VAISHALI            ,71527168H   ,PICT        ,B120053033

              SEM.:1                                                    SEM.:2
       404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  14    39   53  P C   404189   MOBILE COMMUNICATION         PP 100  40  24    37   61  P
       404182   COMPUTER NETWORKS            PP 100  40  23    32   55  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  25    32   57  P
       404183   MICROWAVE ENGINEERING        PP 100  40  20    35   55  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  13    51   64  P
       404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  18    43   61  P C   404192D  WIRELESS NETWORKS            PP 100  40  27    34   61  P
       404185C  PLCS AND AUTOMATION          PP 100  40  13    44   57  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              39  P
       404186   LAB PRACTICE I (CN & MWE)    TW  50  20              34  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              40  P
       404186   LAB PRACTICE I (CN & MWE)    OR  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              40  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              43  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              39  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              41  P C   404195   PROJECT PHASE II             TW 100  40              96  P
       404188   PROJECT PHASE I              OR  50  20              47  P C   404195   PROJECT PHASE II             PR  50  20              46  P

    GRAND TOTAL = 1031/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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     B120053034   BOROLE NEHAL RAJENDRA   VANDANA   ,71527169F   ,PICT   ,B120053034

              SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 16   54   70 P C   404189   MOBILE COMMUNICATION   PP 100 40 26   51   77 P
     404182   COMPUTER NETWORKS   PP 100 40 25   37   62 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 26   43   69 P
     404183   MICROWAVE ENGINEERING   PP 100 40 25   45   70 P C   404191D SOFT COMPUTING   PP 100 40 23   50   73 P
     404184B EMBEDDED SYSTEMS & RTOS   PP 100 40 21   45   66 P C   404192D WIRELESS NETWORKS   PP 100 40 28   42   70 P
     404185D ARTIFICIAL INTELLIGENCE   PP 100 40 13   45   58 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   42 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   44 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   45 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   46 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   40 P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   41 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   46 P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   39 P C   404195   PROJECT PHASE II   TW 100 40   97 P
     404188   PROJECT PHASE I   OR 50 20   39 P C   404195   PROJECT PHASE II   PR 50 20   48 P

     GRAND TOTAL = 1142/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053035   BOTRE AKSHAYRAJ GANPAT   PRATIBHA   ,71527170K   ,PICT   ,B120053035

              SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 14   55   69 P C   404189   MOBILE COMMUNICATION   PP 100 40 26   36   62 P
     404182   COMPUTER NETWORKS   PP 100 40 26   35   61 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 25   44   69 P
     404183   MICROWAVE ENGINEERING   PP 100 40 20   40   60 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 19   55   74 P
     404184A DIGITAL IMAGE PROCESSING   PP 100 40 19   55   74 P C   404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 24   46   70 P
     404185C PLCS AND AUTOMATION   PP 100 40 17   30   47 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   32 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   44 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   45 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   34 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   40 P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   38 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   36 P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   38 P C   404195   PROJECT PHASE II   TW 100 40   96 P
     404188   PROJECT PHASE I   OR 50 20   33 P C   404195   PROJECT PHASE II   PR 50 20   47 P

     GRAND TOTAL = 1069 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION   [& O.163]
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     B120053036   CHANDOLE PALLAVI BHARAT   ARATI   ,71633607D   ,PICT   ,B120053036

              SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 19   61   80 P C   404189   MOBILE COMMUNICATION   PP 100 40 26   41   67 P
     404182   COMPUTER NETWORKS   PP 100 40 29   44   73 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 29   44   73 P
     404183   MICROWAVE ENGINEERING   PP 100 40 29   48   77 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 20   43   63 P
     404184A DIGITAL IMAGE PROCESSING   PP 100 40 26   42   68 P C   404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 22   38   60 P
     404185C PLCS AND AUTOMATION   PP 100 40 27   40   67 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   40 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   47 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   45 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   40 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   36 P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   40 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   41 P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   40 P C   404195   PROJECT PHASE II   TW 100 40   90 P
     404188   PROJECT PHASE I   OR 50 20   40 P C   404195   PROJECT PHASE II   PR 50 20   44 P

     GRAND TOTAL = 1131/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053037   CHANGEDE SHRENIK SANJAY                 SHAILAJA              ,71527174B   ,PICT        ,B120053037

             SEM.:1                                                       SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  13   50   63  P C   404189   MOBILE COMMUNICATION        PP 100  40  15   42   57  P
      404182   COMPUTER NETWORKS           PP 100  40  21   42   63  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  20   38   58  P
      404183   MICROWAVE ENGINEERING       PP 100  40  17   39   56  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  16   50   66  P
      404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  20   39   59  P C   404192D  WIRELESS NETWORKS           PP 100  40  27   41   68  P
      404185C  PLCS AND AUTOMATION         PP 100  40  12   40   52  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20              41  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20             38  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20              45  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20             39  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              38  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             42  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              38  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             32  P C   404195   PROJECT PHASE II            TW 100  40              94  P
      404188   PROJECT PHASE I             OR  50  20             42  P C   404195   PROJECT PHASE II            PR  50  20              47  P

   GRAND TOTAL = 1038/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053038   CHARVI AJAY GUPTA                       RASHMI               ,71527176J   ,PICT        ,B120053038

             SEM.:1                                                       SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  17   55   72  P C   404189   MOBILE COMMUNICATION        PP 100  40  17   47   64  P
      404182   COMPUTER NETWORKS           PP 100  40  26   42   68  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  19   46   65  P
      404183   MICROWAVE ENGINEERING       PP 100  40  22   36   58  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  16   46   62  P
      404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  23   47   70  P C   404192D  WIRELESS NETWORKS           PP 100  40  22   53   75  P
      404185D  ARTIFICIAL INTELLIGENCE     PP 100  40  20   45   65  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20              40  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20             38  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20              44  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20             44  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              39  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             37  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              37  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             34  P C   404195   PROJECT PHASE II            TW 100  40              92  P
      404188   PROJECT PHASE I             OR  50  20             44  P C   404195   PROJECT PHASE II            PR  50  20              44  P

   GRAND TOTAL = 1092/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053039   CHAUDHARI DARSHAN DURGADAS              JAYASHRI             ,71527177G   ,PICT        ,B120053039

             SEM.:1                                                       SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  14   59   73  P C   404189   MOBILE COMMUNICATION        PP 100  40  22   42   64  P
      404182   COMPUTER NETWORKS           PP 100  40  29   43   72  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  29   46   75  P
      404183   MICROWAVE ENGINEERING       PP 100  40  24   50   74  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  19   51   70  P
      404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  22   52   74  P C   404192D  WIRELESS NETWORKS           PP 100  40  29   54   83  P
      404185C  PLCS AND AUTOMATION         PP 100  40  21   45   66  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20              40  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20             36  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20              43  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20             43  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              41  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             41  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              40  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             40  P C   404195   PROJECT PHASE II            TW 100  40              95  P
      404188   PROJECT PHASE I             OR  50  20             46  P C   404195   PROJECT PHASE II            PR  50  20              45  P

   GRAND TOTAL = 1161/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053040   CHAUDHARI MITAL DURGADAS              SUNITA              ,71527178E   ,PICT        ,B120053040

              SEM.:1                                                       SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  13    57    70  P C   404189   MOBILE COMMUNICATION          PP 100  40  23    42    65  P
     404182   COMPUTER NETWORKS            PP 100  40  29    34    63  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  28    43    71  P
     404183   MICROWAVE ENGINEERING        PP 100  40  27    47    74  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  24    45    69  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  24    39    63  P C   404192D  WIRELESS NETWORKS             PP 100  40  29    58    87  P
     404185C  PLCS AND AUTOMATION          PP 100  40  16    37    53  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              40  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              27  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              42  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              38  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              40  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              34  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              39  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              41  P C   404195   PROJECT PHASE II              TW 100  40              93  P
     404188   PROJECT PHASE I              OR  50  20              42  P C   404195   PROJECT PHASE II              PR  50  20              44  P

   GRAND TOTAL = 1095/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053041   CHAUDHARI SAMEER SATISH              SAVITA              ,71527180G   ,PICT        ,B120053041

              SEM.:1                                                       SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  16    51    67  P C   404189   MOBILE COMMUNICATION          PP 100  40  14    39    53  P
     404182   COMPUTER NETWORKS            PP 100  40  21    48    69  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  23    47    70  P
     404183   MICROWAVE ENGINEERING        PP 100  40  23    43    66  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  16    57    73  P
     404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  20    54    74  P C   404192D  WIRELESS NETWORKS             PP 100  40  26    48    74  P
     404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  21    44    65  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              43  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              35  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              43  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              30  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              40  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              39  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              38  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              32  P C   404195   PROJECT PHASE II              TW 100  40              90  P
     404188   PROJECT PHASE I              OR  50  20              38  P C   404195   PROJECT PHASE II              PR  50  20              45  P

   GRAND TOTAL = 1084/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053042   CHAVAN MONIKA RADHAKISAN              SANGITA             ,71633608B   ,PICT        ,B120053042

              SEM.:1                                                       SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  16    60    76  P C   404189   MOBILE COMMUNICATION          PP 100  40  21    63    84  P
     404182   COMPUTER NETWORKS            PP 100  40  28    45    73  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  27    57    84  P
     404183   MICROWAVE ENGINEERING        PP 100  40  25    46    71  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  23    55    78  P
     404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  23    55    78  P C   404192D  WIRELESS NETWORKS             PP 100  40  29    60    89  P
     404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  25    50    75  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              43  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              38  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              43  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              35  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              44  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              44  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              40  P C   404195   PROJECT PHASE II              TW 100  40              94  P
     404188   PROJECT PHASE I              OR  50  20              43  P C   404195   PROJECT PHASE II              PR  50  20              46  P

   GRAND TOTAL = 1220/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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       NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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       B120053043   CHHAJED KSHITIJ SUBHASH                  SANDHYA              ,71527182C   ,PICT        ,B120053043

               SEM.:1                                                            SEM.:2
       404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  12    47    59  P C     404189   MOBILE COMMUNICATION          PP 100  40  19    44    63  P
       404182   COMPUTER NETWORKS            PP 100  40  28    31    59  P C     404190   BROADBAND COMMUNICATION SYS.  PP 100  40  27    39    66  P
       404183   MICROWAVE ENGINEERING        PP 100  40  19    37    56  P C     404191D  SOFT COMPUTING                PP 100  40  24    42    66  P
       404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  23    29    52  P C     404192A  BIOMEDICAL SIGNAL PROCESSING  PP 100  40  21    43    64  P
       404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  14    35    49  P C     404193   LAB PRACTICE III(MC & BCS)    TW  50  20                36  P
       404186   LAB PRACTICE I (CN & MWE)    TW  50  20          37  P C         404193   LAB PRACTICE III(MC & BCS)    OR  50  20                25  P
       404186   LAB PRACTICE I (CN & MWE)    OR  50  20          36  P C         404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                46  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20          42  P C         404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                32  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20          34  P C         404195   PROJECT PHASE II              TW 100  40                85  P
       404188   PROJECT PHASE I              OR  50  20          30  P C         404195   PROJECT PHASE II              PR  50  20                30  P

       GRAND TOTAL = 967/1500 , RESULT:FIRST CLASS
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       B120053044   CHIKHALE TEJASWINI SAYAJIRAO             SUMAN                ,71633609L   ,PICT        ,B120053044

               SEM.:1                                                            SEM.:2
       404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  11    58    69  P C     404189   MOBILE COMMUNICATION          PP 100  40  26    56    82  P
       404182   COMPUTER NETWORKS            PP 100  40  29    42    71  P C     404190   BROADBAND COMMUNICATION SYS.  PP 100  40  25    51    76  P
       404183   MICROWAVE ENGINEERING        PP 100  40  25    55    80  P C     404191C  AUDIO VIDEO ENGINEERING       PP 100  40  24    53    77  P
       404184A  DIGITAL IMAGE PROCESSING     PP 100  40  21    51    72  P C     404192D  WIRELESS NETWORKS             PP 100  40  27    57    84  P
       404185C  PLCS AND AUTOMATION          PP 100  40  22    48    70  P C     404193   LAB PRACTICE III(MC & BCS)    TW  50  20                42  P
       404186   LAB PRACTICE I (CN & MWE)    TW  50  20          40  P C         404193   LAB PRACTICE III(MC & BCS)    OR  50  20                31  P
       404186   LAB PRACTICE I (CN & MWE)    OR  50  20          42  P C         404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                36  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20          41  P C         404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                40  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20          44  P C         404195   PROJECT PHASE II              TW 100  40                85  P
       404188   PROJECT PHASE I              OR  50  20          42  P C         404195   PROJECT PHASE II              PR  50  20                40  P

       GRAND TOTAL = 1164/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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       B120053045   CHINMAY NITIN INDURKAR                   REWA                 ,71527184K   ,PICT        ,B120053045

               SEM.:1                                                            SEM.:2
       404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  06    56    62  P C     404189   MOBILE COMMUNICATION          PP 100  40  24    44    68  P
       404182   COMPUTER NETWORKS            PP 100  40  24    37    61  P C     404190   BROADBAND COMMUNICATION SYS.  PP 100  40  28    45    73  P
       404183   MICROWAVE ENGINEERING        PP 100  40  24    48    72  P C     404191D  SOFT COMPUTING                PP 100  40  26    49    75  P
       404184A  DIGITAL IMAGE PROCESSING     PP 100  40  13    42    55  P C     404192D  WIRELESS NETWORKS             PP 100  40  26    53    79  P
       404185C  PLCS AND AUTOMATION          PP 100  40  20    51    71  P C     404193   LAB PRACTICE III(MC & BCS)    TW  50  20                44  P
       404186   LAB PRACTICE I (CN & MWE)    TW  50  20          43  P C         404193   LAB PRACTICE III(MC & BCS)    OR  50  20                40  P
       404186   LAB PRACTICE I (CN & MWE)    OR  50  20          45  P C         404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                43  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20          43  P C         404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                45  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20          46  P C         404195   PROJECT PHASE II              TW 100  40                92  P
       404188   PROJECT PHASE I              OR  50  20          43  P C         404195   PROJECT PHASE II              PR  50  20                43  P

       GRAND TOTAL = 1143/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053046   CHIRAG                              SUDESH              ,71527185H   ,PICT        ,B120053046

            SEM.:1                                                      SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  16    53    69  P C   404189   MOBILE COMMUNICATION          PP 100  40  22    45    67  P
    404182   COMPUTER NETWORKS           PP 100  40  27    38    65  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  26    48    74  P
    404183   MICROWAVE ENGINEERING       PP 100  40  25    40    65  P C   404191A  SPEECH & AUDIO SIGNAL PROC.   PP 100  40  AA    44    44  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100  40  24    46    70  P C   404192A  BIOMEDICAL SIGNAL PROCESSING  PP 100  40  22    45    67  P
    404185C  PLCS AND AUTOMATION         PP 100  40  13    42    55  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20                38  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20                47  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20                44  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20                39  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                34  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20                35  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                38  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20                28  P C   404195   PROJECT PHASE II              TW 100  40                90  P
    404188   PROJECT PHASE I             OR  50  20                40  P C   404195   PROJECT PHASE II              PR  50  20                44  P

GRAND TOTAL = 1053/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053047   CHOUKSEY SONAL NARENDRA             SHARDA              ,71633610D   ,PICT        ,B120053047

            SEM.:1                                                      SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  13    58    71  P C   404189   MOBILE COMMUNICATION          PP 100  40  18    47    65  P
    404182   COMPUTER NETWORKS           PP 100  40  27    40    67  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  27    49    76  P
    404183   MICROWAVE ENGINEERING       PP 100  40  23    42    65  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  21    40    61  P
    404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  22    44    66  P C   404192D  WIRELESS NETWORKS             PP 100  40  29    53    82  P
    404185C  PLCS AND AUTOMATION         PP 100  40  17    57    74  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20                40  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20                37  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20                42  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20                40  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                24  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20                37  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                39  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20                31  P C   404195   PROJECT PHASE II              TW 100  40                95  P
    404188   PROJECT PHASE I             OR  50  20                40  P C   404195   PROJECT PHASE II              PR  50  20                45  P

GRAND TOTAL = 1097 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION   [& O.163]
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    B120053048   DABADGAONKAR SATYAM SANJAY          SUDHA               ,71527189L   ,PICT        ,B120053048

            SEM.:1                                                      SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  15    53    68  P C   404189   MOBILE COMMUNICATION          PP 100  40  20    45    65  P
    404182   COMPUTER NETWORKS           PP 100  40  29    40    69  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  21    39    60  P
    404183   MICROWAVE ENGINEERING       PP 100  40  19    37    56  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  20    40    60  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100  40  24    47    71  P C   404192A  BIOMEDICAL SIGNAL PROCESSING  PP 100  40  25    45    70  P
    404185C  PLCS AND AUTOMATION         PP 100  40  17    53    70  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20                46  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20                43  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20                45  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20                42  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                40  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20                44  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                40  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20                48  P C   404195   PROJECT PHASE II              TW 100  40                97  P
    404188   PROJECT PHASE I             OR  50  20                37  P C   404195   PROJECT PHASE II              PR  50  20                48  P

GRAND TOTAL = 1119 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION   [& O.163]
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053049   DAHIPHALE VIJAY DATTU                ANTIKA              ,71527190D   ,PICT        ,B120053049

             SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  10    54   64  P C   404189   MOBILE COMMUNICATION        PP 100  40  25    41   66  P
     404182   COMPUTER NETWORKS           PP 100  40  27    37   64  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  26    47   73  P
     404183   MICROWAVE ENGINEERING       PP 100  40  25    37   62  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  16    37   53  P
     404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  19    49   68  P C   404192D  WIRELESS NETWORKS           PP 100  40  23    52   75  P
     404185C  PLCS AND AUTOMATION         PP 100  40  17    49   66  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20              35  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50  20              37   P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20              40  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50  20              45   P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              41  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              43   P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              41  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              41   P C   404195   PROJECT PHASE II            TW 100  40              95  P
     404188   PROJECT PHASE I             OR  50  20              44   P C   404195   PROJECT PHASE II            PR  50  20              45  P

   GRAND TOTAL = 1098/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053050   DEOSALE JINENDRA ANJANKUMAR          SMITA               ,71527194G   ,PICT        ,B120053050

             SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  15    52   67  P C   404189   MOBILE COMMUNICATION        PP 100  40  20    42   62  P
     404182   COMPUTER NETWORKS           PP 100  40  24    33   57  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  25    48   73  P
     404183   MICROWAVE ENGINEERING       PP 100  40  21    41   62  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  17    36   53  P
     404184A  DIGITAL IMAGE PROCESSING    PP 100  40  20    50   70  P C   404192A  BIOMEDICAL SIGNAL PROCESSING PP 100  40  18    39   57  P
     404185C  PLCS AND AUTOMATION         PP 100  40  19    49   68  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20              30  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50  20              43   P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20              40  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50  20              38   P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              38  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              41   P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              39  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              46   P C   404195   PROJECT PHASE II            TW 100  40              96  P
     404188   PROJECT PHASE I             OR  50  20              35   P C   404195   PROJECT PHASE II            PR  50  20              47  P

   GRAND TOTAL = 1062/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053051   DESHMUKH RUCHA UMESH                 VAISHALI            ,71527198K   ,PICT        ,B120053051

             SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  10    58   68  P C   404189   MOBILE COMMUNICATION        PP 100  40  19    39   58  P
     404182   COMPUTER NETWORKS           PP 100  40  29    37   66  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  28    44   72  P
     404183   MICROWAVE ENGINEERING       PP 100  40  26    45   71  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  20    37   57  P
     404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  24    41   65  P C   404192D  WIRELESS NETWORKS           PP 100  40  28    48   76  P
     404185B  ELECTRONIC PRODUCT DESIGN   PP 100  40  18    45   63  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20              40  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50  20              34   P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20              41  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50  20              42   P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              43  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              45   P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              40  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              36   P C   404195   PROJECT PHASE II            TW 100  40              95  P
     404188   PROJECT PHASE I             OR  50  20              43   P C   404195   PROJECT PHASE II            PR  50  20              45  P

   GRAND TOTAL = 1100/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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     B120053052   DESHMUKH TANMAYI VINOD   RASHMI   ,71527199H   ,PICT   ,B120053052

             SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 17   54   71 P C   404189   MOBILE COMMUNICATION   PP 100 40 20   40   60 P
     404182   COMPUTER NETWORKS   PP 100 40 24   40   64 P C   404190   BROADBAND COMMUNICATION SYS.   PP 100 40 18   51   69 P
     404183   MICROWAVE ENGINEERING   PP 100 40 21   43   64 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 17   43   60 P
     404184A DIGITAL IMAGE PROCESSING   PP 100 40 15   49   64 P C   404192D WIRELESS NETWORKS   PP 100 40 28   55   83 P
     404185C PLCS AND AUTOMATION   PP 100 40 22   48   70 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   42 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   34 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   24 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   28 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   36 P
     404187   LAB PRAC. II(VLSI &ELEC. I)   TW 50 20   43 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   38 P
     404187   LAB PRAC. II(VLSI &ELEC. I)   PR 50 20   36 P C   404195   PROJECT PHASE II   TW 100 40   85 P
     404188   PROJECT PHASE I   OR 50 20   41 P C   404195   PROJECT PHASE II   PR 50 20   39 P

     GRAND TOTAL = 1051/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053053   DESHPANDE MANSI VASANT   VAISHALI   ,71527201C   ,PICT   ,B120053053

             SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 10   50   60 P C   404189   MOBILE COMMUNICATION   PP 100 40 20   37   57 P
     404182   COMPUTER NETWORKS   PP 100 40 24   36   60 P C   404190   BROADBAND COMMUNICATION SYS.   PP 100 40 23   44   67 P
     404183   MICROWAVE ENGINEERING   PP 100 40 20   39   59 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 17   34   51 P
     404184B EMBEDDED SYSTEMS & RTOS   PP 100 40 25   48   73 P C   404192D WIRELESS NETWORKS   PP 100 40 27   40   67 P
     404185C PLCS AND AUTOMATION   PP 100 40 22   47   69 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   36 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   38 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   32 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   36 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   40 P
     404187   LAB PRAC. II(VLSI &ELEC. I)   TW 50 20   38 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   34 P
     404187   LAB PRAC. II(VLSI &ELEC. I)   PR 50 20   39 P C   404195   PROJECT PHASE II   TW 100 40   88 P
     404188   PROJECT PHASE I   OR 50 20   39 P C   404195   PROJECT PHASE II   PR 50 20   40 P

     GRAND TOTAL = 1023/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053054   DESHPANDE SUKHADA SUNIL   APARNA   ,71527206D   ,PICT   ,B120053054

             SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 18   53   71 P C   404189   MOBILE COMMUNICATION   PP 100 40 22   42   64 P
     404182   COMPUTER NETWORKS   PP 100 40 27   40   67 P C   404190   BROADBAND COMMUNICATION SYS.   PP 100 40 23   50   73 P
     404183   MICROWAVE ENGINEERING   PP 100 40 26   48   74 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 16   46   62 P
     404184A DIGITAL IMAGE PROCESSING   PP 100 40 24   51   75 P C   404192D WIRELESS NETWORKS   PP 100 40 28   43   71 P
     404185C PLCS AND AUTOMATION   PP 100 40 20   55   75 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   36 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   27 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   43 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   33 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   33 P
     404187   LAB PRAC. II(VLSI &ELEC. I)   TW 50 20   32 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   38 P
     404187   LAB PRAC. II(VLSI &ELEC. I)   PR 50 20   44 P C   404195   PROJECT PHASE II   TW 100 40   90 P
     404188   PROJECT PHASE I   OR 50 20   41 P C   404195   PROJECT PHASE II   PR 50 20   41 P

     GRAND TOTAL = 1090/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,   PREVIOUS SEAT NO.,   COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053055   DESHPANDE UMANG SATISH                 SNEHAL                ,71527207B   ,PICT        ,B120053055

            SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  18    56    74  P C   404189   MOBILE COMMUNICATION          PP 100  40  21    53    74  P
    404182   COMPUTER NETWORKS             PP 100  40  27    48    75  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  27    50    77  P
    404183   MICROWAVE ENGINEERING         PP 100  40  19    43    62  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  20    45    65  P
    404184B  EMBEDDED SYSTEMS & RTOS       PP 100  40  21    50    71  P C   404192D  WIRELESS NETWORKS             PP 100  40  29    46    75  P
    404185D  ARTIFICIAL INTELLIGENCE       PP 100  40  21    44    65  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              44  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20              37  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              44  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20              41  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              44  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              39  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              41  P C   404195   PROJECT PHASE II              TW 100  40              95  P
    404188   PROJECT PHASE I               OR  50  20              47  P C   404195   PROJECT PHASE II              PR  50  20              45  P

 GRAND TOTAL = 1157/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053056   DEVADE SAYALI KALYAN                   SITA                  ,71633611B   ,PICT        ,B120053056

            SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  16    56    72  P C   404189   MOBILE COMMUNICATION          PP 100  40  19    50    69  P
    404182   COMPUTER NETWORKS             PP 100  40  29    45    74  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  28    51    79  P
    404183   MICROWAVE ENGINEERING         PP 100  40  24    50    74  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  20    39    59  P
    404184A  DIGITAL IMAGE PROCESSING      PP 100  40  18    48    66  P C   404192D  WIRELESS NETWORKS             PP 100  40  28    48    76  P
    404185C  PLCS AND AUTOMATION           PP 100  40  20    45    65  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              42  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20              39  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              32  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20              30  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              36  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              39  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              39  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              36  P C   404195   PROJECT PHASE II              TW 100  40              86  P
    404188   PROJECT PHASE I               OR  50  20              38  P C   404195   PROJECT PHASE II              PR  50  20              40  P

 GRAND TOTAL = 1091/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053057   DEVANG VINAY DAVE                      NEETA                 ,71527209J   ,PICT        ,B120053057

            SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  17    57    74  P C   404189   MOBILE COMMUNICATION          PP 100  40  25    47    72  P
    404182   COMPUTER NETWORKS             PP 100  40  24    48    72  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  27    43    70  P
    404183   MICROWAVE ENGINEERING         PP 100  40  17    47    64  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  16    44    60  P
    404184A  DIGITAL IMAGE PROCESSING      PP 100  40  22    52    74  P C   404192A  BIOMEDICAL SIGNAL PROCESSING  PP 100  40  26    41    67  P
    404185C  PLCS AND AUTOMATION           PP 100  40  16    42    58  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              27  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20              43  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              38  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20              38  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              33  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              38  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              38  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              42  P C   404195   PROJECT PHASE II              TW 100  40              95  P
    404188   PROJECT PHASE I               OR  50  20              38  P C   404195   PROJECT PHASE II              PR  50  20              45  P

 GRAND TOTAL = 1086/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    B120053058   DHAKANE KIRANKUMAR SHAHADEO            SHOBHA              ,71633612L   ,PICT         ,B120053058

           SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  16    52   68  P C   404189   MOBILE COMMUNICATION         PP 100  40  16  54   70  P
    404182   COMPUTER NETWORKS             PP 100  40  23    43   66  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  23  40   63  P
    404183   MICROWAVE ENGINEERING         PP 100  40  23    39   62  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  19  45   64  P
    404184A  DIGITAL IMAGE PROCESSING      PP 100  40  19    50   69  P C   404192D  WIRELESS NETWORKS            PP 100  40  23  49   72  P
    404185B  ELECTRONIC PRODUCT DESIGN     PP 100  40  19    42   61  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20           40  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20             43  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20           42  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20             34  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20           34  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20             39  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20           38  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20             44  P C   404195   PROJECT PHASE II             TW 100  40           95  P
    404188   PROJECT PHASE I               OR  50  20             43  P C   404195   PROJECT PHASE II             PR  50  20           40  P

  GRAND TOTAL = 1087/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053059   DHONDGE KANCHAN VIJAY                 NANDINI             ,71633613J   ,PICT         ,B120053059

           SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  16    56   72  P C   404189   MOBILE COMMUNICATION         PP 100  40  23  50   73  P
    404182   COMPUTER NETWORKS             PP 100  40  26    48   74  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  20  45   65  P
    404183   MICROWAVE ENGINEERING         PP 100  40  23    44   67  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  15  44   59  P
    404184A  DIGITAL IMAGE PROCESSING      PP 100  40  28    53   81  P C   404192D  WIRELESS NETWORKS            PP 100  40  29  54   83  P
    404185B  ELECTRONIC PRODUCT DESIGN     PP 100  40  23    49   72  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20           42  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20             41  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20           44  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20             37  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20           40  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20             38  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20           36  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20             47  P C   404195   PROJECT PHASE II             TW 100  40           95  P
    404188   PROJECT PHASE I               OR  50  20             43  P C   404195   PROJECT PHASE II             PR  50  20           41  P

  GRAND TOTAL = 1150/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053060   DHUMALE AJINKYA DNYANESHWAR           SUNITA              ,71633614G   ,PICT         ,B120053060

           SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  14    49   63  P C   404189   MOBILE COMMUNICATION         PP 100  40  17  46   63  P
    404182   COMPUTER NETWORKS             PP 100  40  26    40   66  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  24  40   64  P
    404183   MICROWAVE ENGINEERING         PP 100  40  19    40   59  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  22  45   67  P
    404184A  DIGITAL IMAGE PROCESSING      PP 100  40  20    53   73  P C   404192D  WIRELESS NETWORKS            PP 100  40  26  41   67  P
    404185C  PLCS AND AUTOMATION           PP 100  40  24    53   77  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20           40  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20             48  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20           35  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20             28  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20           30  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20             43  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20           35  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20             25  P C   404195   PROJECT PHASE II             TW 100  40           90  P
    404188   PROJECT PHASE I               OR  50  20             40  P C   404195   PROJECT PHASE II             PR  50  20           44  P

  GRAND TOTAL = 1057/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053061   DIKHALE SNEHAL SUBHASH              JAYSHREE            ,71527217K   ,PICT        ,B120053061

              SEM.:1                                                        SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 14    28    42  P C   404189   MOBILE COMMUNICATION          PP 100 40 21    31    52  P
      404182   COMPUTER NETWORKS            PP 100 40 27    49    76  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100 40 18    51    69  P
      404183   MICROWAVE ENGINEERING        PP 100 40 21    46    67  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100 40 18    37    55  P
      404184B  EMBEDDED SYSTEMS & RTOS      PP 100 40 23    44    67  P C   404192D  WIRELESS NETWORKS             PP 100 40 25    44    69  P
      404185D  ARTIFICIAL INTELLIGENCE      PP 100 40 21    46    67  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50 20            36  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50 20             39  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50 20            40  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50 20             29  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50 20            39  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20             35  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50 20            40  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20             39  P C   404195   PROJECT PHASE II              TW 100 40            96  P
      404188   PROJECT PHASE I              OR  50 20             44  P C   404195   PROJECT PHASE II              PR  50 20            46  P

   GRAND TOTAL = 1047/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053062   GADIYA RUSHABH KANTILAL             SUNITA             ,71633616C   ,PICT        ,B120053062

              SEM.:1                                                        SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 15    49    64  P C   404189   MOBILE COMMUNICATION          PP 100 40 25    45    70  P
      404182   COMPUTER NETWORKS            PP 100 40 29    44    73  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100 40 25    54    79  P
      404183   MICROWAVE ENGINEERING        PP 100 40 29    44    73  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100 40 16    44    60  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100 40 24    52    76  P C   404192D  WIRELESS NETWORKS             PP 100 40 27    49    76  P
      404185C  PLCS AND AUTOMATION          PP 100 40 25    48    73  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50 20            38  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50 20             48  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50 20            39  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50 20             40  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50 20            40  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20             41  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50 20            38  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20             42  P C   404195   PROJECT PHASE II              TW 100 40            87  P
      404188   PROJECT PHASE I              OR  50 20             37  P C   404195   PROJECT PHASE II              PR  50 20            40  P

   GRAND TOTAL = 1134/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053063   GAIKWAD SAMRUDDHI AVINASH           NILIMA             ,71527224B   ,PICT        ,B120053063

              SEM.:1                                                        SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 13    44    57  P     404189   MOBILE COMMUNICATION          PP 100 40 19    32    51  P
      404182   COMPUTER NETWORKS            PP 100 40 26    45    71  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100 40 06    42    48  P
      404183   MICROWAVE ENGINEERING        PP 100 40 22    35    57  P     404191C  AUDIO VIDEO ENGINEERING       PP 100 40 19    46    65  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100 40 22    34    56  P C   404192D  WIRELESS NETWORKS             PP 100 40 21    55    76  P
      404185B  ELECTRONIC PRODUCT DESIGN    PP 100 40 19    41    60  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50 20            35  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50 20             30  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50 20            35  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50 20             22  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50 20            35  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20             31  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50 20            39  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20             25  P C   404195   PROJECT PHASE II              TW 100 40            60  P
      404188   PROJECT PHASE I              OR  50 20             40  P C   404195   PROJECT PHASE II              PR  50 20            22  P

   GRAND TOTAL = 915/1500 , RESULT:FIRST CLASS
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   NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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      B120053064   GANGISHETTI SRAVAN   PADMA   ,71527226J   ,PICT   ,B120053064

              SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 18   48   66 P C   404189   MOBILE COMMUNICATION   PP 100 40 23   40   63 P
      404182   COMPUTER NETWORKS   PP 100 40 25   55   80 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 17   50   67 P
      404183   MICROWAVE ENGINEERING   PP 100 40 27   55   82 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 17   47   64 P
      404184B EMBEDDED SYSTEMS & RTOS   PP 100 40 27   42   69 P C   404192D WIRELESS NETWORKS   PP 100 40 29   53   82 P
      404185D ARTIFICIAL INTELLIGENCE   PP 100 40 26   46   72 P C   404193   LAB PRACTICE III(MC & BCS) TW 50 20   35 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   38 P C   404193   LAB PRACTICE III(MC & BCS) OR 50 20   39 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   40 P C   404194   LAB PRAC. IV(ELECTIVE III) TW 50 20   35 P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   38 P C   404194   LAB PRAC. IV(ELECTIVE III) PR 50 20   39 P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   42 P C   404195   PROJECT PHASE II   TW 100 40   95 P
      404188   PROJECT PHASE I   OR 50 20   46 P C   404195   PROJECT PHASE II   PR 50 20   45 P

   GRAND TOTAL = 1137/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053065   GANGRAS ANURAG ASHUTOSH   APARNA   ,71527227G   ,PICT   ,B120053065

              SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 11   30   41 P C   404189   MOBILE COMMUNICATION   PP 100 40 23   35   58 P
      404182   COMPUTER NETWORKS   PP 100 40 25   42   67 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 18   37   55 P
      404183   MICROWAVE ENGINEERING   PP 100 40 23   40   63 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 15   37   52 P
      404184A DIGITAL IMAGE PROCESSING   PP 100 40 22   49   71 P C   404192D WIRELESS NETWORKS   PP 100 40 26   43   69 P
      404185D ARTIFICIAL INTELLIGENCE   PP 100 40 15   32   47 P C   404193   LAB PRACTICE III(MC & BCS) TW 50 20   26 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   46 P C   404193   LAB PRACTICE III(MC & BCS) OR 50 20   42 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   39 P C   404194   LAB PRAC. IV(ELECTIVE III) TW 50 20   28 P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   30 P C   404194   LAB PRAC. IV(ELECTIVE III) PR 50 20   34 P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   30 P C   404195   PROJECT PHASE II   TW 100 40   86 P
      404188   PROJECT PHASE I   OR 50 20   35 P C   404195   PROJECT PHASE II   PR 50 20   40 P

   GRAND TOTAL = 959/1500 , RESULT:FIRST CLASS
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      B120053066   GARJE PRIYANKA BABASAHEB   SANGITA   ,71527231E   ,PICT   ,B120053066

              SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 19   51   70 P   404189   MOBILE COMMUNICATION   PP 100 40 24   38   62 P
      404182   COMPUTER NETWORKS   PP 100 40 19   36   55 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 18   37   55 P
      404183   MICROWAVE ENGINEERING   PP 100 40 16   46   62 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 17   49   66 P
      404184A DIGITAL IMAGE PROCESSING   PP 100 40 26   48   74 P C   404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 25   38   63 P
      404185C PLCS AND AUTOMATION   PP 100 40 21   35   56 P C   404193   LAB PRACTICE III(MC & BCS) TW 50 20   32 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   45 P C   404193   LAB PRACTICE III(MC & BCS) OR 50 20   38 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   25 P C   404194   LAB PRAC. IV(ELECTIVE III) TW 50 20   36 P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   35 P C   404194   LAB PRAC. IV(ELECTIVE III) PR 50 20   37 P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   30 P C   404195   PROJECT PHASE II   TW 100 40   91 P
      404188   PROJECT PHASE I   OR 50 20   42 P C   404195   PROJECT PHASE II   PR 50 20   45 P

   GRAND TOTAL = 1019 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION   [& O.163]
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053067   GAWANDE KARTIK DILIP                    SANDHYA             ,71527236F   ,PICT        ,B120053067

             SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 03    37$ 40  P         404189   MOBILE COMMUNICATION        PP 100 40 15    32  47  P
      404182   COMPUTER NETWORKS            PP 100 40 21    32  53  P C       404190   BROADBAND COMMUNICATION SYS. PP 100 40 08    34  42  P
      404183   MICROWAVE ENGINEERING        PP 100 40 01    43  44  P         404191C  AUDIO VIDEO ENGINEERING     PP 100 40 13    32  45  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100 40 09    34  43  P C       404192D  WIRELESS NETWORKS           PP 100 40 20    32  52  P
      404185C  PLCS AND AUTOMATION          PP 100 40 03    32  --  F         404193   LAB PRACTICE III(MC & BCS)  TW  50 20            37  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50 20           20  P C       404193   LAB PRACTICE III(MC & BCS)  OR  50 20            23  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50 20           35  P         404194   LAB PRAC. IV(ELECTIVE III)  TW  50 20            30  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20           28  P         404194   LAB PRAC. IV(ELECTIVE III)  PR  50 20            34  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20           35  P         404195   PROJECT PHASE II            TW 100 40            70  P
      404188   PROJECT PHASE I              OR  50 20           34  P C       404195   PROJECT PHASE II            PR  50 20            30  P

   GRAND TOTAL = --/1500 , RESULT:FAILS   [$ O.1]
                                                            RESULT RESERVED FOR BACKLOGS.
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      B120053068   GHADLING SNEHAL JAYWANT                 AASHA               ,71633617M   ,PICT        ,B120053068

             SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 15    43  58  P C       404189   MOBILE COMMUNICATION        PP 100 40 22    46  68  P
      404182   COMPUTER NETWORKS            PP 100 40 27    37  64  P C       404190   BROADBAND COMMUNICATION SYS. PP 100 40 17    36  53  P
      404183   MICROWAVE ENGINEERING        PP 100 40 24    37  61  P C       404191C  AUDIO VIDEO ENGINEERING     PP 100 40 23    39  62  P
      404184B  EMBEDDED SYSTEMS & RTOS      PP 100 40 18    45  63  P C       404192D  WIRELESS NETWORKS           PP 100 40 24    55  79  P
      404185C  PLCS AND AUTOMATION          PP 100 40 11    34  45  P C       404193   LAB PRACTICE III(MC & BCS)  TW  50 20            40  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50 20           36  P C       404193   LAB PRACTICE III(MC & BCS)  OR  50 20            37  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50 20           33  P C       404194   LAB PRAC. IV(ELECTIVE III)  TW  50 20            32  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20           44  P C       404194   LAB PRAC. IV(ELECTIVE III)  PR  50 20            40  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20           33  P C       404195   PROJECT PHASE II            TW 100 40            79  P
      404188   PROJECT PHASE I              OR  50 20           35  P C       404195   PROJECT PHASE II            PR  50 20            36  P

   GRAND TOTAL = 998/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053069   GUPTA SANKALP RAJESH                    SANDHYA             ,71527247M   ,PICT        ,B120053069

             SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 14    30  44  P C       404189   MOBILE COMMUNICATION        PP 100 40 20    30  50  P
      404182   COMPUTER NETWORKS            PP 100 40 20    31  51  P C       404190   BROADBAND COMMUNICATION SYS. PP 100 40 22    34  56  P
      404183   MICROWAVE ENGINEERING        PP 100 40 19    32  51  P C       404191C  AUDIO VIDEO ENGINEERING     PP 100 40 08    32$ 40  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100 40 25    51  76  P C       404192D  WIRELESS NETWORKS           PP 100 40 20    32  52  P
      404185C  PLCS AND AUTOMATION          PP 100 40 18    42  60  P C       404193   LAB PRACTICE III(MC & BCS)  TW  50 20            40  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50 20           44  P C       404193   LAB PRACTICE III(MC & BCS)  OR  50 20            32  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50 20           38  P C       404194   LAB PRAC. IV(ELECTIVE III)  TW  50 20            37  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20           38  P C       404194   LAB PRAC. IV(ELECTIVE III)  PR  50 20            33  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20           30  P C       404195   PROJECT PHASE II            TW 100 40            92  P
      404188   PROJECT PHASE I              OR  50 20           40  P C       404195   PROJECT PHASE II            PR  50 20            46  P

   GRAND TOTAL = 950/1500 , RESULT:FIRST CLASS   [$ O.1]
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      NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053070   GWALHERKAR KRUTIKA MANOJ                SANGITA              ,71527248K   ,PICT       ,B120053070

              SEM.:1                                                    SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  16    39   55  P C   404189   MOBILE COMMUNICATION          PP 100  40  21   35   56  P
      404182   COMPUTER NETWORKS             PP 100  40  23    48   71  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  19   48   67  P
      404183   MICROWAVE ENGINEERING         PP 100  40  23    52   75  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  20   35   55  P
      404184A  DIGITAL IMAGE PROCESSING      PP 100  40  19    47   66  P C   404192A  BIOMEDICAL SIGNAL PROCESSING  PP 100  40  23   46   69  P
      404185B  ELECTRONIC PRODUCT DESIGN     PP 100  40  20    42   62  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20             42  P
      404186   LAB PRACTICE I (CN & MWE)     TW  50  20             35  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20             40  P
      404186   LAB PRACTICE I (CN & MWE)     OR  50  20             40  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             30  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20             32  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             25  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20             34  P C   404195   PROJECT PHASE II              TW 100  40             78  P
      404188   PROJECT PHASE I               OR  50  20             33  P C   404195   PROJECT PHASE II              PR  50  20             33  P

   GRAND TOTAL = 998/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053071   HALYAL SHUBHAM RAJESH                    NEHA                 ,71527249H   ,PICT       ,B120053071

              SEM.:1                                                    SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  13    37   50  P C   404189   MOBILE COMMUNICATION          PP 100  40  17   43   60  P
      404182   COMPUTER NETWORKS             PP 100  40  23    41   64  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  17   43   60  P
      404183   MICROWAVE ENGINEERING         PP 100  40  14    41   55  P C   404191D  SOFT COMPUTING                PP 100  40  15   41   56  P
      404184A  DIGITAL IMAGE PROCESSING      PP 100  40  14    49   63  P C   404192D  WIRELESS NETWORKS             PP 100  40  21   40   61  P
      404185B  ELECTRONIC PRODUCT DESIGN     PP 100  40  16    50   66  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20             35  P
      404186   LAB PRACTICE I (CN & MWE)     TW  50  20             27  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20             40  P
      404186   LAB PRACTICE I (CN & MWE)     OR  50  20             32  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             30  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20             37  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             32  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20             44  P C   404195   PROJECT PHASE II              TW 100  40             94  P
      404188   PROJECT PHASE I               OR  50  20             38  P C   404195   PROJECT PHASE II              PR  50  20             44  P

   GRAND TOTAL = 988/1500 , RESULT:FIRST CLASS
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      B120053072   HEMANT KUMAR DUKRE                       REKHA                ,71527251K   ,PICT       ,B120053072

              SEM.:1                                                    SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  11    32   43  P C   404189   MOBILE COMMUNICATION          PP 100  40  22   43   65  P
      404182   COMPUTER NETWORKS             PP 100  40  23    54   77  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  21   46   67  P
      404183   MICROWAVE ENGINEERING         PP 100  40  15    37   52  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  16   35   51  P
      404184A  DIGITAL IMAGE PROCESSING      PP 100  40  20    49   69  P C   404192D  WIRELESS NETWORKS             PP 100  40  17   41   58  P
      404185C  PLCS AND AUTOMATION           PP 100  40  16    42   58  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20             43  P
      404186   LAB PRACTICE I (CN & MWE)     TW  50  20             47  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20             32  P
      404186   LAB PRACTICE I (CN & MWE)     OR  50  20             33  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             41  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20             42  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             37  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20             32  P C   404195   PROJECT PHASE II              TW 100  40             95  P
      404188   PROJECT PHASE I               OR  50  20             39  P C   404195   PROJECT PHASE II              PR  50  20             45  P

   GRAND TOTAL = 1026/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053073   INGALE RAVEENA RAVINDRA              RAMA                  ,71527255B   ,PICT        ,B120053073

             SEM.:1                                                           SEM.:2
      404181  VLSI DESIGN & TECHNOLOGY    PP 100  40  17     28   45  P C    404189  MOBILE COMMUNICATION         PP 100  40  21    37   58  P
      404182  COMPUTER NETWORKS           PP 100  40  21     50   71  P C    404190  BROADBAND COMMUNICATION SYS. PP 100  40  14    55   69  P
      404183  MICROWAVE ENGINEERING       PP 100  40  25     39   64  P C    404191C AUDIO VIDEO ENGINEERING      PP 100  40  24    47   71  P
      404184B EMBEDDED SYSTEMS & RTOS     PP 100  40  23     47   70  P C    404192D WIRELESS NETWORKS            PP 100  40  28    53   81  P
      404185D ARTIFICIAL INTELLIGENCE     PP 100  40  21     40   61  P C    404193  LAB PRACTICE III(MC & BCS)   TW  50  20             45  P
      404186  LAB PRACTICE I (CN & MWE)   TW  50  20              40  P C    404193  LAB PRACTICE III(MC & BCS)   OR  50  20             32  P
      404186  LAB PRACTICE I (CN & MWE)   OR  50  20              35  P C    404194  LAB PRAC. IV(ELECTIVE III)   TW  50  20             45  P
      404187  LAB PRAC. II(VLSI &ELEC. I) TW  50  20              42  P C    404194  LAB PRAC. IV(ELECTIVE III)   PR  50  20             40  P
      404187  LAB PRAC. II(VLSI &ELEC. I) PR  50  20              33  P C    404195  PROJECT PHASE II             TW 100  40             95  P
      404188  PROJECT PHASE I             OR  50  20              44  P C    404195  PROJECT PHASE II             PR  50  20             45  P

   GRAND TOTAL = 1086/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053074   ISHAN TRIPATHI                       UMA                   ,71527257J   ,PICT        ,B120053074

             SEM.:1                                                           SEM.:2
      404181  VLSI DESIGN & TECHNOLOGY    PP 100  40  09     34   43  P      404189  MOBILE COMMUNICATION         PP 100  40  16    38   54  P
      404182  COMPUTER NETWORKS           PP 100  40  18     32   50  P C    404190  BROADBAND COMMUNICATION SYS. PP 100  40  13    40   53  P
      404183  MICROWAVE ENGINEERING       PP 100  40  22     45   67  P C    404191C AUDIO VIDEO ENGINEERING      PP 100  40  12    37   49  P
      404184A DIGITAL IMAGE PROCESSING    PP 100  40  13     35   48  P C    404192A BIOMEDICAL SIGNAL PROCESSING PP 100  40  18    36   54  P
      404185C PLCS AND AUTOMATION         PP 100  40  15     29   44  P C    404193  LAB PRACTICE III(MC & BCS)   TW  50  20             33  P
      404186  LAB PRACTICE I (CN & MWE)   TW  50  20              33  P C    404193  LAB PRACTICE III(MC & BCS)   OR  50  20             25  P
      404186  LAB PRACTICE I (CN & MWE)   OR  50  20              35  P C    404194  LAB PRAC. IV(ELECTIVE III)   TW  50  20             34  P
      404187  LAB PRAC. II(VLSI &ELEC. I) TW  50  20              27  P C    404194  LAB PRAC. IV(ELECTIVE III)   PR  50  20             37  P
      404187  LAB PRAC. II(VLSI &ELEC. I) PR  50  20              25  P C    404195  PROJECT PHASE II             TW 100  40             75  P
      404188  PROJECT PHASE I             OR  50  20              31  P C    404195  PROJECT PHASE II             PR  50  20             33  P

   GRAND TOTAL = 850/1500 , RESULT:HIGHER SECOND CLASS
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      B120053075   JADHAV ASHRU BABAN                   ASHA                  ,71527258G   ,PICT        ,B120053075

             SEM.:1                                                           SEM.:2
      404181  VLSI DESIGN & TECHNOLOGY    PP 100  40  17     44   61  P C    404189  MOBILE COMMUNICATION         PP 100  40  23    49   72  P
      404182  COMPUTER NETWORKS           PP 100  40  29     52   81  P C    404190  BROADBAND COMMUNICATION SYS. PP 100  40  27    54   81  P
      404183  MICROWAVE ENGINEERING       PP 100  40  21     51   72  P C    404191C AUDIO VIDEO ENGINEERING      PP 100  40  22    42   64  P
      404184B EMBEDDED SYSTEMS & RTOS     PP 100  40  22     51   73  P C    404192D WIRELESS NETWORKS            PP 100  40  29    48   77  P
      404185B ELECTRONIC PRODUCT DESIGN   PP 100  40  23     39   62  P C    404193  LAB PRACTICE III(MC & BCS)   TW  50  20             44  P
      404186  LAB PRACTICE I (CN & MWE)   TW  50  20              47  P C    404193  LAB PRACTICE III(MC & BCS)   OR  50  20             32  P
      404186  LAB PRACTICE I (CN & MWE)   OR  50  20              32  P C    404194  LAB PRAC. IV(ELECTIVE III)   TW  50  20             40  P
      404187  LAB PRAC. II(VLSI &ELEC. I) TW  50  20              46  P C    404194  LAB PRAC. IV(ELECTIVE III)   PR  50  20             36  P
      404187  LAB PRAC. II(VLSI &ELEC. I) PR  50  20              40  P C    404195  PROJECT PHASE II             TW 100  40             90  P
      404188  PROJECT PHASE I             OR  50  20              44  P C    404195  PROJECT PHASE II             PR  50  20             40  P

   GRAND TOTAL = 1134/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
     OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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       B120053076   JADHAV PRASANNA YESHWANT                 POONAM                ,71527260J   ,PICT        ,B120053076

             SEM.:1                                                             SEM.:2
       404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  18    56    74  P C    404189   MOBILE COMMUNICATION          PP 100  40  22    50    72  P
       404182   COMPUTER NETWORKS            PP 100  40  27    56    83  P C    404190   BROADBAND COMMUNICATION SYS.  PP 100  40  26    53    79  P
       404183   MICROWAVE ENGINEERING        PP 100  40  25    48    73  P C    404191C  AUDIO VIDEO ENGINEERING       PP 100  40  15    47    62  P
       404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  21    51    72  P C    404192D  WIRELESS NETWORKS             PP 100  40  29    45    74  P
       404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  19    52    71  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50  20                46  P
       404186   LAB PRACTICE I (CN & MWE)    TW  50  20              37  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50  20                43  P
       404186   LAB PRACTICE I (CN & MWE)    OR  50  20              40  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                45  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              45  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                39  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              43  P C    404195   PROJECT PHASE II              TW 100  40                92  P
       404188   PROJECT PHASE I              OR  50  20              40  P C    404195   PROJECT PHASE II              PR  50  20                42  P

     GRAND TOTAL = 1172/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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       B120053077   JADHAV SANJIVANI PRABHAKAR               PHULABAI              ,71633618K   ,PICT        ,B120053077

             SEM.:1                                                             SEM.:2
       404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  15    51    66  P C    404189   MOBILE COMMUNICATION          PP 100  40  28    53    81  P
       404182   COMPUTER NETWORKS            PP 100  40  28    45    73  P C    404190   BROADBAND COMMUNICATION SYS.  PP 100  40  26    46    72  P
       404183   MICROWAVE ENGINEERING        PP 100  40  23    56    79  P C    404191C  AUDIO VIDEO ENGINEERING       PP 100  40  24    43    67  P
       404184A  DIGITAL IMAGE PROCESSING     PP 100  40  18    51    69  P C    404192D  WIRELESS NETWORKS             PP 100  40  28    42    70  P
       404185C  PLCS AND AUTOMATION          PP 100  40  22    53    75  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50  20                39  P
       404186   LAB PRACTICE I (CN & MWE)    TW  50  20              42  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50  20                41  P
       404186   LAB PRACTICE I (CN & MWE)    OR  50  20              38  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                45  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              38  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                38  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              38  P C    404195   PROJECT PHASE II              TW 100  40                84  P
       404188   PROJECT PHASE I              OR  50  20              37  P C    404195   PROJECT PHASE II              PR  50  20                42  P

     GRAND TOTAL = 1134/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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       B120053078   JAMBHALIKAR ADITYA RAVINDRA              SNEHA                 ,71633619H   ,PICT        ,B120053078

             SEM.:1                                                             SEM.:2
       404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  15    52    67  P C    404189   MOBILE COMMUNICATION          PP 100  40  22    40    62  P
       404182   COMPUTER NETWORKS            PP 100  40  24    59    83  P C    404190   BROADBAND COMMUNICATION SYS.  PP 100  40  24    48    72  P
       404183   MICROWAVE ENGINEERING        PP 100  40  26    47    73  P C    404191D  SOFT COMPUTING                PP 100  40  20    51    71  P
       404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  21    47    68  P C    404192D  WIRELESS NETWORKS             PP 100  40  24    42    66  P
       404185C  PLCS AND AUTOMATION          PP 100  40  21    53    74  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50  20                42  P
       404186   LAB PRACTICE I (CN & MWE)    TW  50  20              36  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50  20                44  P
       404186   LAB PRACTICE I (CN & MWE)    OR  50  20              40  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                40  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              44  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                38  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              40  P C    404195   PROJECT PHASE II              TW 100  40                89  P
       404188   PROJECT PHASE I              OR  50  20              43  P C    404195   PROJECT PHASE II              PR  50  20                41  P

     GRAND TOTAL = 1133/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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      B120053079   JAMBHULKAR SAHIL SIDDHARTH              CHHAYA              ,71527272B  ,PICT       ,B120053079

           SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 12    39   51  P C   404189   MOBILE COMMUNICATION          PP 100 40 19    47   66  P
      404182   COMPUTER NETWORKS            PP 100 40 24    32   56  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100 40 21    48   69  P
      404183   MICROWAVE ENGINEERING        PP 100 40 22    47   69  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100 40 19    39   58  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100 40 22    47   69  P C   404192D  WIRELESS NETWORKS             PP 100 40 24    40   64  P
      404185B  ELECTRONIC PRODUCT DESIGN    PP 100 40 17    41   58  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50 20              37  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50 20             35  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50 20              43  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50 20             39  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50 20              41  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20             37  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50 20              38  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20             40  P C   404195   PROJECT PHASE II              TW 100 40              85  P
      404188   PROJECT PHASE I              OR  50 20             44  P C   404195   PROJECT PHASE II              PR  50 20              40  P

   GRAND TOTAL = 1039/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053080   JIGISHA PATIL                           UJWALA              ,71527273L  ,PICT       ,B120053080

           SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 20    48   68  P C   404189   MOBILE COMMUNICATION          PP 100 40 23    52   75  P
      404182   COMPUTER NETWORKS            PP 100 40 28    52   80  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100 40 21    48   69  P
      404183   MICROWAVE ENGINEERING        PP 100 40 23    54   77  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100 40 22    42   64  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100 40 23    57   80  P C   404192D  WIRELESS NETWORKS             PP 100 40 28    40   68  P
      404185C  PLCS AND AUTOMATION          PP 100 40 20    38   58  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50 20              34  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50 20             33  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50 20              42  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50 20             37  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50 20              34  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20             33  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50 20              39  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20             45  P C   404195   PROJECT PHASE II              TW 100 40              95  P
      404188   PROJECT PHASE I              OR  50 20             41  P C   404195   PROJECT PHASE II              PR  50 20              45  P

   GRAND TOTAL = 1117/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053082   JOGDAND ANUJA TRYAMBAK                  PREMKALA            ,71633621K  ,PICT       ,B120053082

           SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 13    36   49  P C   404189   MOBILE COMMUNICATION          PP 100 40 22    45   67  P
      404182   COMPUTER NETWORKS            PP 100 40 25    44   69  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100 40 19    47   66  P
      404183   MICROWAVE ENGINEERING        PP 100 40 19    37   56  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100 40 27    42   69  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100 40 20    51   71  P C   404192D  WIRELESS NETWORKS             PP 100 40 19    38   57  P
      404185B  ELECTRONIC PRODUCT DESIGN    PP 100 40 22    37   59  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50 20              43  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50 20             41  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50 20              37  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50 20             39  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50 20              38  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20             41  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50 20              36  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20             35  P C   404195   PROJECT PHASE II              TW 100 40              91  P
      404188   PROJECT PHASE I              OR  50 20             38  P C   404195   PROJECT PHASE II              PR  50 20              43  P

   GRAND TOTAL = 1045/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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  NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
  OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053083   JOSHI BHAVESH DILIP                 SHARADA              ,71527274J   ,PICT        ,B120053083

          SEM.:1                                               SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  11    31   42  P C   404189   MOBILE COMMUNICATION          PP 100  40  19    43   62  P
    404182   COMPUTER NETWORKS             PP 100  40  26    36   62  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  23    44   67  P
    404183   MICROWAVE ENGINEERING         PP 100  40  18    36   54  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  22    42   64  P
    404184B  EMBEDDED SYSTEMS & RTOS       PP 100  40  12    41   53  P C   404192D  WIRELESS NETWORKS             PP 100  40  23    37   60  P
    404185C  PLCS AND AUTOMATION           PP 100  40  18    28   46  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              28  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20              34  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              25  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20              35  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              35  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              35  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              40  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              34  P C   404195   PROJECT PHASE II              TW 100  40              88  P
    404188   PROJECT PHASE I               OR  50  20              36  P C   404195   PROJECT PHASE II              PR  50  20              42  P

  GRAND TOTAL = 942/1500 , RESULT:FIRST CLASS
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    B120053084   JOSHI CHAITANYA SHRIKANT            SUNITA               ,71300979K   ,PICT        ,B120053084

          SEM.:1                                               SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  03    44   47  P     404189   MOBILE COMMUNICATION          PP 100  40  21    39   60  P
    404182   COMPUTER NETWORKS             PP 100  40  19    39   58  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  20    40   60  P
    404183   MICROWAVE ENGINEERING         PP 100  40  07    45   52  P     404191C  AUDIO VIDEO ENGINEERING       PP 100  40  15    38   53  P
    404184A  DIGITAL IMAGE PROCESSING      PP 100  40  08    36   44  P C   404192A  BIOMEDICAL SIGNAL PROCESSING  PP 100  40  22    39   61  P
    404185C  PLCS AND AUTOMATION           PP 100  40  05    40   45  P     404193   LAB PRACTICE III(MC & BCS)    TW  50  20              33  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20              38  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              25  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20              28  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              24  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              28  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              34  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              43  P C   404195   PROJECT PHASE II              TW 100  40              96  P
    404188   PROJECT PHASE I               OR  50  20              37  P C   404195   PROJECT PHASE II              PR  50  20              45  P

  GRAND TOTAL = 911/1500 , RESULT:FIRST CLASS
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    B120053085   JOSHI RADHA VINAY                   JYOTI                ,71527275G   ,PICT        ,B120053085

          SEM.:1                                               SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  08    33   41  P C   404189   MOBILE COMMUNICATION          PP 100  40  26    37   63  P
    404182   COMPUTER NETWORKS             PP 100  40  19    48   67  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  12    42   54  P
    404183   MICROWAVE ENGINEERING         PP 100  40  14    34   48  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  13    28   41  P
    404184B  EMBEDDED SYSTEMS & RTOS       PP 100  40  08    49   57  P C   404192D  WIRELESS NETWORKS             PP 100  40  25    33   58  P
    404185D  ARTIFICIAL INTELLIGENCE       PP 100  40  08    41   49  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              37  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20              37  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              24  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20              36  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              34  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              37  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              36  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              32  P C   404195   PROJECT PHASE II              TW 100  40              95  P
    404188   PROJECT PHASE I               OR  50  20              42  P C   404195   PROJECT PHASE II              PR  50  20              43  P

  GRAND TOTAL = 931/1500 , RESULT:FIRST CLASS
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    B120053086   JOSHI SHISHIR ASHISH                 PRATIBHA              ,71633622H   ,PICT        ,B120053086

            SEM.:1                                                         SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  12    30    42  P C  404189   MOBILE COMMUNICATION          PP 100  40  20    42   62  P
    404182   COMPUTER NETWORKS             PP 100  40  27    45    72  P C  404190   BROADBAND COMMUNICATION SYS.  PP 100  40  17    39   56  P
    404183   MICROWAVE ENGINEERING         PP 100  40  22    45    67  P C  404191D  SOFT COMPUTING                PP 100  40  15    35   50  P
    404184A  DIGITAL IMAGE PROCESSING      PP 100  40  14    53    67  P C  404192D  WIRELESS NETWORKS             PP 100  40  27    40   67  P
    404185D  ARTIFICIAL INTELLIGENCE       PP 100  40  10    42    52  P C  404193   LAB PRACTICE III(MC & BCS)    TW  50  20             42  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20             44  P C  404193   LAB PRACTICE III(MC & BCS)    OR  50  20             40  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20             43  P C  404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             26  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20             36  P C  404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             40  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20             40  P C  404195   PROJECT PHASE II              TW 100  40             95  P
    404188   PROJECT PHASE I               OR  50  20             43  P C  404195   PROJECT PHASE II              PR  50  20             45  P

 GRAND TOTAL = 1029/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053087   JUNAID S PATEL                       MEHNAZ               ,71527278M   ,PICT        ,B120053087

            SEM.:1                                                         SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  14    57    71  P C  404189   MOBILE COMMUNICATION          PP 100  40  13    38   51  P
    404182   COMPUTER NETWORKS             PP 100  40  22    39    61  P C  404190   BROADBAND COMMUNICATION SYS.  PP 100  40  09    44   53  P
    404183   MICROWAVE ENGINEERING         PP 100  40  09    42    51  P C  404191C  AUDIO VIDEO ENGINEERING       PP 100  40  24    39   63  P
    404184A  DIGITAL IMAGE PROCESSING      PP 100  40  19    50    69  P C  404192D  WIRELESS NETWORKS             PP 100  40  20    43   63  P
    404185C  PLCS AND AUTOMATION           PP 100  40  12    40    52  P C  404193   LAB PRACTICE III(MC & BCS)    TW  50  20             38  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20             37  P C  404193   LAB PRACTICE III(MC & BCS)    OR  50  20             39  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20             34  P C  404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             34  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20             35  P C  404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             35  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20             30  P C  404195   PROJECT PHASE II              TW 100  40             95  P
    404188   PROJECT PHASE I               OR  50  20             31  P C  404195   PROJECT PHASE II              PR  50  20             45  P

 GRAND TOTAL = 987/1500 , RESULT:FIRST CLASS
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    B120053088   KADAM SHIVANJALI RAJENDRA            SUNITA               ,71633623F   ,PICT        ,B120053088

            SEM.:1                                                         SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  16    51    67  P C  404189   MOBILE COMMUNICATION          PP 100  40  20    38   58  P
    404182   COMPUTER NETWORKS             PP 100  40  27    37    64  P C  404190   BROADBAND COMMUNICATION SYS.  PP 100  40  23    42   65  P
    404183   MICROWAVE ENGINEERING         PP 100  40  20    48    68  P C  404191C  AUDIO VIDEO ENGINEERING       PP 100  40  16    42   58  P
    404184A  DIGITAL IMAGE PROCESSING      PP 100  40  24    47    71  P C  404192D  WIRELESS NETWORKS             PP 100  40  25    38   63  P
    404185C  PLCS AND AUTOMATION           PP 100  40  23    32    55  P C  404193   LAB PRACTICE III(MC & BCS)    TW  50  20             41  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20             41  P C  404193   LAB PRACTICE III(MC & BCS)    OR  50  20             40  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20             45  P C  404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             39  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20             40  P C  404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             40  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20             38  P C  404195   PROJECT PHASE II              TW 100  40             96  P
    404188   PROJECT PHASE I               OR  50  20             45  P C  404195   PROJECT PHASE II              PR  50  20             47  P

 GRAND TOTAL = 1081/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
     OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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        B120053089   KADAM SHRADDHA SHALIGRAM            MANISHA            ,71633624D   ,PICT        ,B120053089

                SEM.:1                                                     SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  13    55    68  P C   404189   MOBILE COMMUNICATION         PP 100  40  25    47    72  P
        404182   COMPUTER NETWORKS           PP 100  40  26    48    74  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  25    55    80  P
        404183   MICROWAVE ENGINEERING       PP 100  40  20    52    72  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  26    50    76  P
        404184A  DIGITAL IMAGE PROCESSING    PP 100  40  14    54    68  P C   404192D  WIRELESS NETWORKS            PP 100  40  24    47    71  P
        404185D  ARTIFICIAL INTELLIGENCE     PP 100  40  18    48    66  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20                44  P
        404186   LAB PRACTICE I (CN & MWE)   TW  50  20             39  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20                34  P
        404186   LAB PRACTICE I (CN & MWE)   OR  50  20             36  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20                40  P
        404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             40  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20                39  P
        404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             40  P C   404195   PROJECT PHASE II             TW 100  40                88  P
        404188   PROJECT PHASE I             OR  50  20             40  P C   404195   PROJECT PHASE II             PR  50  20                40  P

     GRAND TOTAL = 1127/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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        B120053090   KAKADE ROSHANI DATTU                MINAKSHI           ,71633625B   ,PICT        ,B120053090

                SEM.:1                                                     SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  17    52    69  P C   404189   MOBILE COMMUNICATION         PP 100  40  23    47    70  P
        404182   COMPUTER NETWORKS           PP 100  40  19    44    63  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  26    51    77  P
        404183   MICROWAVE ENGINEERING       PP 100  40  09    55    64  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  23    46    69  P
        404184A  DIGITAL IMAGE PROCESSING    PP 100  40  16    49    65  P C   404192D  WIRELESS NETWORKS            PP 100  40  28    40    68  P
        404185C  PLCS AND AUTOMATION         PP 100  40  17    45    62  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20                40  P
        404186   LAB PRACTICE I (CN & MWE)   TW  50  20             41  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20                30  P
        404186   LAB PRACTICE I (CN & MWE)   OR  50  20             40  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20                32  P
        404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             35  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20                33  P
        404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             23  P C   404195   PROJECT PHASE II             TW 100  40                80  P
        404188   PROJECT PHASE I             OR  50  20             38  P C   404195   PROJECT PHASE II             PR  50  20                32  P

     GRAND TOTAL = 1031/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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        B120053091   KALE UMA PRAFULLCHANDRA             ARCHANA            ,71527284F   ,PICT        ,B120053091

                SEM.:1                                                     SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  17    61    78  P C   404189   MOBILE COMMUNICATION         PP 100  40  29    52    81  P
        404182   COMPUTER NETWORKS           PP 100  40  27    49    76  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  26    53    79  P
        404183   MICROWAVE ENGINEERING       PP 100  40  24    48    72  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  24    56    80  P
        404184A  DIGITAL IMAGE PROCESSING    PP 100  40  28    61    89  P C   404192D  WIRELESS NETWORKS            PP 100  40  29    47    76  P
        404185B  ELECTRONIC PRODUCT DESIGN   PP 100  40  24    46    70  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20                42  P
        404186   LAB PRACTICE I (CN & MWE)   TW  50  20             43  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20                44  P
        404186   LAB PRACTICE I (CN & MWE)   OR  50  20             40  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20                43  P
        404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             33  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20                40  P
        404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             46  P C   404195   PROJECT PHASE II             TW 100  40                96  P
        404188   PROJECT PHASE I             OR  50  20             44  P C   404195   PROJECT PHASE II             PR  50  20                47  P

     GRAND TOTAL = 1219/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,  SEAT NO.
      OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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      B120053092   KAMBLE KAJAL VIJAY                    GEETA              ,71633626L   ,PICT        ,B120053092

              SEM.:1                                                  SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  11    45   56  P C   404189   MOBILE COMMUNICATION         PP 100  40  22    42   64  P
      404182   COMPUTER NETWORKS           PP 100  40  25    36   61  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  28    43   71  P
      404183   MICROWAVE ENGINEERING       PP 100  40  21    45   66  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  20    43   63  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  12    42   54  P C   404192D  WIRELESS NETWORKS            PP 100  40  25    44   69  P
      404185C  PLCS AND AUTOMATION         PP 100  40  18    45   63  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              43  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              42  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              24  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              35  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              40  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              36  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              37  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              32  P C   404195   PROJECT PHASE II             TW 100  40              84  P
      404188   PROJECT PHASE I             OR  50  20              41  P C   404195   PROJECT PHASE II             PR  50  20              38  P

   GRAND TOTAL = 1019/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053093   KAMBLE PRAJAKTA SHRIMANT              SANGEETA           ,71527287L   ,PICT        ,B120053093

              SEM.:1                                                  SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  09    45   54  P C   404189   MOBILE COMMUNICATION         PP 100  40  17    30   47  P
      404182   COMPUTER NETWORKS           PP 100  40  20    33   53  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  22    28   50  P
      404183   MICROWAVE ENGINEERING       PP 100  40  16    32   48  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  13    37   50  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  15    39   54  P C   404192D  WIRELESS NETWORKS            PP 100  40  18    40   58  P
      404185B  ELECTRONIC PRODUCT DESIGN   PP 100  40  18    31   49  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              36  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              37  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              31  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              38  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              32  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              40  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              36  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              34  P C   404195   PROJECT PHASE II             TW 100  40              88  P
      404188   PROJECT PHASE I             OR  50  20              38  P C   404195   PROJECT PHASE II             PR  50  20              42  P

   GRAND TOTAL = 915/1500 , RESULT:FIRST CLASS
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      B120053094   KAMBLE RAJAT JEETENDRA                RESHMA             ,71527289G   ,PICT        ,B120053094

              SEM.:1                                                  SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  12    33   45  P C   404189   MOBILE COMMUNICATION         PP 100  40  22    40   62  P
      404182   COMPUTER NETWORKS           PP 100  40  18    35   53  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  25    48   73  P
      404183   MICROWAVE ENGINEERING       PP 100  40  15    41   56  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  14    40   54  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  21    43   64  P C   404192D  WIRELESS NETWORKS            PP 100  40  15    38   53  P
      404185C  PLCS AND AUTOMATION         PP 100  40  13    31   44  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              34  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              44  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              36  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              34  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              33  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              33  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              38  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              34  P C   404195   PROJECT PHASE II             TW 100  40              90  P
      404188   PROJECT PHASE I             OR  50  20              35  P C   404195   PROJECT PHASE II             PR  50  20              42  P

   GRAND TOTAL = 957/1500 , RESULT:FIRST CLASS
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053095   KANDGE MANDAR SANJAY                JYOTI                ,71527291J   ,PICT        ,B120053095

            SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  08    32    40  P C  404189   MOBILE COMMUNICATION          PP 100  40  13    33    46  P
    404182   COMPUTER NETWORKS           PP 100  40  19    28    47  P C  404190   BROADBAND COMMUNICATION SYS.  PP 100  40  23    45    68  P
    404183   MICROWAVE ENGINEERING       PP 100  40  13    33    46  P C  404191C  AUDIO VIDEO ENGINEERING       PP 100  40  13    39    52  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100  40  19    39    58  P C  404192A  BIOMEDICAL SIGNAL PROCESSING  PP 100  40  14    37    51  P
    404185B  ELECTRONIC PRODUCT DESIGN   PP 100  40  12    33    45  P C  404193   LAB PRACTICE III(MC & BCS)    TW  50  20              37  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20              20  P C  404193   LAB PRACTICE III(MC & BCS)    OR  50  20              24  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20              28  P C  404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              37  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              27  P    404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              40  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              34  P    404195   PROJECT PHASE II              TW 100  40              90  P
    404188   PROJECT PHASE I             OR  50  20              36  P C  404195   PROJECT PHASE II              PR  50  20              44  P

    GRAND TOTAL = 870/1500 , RESULT:HIGHER SECOND CLASS
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    B120053096   KANJIA ANERI PARESHKUMAR            SHAILA               ,71527293E   ,PICT        ,B120053096

            SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  12    59    71  P C  404189   MOBILE COMMUNICATION          PP 100  40  16    45    61  P
    404182   COMPUTER NETWORKS           PP 100  40  26    44    70  P C  404190   BROADBAND COMMUNICATION SYS.  PP 100  40  22    51    73  P
    404183   MICROWAVE ENGINEERING       PP 100  40  24    48    72  P C  404191C  AUDIO VIDEO ENGINEERING       PP 100  40  14    57    71  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100  40  18    58    76  P C  404192D  WIRELESS NETWORKS             PP 100  40  29    38    67  P
    404185B  ELECTRONIC PRODUCT DESIGN   PP 100  40  20    35    55  P C  404193   LAB PRACTICE III(MC & BCS)    TW  50  20              42  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20              43  P C  404193   LAB PRACTICE III(MC & BCS)    OR  50  20              29  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20              46  P C  404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              37  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              43  P C  404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              40  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              43  P C  404195   PROJECT PHASE II              TW 100  40              92  P
    404188   PROJECT PHASE I             OR  50  20              44  P C  404195   PROJECT PHASE II              PR  50  20              43  P

    GRAND TOTAL = 1118/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053097   KARAN JUNEJA                        NEELAM               ,71527296K   ,PICT        ,B120053097

            SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  09    49    58  P C  404189   MOBILE COMMUNICATION          PP 100  40  21    28$   49  P
    404182   COMPUTER NETWORKS           PP 100  40  28    28    56  P C  404190   BROADBAND COMMUNICATION SYS.  PP 100  40  22    31    53  P
    404183   MICROWAVE ENGINEERING       PP 100  40  11    45    56  P C  404191C  AUDIO VIDEO ENGINEERING       PP 100  40  18    45    63  P
    404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  16    42    58  P C  404192D  WIRELESS NETWORKS             PP 100  40  24    38    62  P
    404185D  ARTIFICIAL INTELLIGENCE     PP 100  40  13    31    44  P C  404193   LAB PRACTICE III(MC & BCS)    TW  50  20              34  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20              34  P C  404193   LAB PRACTICE III(MC & BCS)    OR  50  20              38  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20              40  P C  404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              30  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              32  P C  404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              36  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              34  P C  404195   PROJECT PHASE II              TW 100  40              96  P
    404188   PROJECT PHASE I             OR  50  20              48  P C  404195   PROJECT PHASE II              PR  50  20              47  P

    GRAND TOTAL = 968/1500 , RESULT:FIRST CLASS   [$ O.1]
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053098   KARPE ANJALI SHRIKANT                 ARCHANA            ,71633627J   ,PICT        ,B120053098

             SEM.:1                                                          SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  14    57    71  P C   404189   MOBILE COMMUNICATION          PP 100  40  25    44    69  P
     404182   COMPUTER NETWORKS            PP 100  40  29    40    69  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  22    51    73  P
     404183   MICROWAVE ENGINEERING        PP 100  40  21    44    65  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  27    56    83  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  17    55    72  P C   404192D  WIRELESS NETWORKS             PP 100  40  26    41    67  P
     404185C  PLCS AND AUTOMATION          PP 100  40  22    50    72  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20                43  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20            45  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20                24  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20            36  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                32  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20            42  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                38  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20            44  P C   404195   PROJECT PHASE II              TW 100  40                92  P
     404188   PROJECT PHASE I              OR  50  20            38  P C   404195   PROJECT PHASE II              PR  50  20                45  P

   GRAND TOTAL = 1120/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053099   KASTURE DEEPALI VINAYAK               MANISHA            ,71527302H   ,PICT        ,B120053099

             SEM.:1                                                          SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  13    47    60  P C   404189   MOBILE COMMUNICATION          PP 100  40  16    23#   39# P
     404182   COMPUTER NETWORKS            PP 100  40  22    32    54  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  19    30    49  P
     404183   MICROWAVE ENGINEERING        PP 100  40  05    44    49  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  22    42    64  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  16    44    60  P C   404192D  WIRELESS NETWORKS             PP 100  40  16    39    55  P
     404185C  PLCS AND AUTOMATION          PP 100  40  10    37    47  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20                40  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20            42  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20                29  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20            42  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                40  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20            41  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                38  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20            34  P C   404195   PROJECT PHASE II              TW 100  40                94  P
     404188   PROJECT PHASE I              OR  50  20            42  P C   404195   PROJECT PHASE II              PR  50  20                45  P

   GRAND TOTAL = 964/1500 , RESULT:FIRST CLASS   [# O.4]
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     B120053100   KATIKAR VIKRANT RAJEEV                SUNITA             ,71527305B   ,PICT        ,B120053100

             SEM.:1                                                          SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  17    60    77  P C   404189   MOBILE COMMUNICATION          PP 100  40  23    45    68  P
     404182   COMPUTER NETWORKS            PP 100  40  28    46    74  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  28    49    77  P
     404183   MICROWAVE ENGINEERING        PP 100  40  20    50    70  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  21    60    81  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  22    59    81  P C   404192D  WIRELESS NETWORKS             PP 100  40  29    47    76  P
     404185C  PLCS AND AUTOMATION          PP 100  40  16    50    66  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20                37  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20            37  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20                36  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20            42  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                42  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20            38  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                37  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20            38  P C   404195   PROJECT PHASE II              TW 100  40                85  P
     404188   PROJECT PHASE I              OR  50  20            45  P C   404195   PROJECT PHASE II              PR  50  20                40  P

   GRAND TOTAL = 1147/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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     B120053101   KAUSHIK DASS                        PAMY                ,71527306L   ,PICT        ,B120053101

              SEM.:1                                                          SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  16    50   66  P C   404189   MOBILE COMMUNICATION          PP 100  40  23    45   68  P
     404182   COMPUTER NETWORKS            PP 100  40  27    40   67  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  28    48   76  P
     404183   MICROWAVE ENGINEERING        PP 100  40  25    51   76  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  22    54   76  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  22    51   73  P C   404192A  BIOMEDICAL SIGNAL PROCESSING  PP 100  40  26    45   71  P
     404185C  PLCS AND AUTOMATION          PP 100  40  22    47   69  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              38  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              46  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              36  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              43  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              42  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              36  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              40  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              47  P C   404195   PROJECT PHASE II              TW 100  40              90  P
     404188   PROJECT PHASE I              OR  50  20              39  P C   404195   PROJECT PHASE II              PR  50  20              42  P

   GRAND TOTAL = 1141/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053103   KHADE YASHWANT BHASKAR              RESHMA              ,71527310J   ,PICT        ,B120053103

              SEM.:1                                                          SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  18    47   65  P C   404189   MOBILE COMMUNICATION          PP 100  40  25    38   63  P
     404182   COMPUTER NETWORKS            PP 100  40  25    32   57  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  28    54   82  P
     404183   MICROWAVE ENGINEERING        PP 100  40  20    38   58  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  20    52   72  P
     404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  20    46   66  P C   404192D  WIRELESS NETWORKS             PP 100  40  28    35   63  P
     404185C  PLCS AND AUTOMATION          PP 100  40  19    45   64  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              41  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              35  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              40  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              41  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              41  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              37  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              39  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              37  P C   404195   PROJECT PHASE II              TW 100  40              90  P
     404188   PROJECT PHASE I              OR  50  20              40  P C   404195   PROJECT PHASE II              PR  50  20              40  P

   GRAND TOTAL = 1071/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053104   KHAMAMKAR RACHANA NITIN             DEEPA              ,71527311G   ,PICT        ,B120053104

              SEM.:1                                                          SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  18    58   76  P C   404189   MOBILE COMMUNICATION          PP 100  40  20    56   76  P
     404182   COMPUTER NETWORKS            PP 100  40  27    43   70  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  24    56   80  P
     404183   MICROWAVE ENGINEERING        PP 100  40  25    45   70  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  23    61   84  P
     404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  24    60   84  P C   404192D  WIRELESS NETWORKS             PP 100  40  25    41   66  P
     404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  20    52   72  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              43  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              48  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              31  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              45  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              38  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              46  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              40  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              39  P C   404195   PROJECT PHASE II              TW 100  40              94  P
     404188   PROJECT PHASE I              OR  50  20              44  P C   404195   PROJECT PHASE II              PR  50  20              43  P

   GRAND TOTAL = 1189/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053105   KHATRI GEETA HARISH                  KAMLA              ,71527315K   ,PICT        ,B120053105

            SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  15    46   61  P C   404189   MOBILE COMMUNICATION        PP 100  40  15    50   65  P
     404182   COMPUTER NETWORKS           PP 100  40  24    39   63  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  27    51   78  P
     404183   MICROWAVE ENGINEERING       PP 100  40  16    48   64  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  18    47   65  P
     404184A  DIGITAL IMAGE PROCESSING    PP 100  40  25    38   63  P C   404192D  WIRELESS NETWORKS           PP 100  40  19    34   53  P
     404185C  PLCS AND AUTOMATION         PP 100  40  20    37   57  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20              40  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50  20              46  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20              40  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50  20              45  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              45  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              41  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              46  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              46  P C   404195   PROJECT PHASE II            TW 100  40              87  P
     404188   PROJECT PHASE I             OR  50  20              38  P C   404195   PROJECT PHASE II            PR  50  20              42  P

   GRAND TOTAL = 1085/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053106   KOKATE PUJA SURESH                   INDUBAI            ,71633628G   ,PICT        ,B120053106

            SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  13    53   66  P C   404189   MOBILE COMMUNICATION        PP 100  40  19    44   63  P
     404182   COMPUTER NETWORKS           PP 100  40  29    36   65  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  24    41   65  P
     404183   MICROWAVE ENGINEERING       PP 100  40  22    40   62  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  18    53   71  P
     404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  21    52   73  P C   404192D  WIRELESS NETWORKS           PP 100  40  25    39   64  P
     404185C  PLCS AND AUTOMATION         PP 100  40  21    43   64  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20              42  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50  20              40  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20              31  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50  20              40  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              36  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              40  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              40  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              31  P C   404195   PROJECT PHASE II            TW 100  40              85  P
     404188   PROJECT PHASE I             OR  50  20              41  P C   404195   PROJECT PHASE II            PR  50  20              40  P

   GRAND TOTAL = 1059/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053107   KOKATE VIJAY SUNIL                   VIMAL              ,71400955F   ,PICT        ,B120053107

            SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  12    49   61  P C   404189   MOBILE COMMUNICATION        PP 100  40  18    33   51  P
     404182   COMPUTER NETWORKS           PP 100  40  21    36   57  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  22    45   67  P
     404183   MICROWAVE ENGINEERING       PP 100  40  07    43   50  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  20    48   68  P
     404184A  DIGITAL IMAGE PROCESSING    PP 100  40  18    51   69  P C   404192A  BIOMEDICAL SIGNAL PROCESSING PP 100  40  21    47   68  P
     404185C  PLCS AND AUTOMATION         PP 100  40  20    42   62  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20              39  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50  20              44  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20              25  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50  20              32  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              32  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              39  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              38  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              35  P C   404195   PROJECT PHASE II            TW 100  40              82  P
     404188   PROJECT PHASE I             OR  50  20              42  P C   404195   PROJECT PHASE II            PR  50  20              40  P

   GRAND TOTAL = 1001/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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       B120053108   KONDE ATHARWA AJIT                    SANGEETA            ,71527324J   ,PICT       ,B120053108

              SEM.:1                                                        SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  05    39   44  P C   404189   MOBILE COMMUNICATION         PP 100  40  16    32   48  P
      404182   COMPUTER NETWORKS           PP 100  40  16    30   46  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  23    35   58  P
      404183   MICROWAVE ENGINEERING       PP 100  40  10    41   51  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  15    47   62  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  09    46   55  P C   404192A  BIOMEDICAL SIGNAL PROCESSING PP 100  40  19    37   56  P
      404185B  ELECTRONIC PRODUCT DESIGN   PP 100  40  15    31   46  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              42  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              47  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              25  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              30  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              38  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              44  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              38  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              25  P C   404195   PROJECT PHASE II             TW 100  40              94  P
      404188   PROJECT PHASE I             OR  50  20              40  P C   404195   PROJECT PHASE II             PR  50  20              43  P

    GRAND TOTAL = 932/1500 , RESULT:FIRST CLASS
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       B120053109   KONDE ESHWARA AJIT                    SANGEETA            ,71527325G   ,PICT       ,B120053109

              SEM.:1                                                        SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  15    42   57  P C   404189   MOBILE COMMUNICATION         PP 100  40  23    42   65  P
      404182   COMPUTER NETWORKS           PP 100  40  23    29   52  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  25    47   72  P
      404183   MICROWAVE ENGINEERING       PP 100  40  20    39   59  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  16    54   70  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  14    56   70  P C   404192A  BIOMEDICAL SIGNAL PROCESSING PP 100  40  26    42   68  P
      404185B  ELECTRONIC PRODUCT DESIGN   PP 100  40  19    34   53  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              42  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              38  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              28  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              40  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              39  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              44  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              40  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              35  P C   404195   PROJECT PHASE II             TW 100  40              94  P
      404188   PROJECT PHASE I             OR  50  20              41  P C   404195   PROJECT PHASE II             PR  50  20              44  P

    GRAND TOTAL = 1051/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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       B120053110   KOTHEKAR BHAKTI SUNIL                 CHITRA             ,71633629E   ,PICT       ,B120053110

              SEM.:1                                                        SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  17    54   71  P C   404189   MOBILE COMMUNICATION         PP 100  40  21    41   62  P
      404182   COMPUTER NETWORKS           PP 100  40  28    40   68  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  27    55   82  P
      404183   MICROWAVE ENGINEERING       PP 100  40  26    44   70  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  21    55   76  P
      404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  20    60   80  P C   404192D  WIRELESS NETWORKS            PP 100  40  27    45   72  P
      404185C  PLCS AND AUTOMATION         PP 100  40  16    45   61  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              40  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              36  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              42  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              45  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              38  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              41  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              43  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              40  P C   404195   PROJECT PHASE II             TW 100  40              94  P
      404188   PROJECT PHASE I             OR  50  20              43  P C   404195   PROJECT PHASE II             PR  50  20              44  P

    GRAND TOTAL = 1148/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053111   KULKARNI HARSH MAHESH                  SHILPA              ,71527335D   ,PICT        ,B120053111

              SEM.:1                                                           SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  07    48   55  P C   404189   MOBILE COMMUNICATION        PP 100  40  20    39   59  P
      404182   COMPUTER NETWORKS           PP 100  40  14    35   49  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  28    52   80  P
      404183   MICROWAVE ENGINEERING       PP 100  40  14    42   56  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  21    52   73  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  05    49   54  P C   404192D  WIRELESS NETWORKS           PP 100  40  23    47   70  P
      404185C  PLCS AND AUTOMATION         PP 100  40  15    51   66  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20             43  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20             38  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20             37  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20             32  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20             42  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             35  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20             40  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             46  P C   404195   PROJECT PHASE II            TW 100  40             78  P
      404188   PROJECT PHASE I             OR  50  20             35  P C   404195   PROJECT PHASE II            PR  50  20             35  P

      GRAND TOTAL = 1023/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053112   KULKARNI ISHA SHRIKANT                 MUKTA               ,71527336B   ,PICT        ,B120053112

              SEM.:1                                                           SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  14    57   71  P C   404189   MOBILE COMMUNICATION        PP 100  40  25    39   64  P
      404182   COMPUTER NETWORKS           PP 100  40  27    38   65  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  26    52   78  P
      404183   MICROWAVE ENGINEERING       PP 100  40  22    50   72  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  22    54   76  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  24    55   79  P C   404192D  WIRELESS NETWORKS           PP 100  40  25    44   69  P
      404185C  PLCS AND AUTOMATION         PP 100  40  24    51   75  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20             38  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20             37  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20             40  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20             40  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20             42  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             44  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20             40  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             47  P C   404195   PROJECT PHASE II            TW 100  40             93  P
      404188   PROJECT PHASE I             OR  50  20             40  P C   404195   PROJECT PHASE II            PR  50  20             44  P

      GRAND TOTAL = 1154/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053113   KULKARNI RIGVED RAJESH                 SAMPADA             ,71527340L   ,PICT        ,B120053113

              SEM.:1                                                           SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  12    49   61  P C   404189   MOBILE COMMUNICATION        PP 100  40  12    30   42  P
      404182   COMPUTER NETWORKS           PP 100  40  18    29   47  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  19    42   61  P
      404183   MICROWAVE ENGINEERING       PP 100  40  15    44   59  P C   404191D  SOFT COMPUTING              PP 100  40  16    36   52  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  17    38   55  P C   404192D  WIRELESS NETWORKS           PP 100  40  18    36   54  P
      404185C  PLCS AND AUTOMATION         PP 100  40  15    41   56  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20             40  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20             47  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20             41  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20             46  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20             43  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             33  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20             42  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             43  P C   404195   PROJECT PHASE II            TW 100  40             97  P
      404188   PROJECT PHASE I             OR  50  20             47  P C   404195   PROJECT PHASE II            PR  50  20             48  P

      GRAND TOTAL = 1014/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053114   KULKARNI SHEKHAR DIPAK              JYOTI                ,71633630J   ,PICT        ,B120053114

               SEM.:1                                                      SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  14    52    66  P C   404189   MOBILE COMMUNICATION          PP 100  40  13    39    52  P
      404182   COMPUTER NETWORKS            PP 100  40  27    38    65  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  14    46    60  P
      404183   MICROWAVE ENGINEERING        PP 100  40  23    46    69  P C   404191D  SOFT COMPUTING                PP 100  40  18    44    62  P
      404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  23    56    79  P C   404192D  WIRELESS NETWORKS             PP 100  40  23    39    62  P
      404185C  PLCS AND AUTOMATION          PP 100  40  19    50    69  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              42  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20              36  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              40  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              41  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              43  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              35  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              41  P C   404195   PROJECT PHASE II              TW 100  40              89  P
      404188   PROJECT PHASE I              OR  50  20              44  P C   404195   PROJECT PHASE II              PR  50  20              41  P

   GRAND TOTAL = 1078/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053115   KUMBHARKAR POOJA ASHOK              SWATI                ,71527344C   ,PICT        ,B120053115

               SEM.:1                                                      SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  18    54    72  P C   404189   MOBILE COMMUNICATION          PP 100  40  22    51    73  P
      404182   COMPUTER NETWORKS            PP 100  40  29    38    67  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  25    49    74  P
      404183   MICROWAVE ENGINEERING        PP 100  40  26    37    63  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  21    60    81  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100  40  21    51    72  P C   404192D  WIRELESS NETWORKS             PP 100  40  14    40    54  P
      404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  20    47    67  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              40  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20              37  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              41  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20              45  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              44  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              41  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              40  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              46  P C   404195   PROJECT PHASE II              TW 100  40              90  P
      404188   PROJECT PHASE I              OR  50  20              39  P C   404195   PROJECT PHASE II              PR  50  20              42  P

   GRAND TOTAL = 1128/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053116   KURANI ANKIT DEEPAK                 HEENA                ,71527345M   ,PICT        ,B120053116

               SEM.:1                                                      SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  17    60    77  P C   404189   MOBILE COMMUNICATION          PP 100  40  26    36    62  P
      404182   COMPUTER NETWORKS            PP 100  40  25    36    61  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  25    48    73  P
      404183   MICROWAVE ENGINEERING        PP 100  40  18    45    63  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  22    50    72  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100  40  22    56    78  P C   404192A  BIOMEDICAL SIGNAL PROCESSING  PP 100  40  26    44    70  P
      404185C  PLCS AND AUTOMATION          PP 100  40  27    47    74  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              34  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20              44  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              39  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              40  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              35  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              39  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              44  P C   404195   PROJECT PHASE II              TW 100  40              90  P
      404188   PROJECT PHASE I              OR  50  20              38  P C   404195   PROJECT PHASE II              PR  50  20              43  P

   GRAND TOTAL = 1118/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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        NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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        B120053117   KUTE SHUBHAM SUNIL                     JYOTI                ,71400979C   ,PICT         ,B120053117

                SEM.:1                                                           SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY       PP 100  40  06    40    46  P      404189   MOBILE COMMUNICATION           PP 100  40  25    16    --  F
        404182   COMPUTER NETWORKS              PP 100  40  13    32    45  P      404190   BROADBAND COMMUNICATION SYS.   PP 100  40  19    32    51  P
        404183   MICROWAVE ENGINEERING          PP 100  40  07    40    47  P      404191C  AUDIO VIDEO ENGINEERING        PP 100  40  06    44    50  P
        404184A  DIGITAL IMAGE PROCESSING       PP 100  40  12    29    41  P C    404192D  WIRELESS NETWORKS              PP 100  40  10    42    52  P
        404185C  PLCS AND AUTOMATION            PP 100  40  06    37    43  P      404193   LAB PRACTICE III(MC & BCS)     TW  50  20                20  P
        404186   LAB PRACTICE I (CN & MWE)      TW  50  20              23  P C    404193   LAB PRACTICE III(MC & BCS)     OR  50  20                22  P
        404186   LAB PRACTICE I (CN & MWE)      OR  50  20              35  P C    404194   LAB PRAC. IV(ELECTIVE III)     TW  50  20                24  P
        404187   LAB PRAC. II(VLSI &ELEC. I)    TW  50  20              21  P C    404194   LAB PRAC. IV(ELECTIVE III)     PR  50  20                32  P
        404187   LAB PRAC. II(VLSI &ELEC. I)    PR  50  20              28  P      404195   PROJECT PHASE II               TW 100  40                90  P
        404188   PROJECT PHASE I                OR  50  20              40  P C    404195   PROJECT PHASE II               PR  50  20                40  P

GRAND TOTAL = --/1500 , RESULT:FAILS
                                                                RESULT RESERVED FOR BACKLOGS.
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        B120053118   LAYKUMAR BHARAT PATEL                  KRISHNA              ,71527350H   ,PICT         ,B120053118

                SEM.:1                                                           SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY       PP 100  40  16    47    63  P C    404189   MOBILE COMMUNICATION           PP 100  40  23    44    67  P
        404182   COMPUTER NETWORKS              PP 100  40  27    34    61  P C    404190   BROADBAND COMMUNICATION SYS.   PP 100  40  26    45    71  P
        404183   MICROWAVE ENGINEERING          PP 100  40  23    44    67  P C    404191D  SOFT COMPUTING                 PP 100  40  18    45    63  P
        404184A  DIGITAL IMAGE PROCESSING       PP 100  40  19    55    74  P C    404192A  BIOMEDICAL SIGNAL PROCESSING   PP 100  40  25    43    68  P
        404185D  ARTIFICIAL INTELLIGENCE        PP 100  40  18    45    63  P C    404193   LAB PRACTICE III(MC & BCS)     TW  50  20                37  P
        404186   LAB PRACTICE I (CN & MWE)      TW  50  20              36  P C    404193   LAB PRACTICE III(MC & BCS)     OR  50  20                43  P
        404186   LAB PRACTICE I (CN & MWE)      OR  50  20              47  P C    404194   LAB PRAC. IV(ELECTIVE III)     TW  50  20                35  P
        404187   LAB PRAC. II(VLSI &ELEC. I)    TW  50  20              40  P C    404194   LAB PRAC. IV(ELECTIVE III)     PR  50  20                42  P
        404187   LAB PRAC. II(VLSI &ELEC. I)    PR  50  20              44  P C    404195   PROJECT PHASE II               TW 100  40                96  P
        404188   PROJECT PHASE I                OR  50  20              42  P C    404195   PROJECT PHASE II               PR  50  20                46  P

GRAND TOTAL = 1105/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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        B120053119   LIMAYE SWANAND RAGHUNATH               GAURI                ,71527351F   ,PICT         ,B120053119

                SEM.:1                                                           SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY       PP 100  40  21    57    78  P C    404189   MOBILE COMMUNICATION           PP 100  40  20    38    58  P
        404182   COMPUTER NETWORKS              PP 100  40  28    35    63  P C    404190   BROADBAND COMMUNICATION SYS.   PP 100  40  28    51    79  P
        404183   MICROWAVE ENGINEERING          PP 100  40  24    43    67  P C    404191C  AUDIO VIDEO ENGINEERING        PP 100  40  23    51    74  P
        404184B  EMBEDDED SYSTEMS & RTOS        PP 100  40  22    49    71  P C    404192D  WIRELESS NETWORKS              PP 100  40  28    39    67  P
        404185D  ARTIFICIAL INTELLIGENCE        PP 100  40  17    51    68  P C    404193   LAB PRACTICE III(MC & BCS)     TW  50  20                46  P
        404186   LAB PRACTICE I (CN & MWE)      TW  50  20              38  P C    404193   LAB PRACTICE III(MC & BCS)     OR  50  20                44  P
        404186   LAB PRACTICE I (CN & MWE)      OR  50  20              47  P C    404194   LAB PRAC. IV(ELECTIVE III)     TW  50  20                44  P
        404187   LAB PRAC. II(VLSI &ELEC. I)    TW  50  20              42  P C    404194   LAB PRAC. IV(ELECTIVE III)     PR  50  20                44  P
        404187   LAB PRAC. II(VLSI &ELEC. I)    PR  50  20              44  P C    404195   PROJECT PHASE II               TW 100  40                94  P
        404188   PROJECT PHASE I                OR  50  20              45  P C    404195   PROJECT PHASE II               PR  50  20                46  P

GRAND TOTAL = 1159/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053120   LOKHANDE HARSHVARDHAN RAJENDRA         SUJATA              ,71633632E   ,PICT        ,B120053120

            SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  16     53    69  P C   404189   MOBILE COMMUNICATION          PP 100  40  19     31    50  P
    404182   COMPUTER NETWORKS            PP 100  40  24     38    62  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  23     46    69  P
    404183   MICROWAVE ENGINEERING        PP 100  40  09     58    67  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  24     54    78  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  22     58    80  P C   404192D  WIRELESS NETWORKS             PP 100  40  28     47    75  P
    404185C  PLCS AND AUTOMATION          PP 100  40  23     55    78  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20                 38  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20                 38  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20                 44  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20                 42  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                 28  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20                 34  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                 36  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20                 31  P     404195   PROJECT PHASE II              TW 100  40                 90  P
    404188   PROJECT PHASE I              OR  50  20                 39  P C   404195   PROJECT PHASE II              PR  50  20                 42  P

  GRAND TOTAL = 1090/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053121   LONDHE KAJAL VIJAY                     NILAWATI            ,71633633C   ,PICT        ,B120053121

            SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  14     33    47  P C   404189   MOBILE COMMUNICATION          PP 100  40  15     44    59  P
    404182   COMPUTER NETWORKS            PP 100  40  24     36    60  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  20     43    63  P
    404183   MICROWAVE ENGINEERING        PP 100  40  15     35    50  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  19     53    72  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  20     52    72  P C   404192D  WIRELESS NETWORKS             PP 100  40  24     46    70  P
    404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  22     38    60  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20                 41  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20                 42  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20                 40  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20                 40  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                 34  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20                 37  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                 38  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20                 35  P C   404195   PROJECT PHASE II              TW 100  40                 90  P
    404188   PROJECT PHASE I              OR  50  20                 35  P C   404195   PROJECT PHASE II              PR  50  20                 38  P

  GRAND TOTAL = 1023/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053122   LONKAR AKASH MORESHWAR                 KALPANA             ,71633634M   ,PICT        ,B120053122

            SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  17     37    54  P C   404189   MOBILE COMMUNICATION          PP 100  40  22     29    51  P
    404182   COMPUTER NETWORKS            PP 100  40  29     49    78  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  28     45    73  P
    404183   MICROWAVE ENGINEERING        PP 100  40  19     52    71  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  20     57    77  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  24     56    80  P C   404192D  WIRELESS NETWORKS             PP 100  40  29     46    75  P
    404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  23     43    66  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20                 39  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20                 44  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20                 41  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20                 40  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20                 34  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20                 36  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20                 40  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20                 46  P C   404195   PROJECT PHASE II              TW 100  40                 95  P
    404188   PROJECT PHASE I              OR  50  20                 42  P C   404195   PROJECT PHASE II              PR  50  20                 40  P

  GRAND TOTAL = 1122/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,  SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    B120053123   LOUTE SHWETA SUNIL                    VANDANA              ,71400988B   ,PICT        ,B120053123

            SEM.:1                                                         SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  09    32    41  P      404189   MOBILE COMMUNICATION        PP 100  40  19    32    51  P
    404182   COMPUTER NETWORKS            PP 100  40  14    30    44  P C    404190   BROADBAND COMMUNICATION SYS. PP 100  40  10    31    41  P
    404183   MICROWAVE ENGINEERING        PP 100  40  05    40    45  P C    404191C  AUDIO VIDEO ENGINEERING     PP 100  40  16    44    60  P
    404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  19    28    47  P C    404192A  BIOMEDICAL SIGNAL PROCESSING PP 100  40  21    40    61  P
    404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  17    37    54  P C    404193   LAB PRACTICE III(MC & BCS)  TW  50  20                39  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20                36  P C    404193   LAB PRACTICE III(MC & BCS)  OR  50  20                42  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20                36  P C    404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20                33  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20                39  P C    404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20                38  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20                32  P C    404195   PROJECT PHASE II            TW 100  40                85  P
    404188   PROJECT PHASE I              OR  50  20                33  P C    404195   PROJECT PHASE II            PR  50  20                43  P

GRAND TOTAL = 900/1500 , RESULT:FIRST CLASS
                                                          RESULT RESERVED FOR BACKLOGS.
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    B120053124   LOYA TEJUS KAILASH                    TARIKA               ,71527356G   ,PICT        ,B120053124

            SEM.:1                                                         SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  10    34    44  P      404189   MOBILE COMMUNICATION        PP 100  40  09    35    44  P
    404182   COMPUTER NETWORKS            PP 100  40  17    31    48  P C    404190   BROADBAND COMMUNICATION SYS. PP 100  40  18    36    54  P
    404183   MICROWAVE ENGINEERING        PP 100  40  13    39    52  P      404191C  AUDIO VIDEO ENGINEERING     PP 100  40  14    46    60  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  13    33    46  P C    404192A  BIOMEDICAL SIGNAL PROCESSING PP 100  40  23    30    53  P
    404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  14    29    43  P C    404193   LAB PRACTICE III(MC & BCS)  TW  50  20                38  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20                42  P C    404193   LAB PRACTICE III(MC & BCS)  OR  50  20                44  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20                42  P C    404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20                31  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20                33  P C    404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20                36  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20                32  P C    404195   PROJECT PHASE II            TW 100  40                95  P
    404188   PROJECT PHASE I              OR  50  20                42  P C    404195   PROJECT PHASE II            PR  50  20                44  P

GRAND TOTAL = 923 + 10/1500 , RESULT:FIRST CLASS   [& O.163]
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    B120053125   LUNAWAT KARISHMA SANDEEP              VEENA                ,71527357E   ,PICT        ,B120053125

            SEM.:1                                                         SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  21    37    58  P C    404189   MOBILE COMMUNICATION        PP 100  40  27    47    74  P
    404182   COMPUTER NETWORKS            PP 100  40  28    46    74  P C    404190   BROADBAND COMMUNICATION SYS. PP 100  40  26    55    81  P
    404183   MICROWAVE ENGINEERING        PP 100  40  23    49    72  P C    404191C  AUDIO VIDEO ENGINEERING     PP 100  40  15    62    77  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  26    57    83  P C    404192A  BIOMEDICAL SIGNAL PROCESSING PP 100  40  28    58    86  P
    404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  24    50    74  P C    404193   LAB PRACTICE III(MC & BCS)  TW  50  20                37  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20                42  P C    404193   LAB PRACTICE III(MC & BCS)  OR  50  20                40  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20                40  P C    404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20                40  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20                32  P C    404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20                39  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20                38  P      404195   PROJECT PHASE II            TW 100  40                94  P
    404188   PROJECT PHASE I              OR  50  20                44  P C    404195   PROJECT PHASE II            PR  50  20                44  P

GRAND TOTAL = 1169/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053126   MACHALE SHREYASH KISHOR                VAISHLI                ,71527359M   ,PICT        ,B120053126

            SEM.:1                                                            SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  07    33$  40  P       404189   MOBILE COMMUNICATION        PP 100  40  12    35    47  P
    404182   COMPUTER NETWORKS            PP 100  40  26    35   61  P C     404190   BROADBAND COMMUNICATION SYS. PP 100  40  17    41    58  P
    404183   MICROWAVE ENGINEERING        PP 100  40  16    39   55  P C     404191C  AUDIO VIDEO ENGINEERING     PP 100  40  09    43    52  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  21    34   55  P C     404192D  WIRELESS NETWORKS           PP 100  40  22    40    62  P
    404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  17    33   50  P C     404193   LAB PRACTICE III(MC & BCS)  TW  50  20              35  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              37  P C     404193   LAB PRACTICE III(MC & BCS)  OR  50  20              38  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              24  P C     404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              24  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              30  P C     404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              36  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              35  P C     404195   PROJECT PHASE II            TW 100  40              50  P
    404188   PROJECT PHASE I              OR  50  20              31  P C     404195   PROJECT PHASE II            PR  50  20              25  P

GRAND TOTAL = 845/1500 , RESULT:HIGHER SECOND CLASS   [$ O.1]
                                                                RESULT RESERVED FOR BACKLOGS.
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    B120053127   MADHAVE AISHWARYA RAJENDRA             SWATI                  ,71539461E   ,PICT        ,B120053127

            SEM.:1                                                            SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  18    36   54  P C     404189   MOBILE COMMUNICATION        PP 100  40  16    48    64  P
    404182   COMPUTER NETWORKS            PP 100  40  23    43   66  P C     404190   BROADBAND COMMUNICATION SYS. PP 100  40  21    45    66  P
    404183   MICROWAVE ENGINEERING        PP 100  40  08    44   52  P C     404191C  AUDIO VIDEO ENGINEERING     PP 100  40  18    51    69  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  18    50   68  P C     404192D  WIRELESS NETWORKS           PP 100  40  25    45    70  P
    404185C  PLCS AND AUTOMATION          PP 100  40  25    37   62  P C     404193   LAB PRACTICE III(MC & BCS)  TW  50  20              38  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              41  P C     404193   LAB PRACTICE III(MC & BCS)  OR  50  20              35  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              35  P C     404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              24  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              33  P C     404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              38  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              34  P C     404195   PROJECT PHASE II            TW 100  40              88  P
    404188   PROJECT PHASE I              OR  50  20              38  P C     404195   PROJECT PHASE II            PR  50  20              42  P

GRAND TOTAL = 1017/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053128   MAHADIK PRADNYA PRAKASH                SUNANDA                ,71633635K   ,PICT        ,B120053128

            SEM.:1                                                            SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  13    32   45  P C     404189   MOBILE COMMUNICATION        PP 100  40  23    39    62  P
    404182   COMPUTER NETWORKS            PP 100  40  19    33   52  P C     404190   BROADBAND COMMUNICATION SYS. PP 100  40  16    45    61  P
    404183   MICROWAVE ENGINEERING        PP 100  40  17    50   67  P C     404191C  AUDIO VIDEO ENGINEERING     PP 100  40  18    56    74  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  20    57   77  P C     404192D  WIRELESS NETWORKS           PP 100  40  23    39    62  P
    404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  25    39   64  P C     404193   LAB PRACTICE III(MC & BCS)  TW  50  20              42  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              42  P C     404193   LAB PRACTICE III(MC & BCS)  OR  50  20              40  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              45  P C     404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              34  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              40  P C     404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              40  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              33  P       404195   PROJECT PHASE II            TW 100  40              90  P
    404188   PROJECT PHASE I              OR  50  20              37  P C     404195   PROJECT PHASE II            PR  50  20              38  P

GRAND TOTAL = 1045/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053129   MAHAJAN LEENA HUKUMCHAND            SADHANA             ,71633636H   ,PICT        ,B120053129

              SEM.:1                                                       SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  18    32    50  P C   404189   MOBILE COMMUNICATION           PP 100  40  21    49   70  P
     404182   COMPUTER NETWORKS            PP 100  40  29    46    75  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  19    47   66  P
     404183   MICROWAVE ENGINEERING        PP 100  40  19    52    71  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  27    56   83  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  22    59    81  P C   404192D  WIRELESS NETWORKS             PP 100  40  28    47   75  P
     404185C  PLCS AND AUTOMATION          PP 100  40  25    52    77  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              42  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              48  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              39  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              45  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              35  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              39  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              39  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              30  P C   404195   PROJECT PHASE II              TW 100  40              92  P
     404188   PROJECT PHASE I              OR  50  20              44  P C   404195   PROJECT PHASE II              PR  50  20              42  P

  GRAND TOTAL = 1143/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053130   MAHAJAN PRANJALI BINDUMADHAV        VIDYA               ,71527362M   ,PICT        ,B120053130

              SEM.:1                                                       SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  13    33    46  P C   404189   MOBILE COMMUNICATION           PP 100  40  22    40   62  P
     404182   COMPUTER NETWORKS            PP 100  40  27    42    69  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  21    52   73  P
     404183   MICROWAVE ENGINEERING        PP 100  40  23    48    71  P C   404191D  SOFT COMPUTING                PP 100  40  21    40   61  P
     404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  22    50    72  P C   404192D  WIRELESS NETWORKS             PP 100  40  28    48   76  P
     404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  20    45    65  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              38  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              38  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              40  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              41  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              30  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              43  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              32  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              40  P C   404195   PROJECT PHASE II              TW 100  40              95  P
     404188   PROJECT PHASE I              OR  50  20              37  P C   404195   PROJECT PHASE II              PR  50  20              43  P

  GRAND TOTAL = 1072/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053131   MAHAJAN SWAPNIL RAVINDRA            SUNITA              ,71400991B   ,PICT        ,B120053131

              SEM.:1                                                       SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  AA    AA    --  F     404189   MOBILE COMMUNICATION           PP 100  40  24    31   55  P
     404182   COMPUTER NETWORKS            PP 100  40  15    30    45  P     404190   BROADBAND COMMUNICATION SYS.  PP 100  40  20    36   56  P
     404183   MICROWAVE ENGINEERING        PP 100  40  AA    AA    --  F     404191C  AUDIO VIDEO ENGINEERING       PP 100  40  11    40   51  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  AA    AA    --  F     404192A  BIOMEDICAL SIGNAL PROCESSING  PP 100  40  20    40   60  P
     404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  13    34    47  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              30  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              35  P     404193   LAB PRACTICE III(MC & BCS)    OR  50  20              32  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              27  P     404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              24  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              27  P     404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              34  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              30  P     404195   PROJECT PHASE II              TW 100  40              65  P
     404188   PROJECT PHASE I              OR  50  20              35  P C   404195   PROJECT PHASE II              PR  50  20              35  P

  GRAND TOTAL = --/1500 , RESULT:FAILS    [& O.163]
                                                        RESULT RESERVED FOR BACKLOGS.
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053132   MAHAJAN TEJASWI RAJENDRA              VANDANA           ,71633637F   ,PICT        ,B120053132

            SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  19    47   66  P C   404189   MOBILE COMMUNICATION          PP 100  40  22    58   80  P
     404182   COMPUTER NETWORKS            PP 100  40  29    52   81  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  22    52   74  P
     404183   MICROWAVE ENGINEERING        PP 100  40  23    52   75  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  26    62   88  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  27    63   90  P C   404192D  WIRELESS NETWORKS             PP 100  40  28    49   77  P
     404185C  PLCS AND AUTOMATION          PP 100  40  25    58   83  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              43  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              48  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              41  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              35  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              41  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              45  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              42  P C   404195   PROJECT PHASE II              TW 100  40              90  P
     404188   PROJECT PHASE I              OR  50  20              44  P C   404195   PROJECT PHASE II              PR  50  20              41  P

  GRAND TOTAL = 1226/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053133   MAHIMA MUNDRA                         REKHA             ,71527363K   ,PICT        ,B120053133

            SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  11    30   41  P C   404189   MOBILE COMMUNICATION          PP 100  40  20    48   68  P
     404182   COMPUTER NETWORKS            PP 100  40  26    32   58  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  19    50   69  P
     404183   MICROWAVE ENGINEERING        PP 100  40  22    47   69  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  14    49   63  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  19    49   68  P C   404192D  WIRELESS NETWORKS             PP 100  40  22    45   67  P
     404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  17    43   60  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              36  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              40  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              43  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              36  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              35  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              40  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              39  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              35  P C   404195   PROJECT PHASE II              TW 100  40              93  P
     404188   PROJECT PHASE I              OR  50  20              41  P C   404195   PROJECT PHASE II              PR  50  20              45  P

  GRAND TOTAL = 1046/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053134   MAJI SHREYA SAMBHUNATH                CHANDANA          ,71527365F   ,PICT        ,B120053134

            SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  14    31   45  P C   404189   MOBILE COMMUNICATION          PP 100  40  23    43   66  P
     404182   COMPUTER NETWORKS            PP 100  40  26    39   65  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  17    54   71  P
     404183   MICROWAVE ENGINEERING        PP 100  40  11    46   57  P C   404191D  SOFT COMPUTING                PP 100  40  17    50   67  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  16    47   63  P C   404192D  WIRELESS NETWORKS             PP 100  40  25    44   69  P
     404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  26    46   72  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              21  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              38  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              22  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              38  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              29  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              33  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              41  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              38  P C   404195   PROJECT PHASE II              TW 100  40              92  P
     404188   PROJECT PHASE I              OR  50  20              42  P C   404195   PROJECT PHASE II              PR  50  20              44  P

  GRAND TOTAL = 1013/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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        B120053135   MANASI RAVINDRA JAGTAP                  BEBINANDA            ,71527371L   ,PICT        ,B120053135

              SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  09    36    45  P       404189   MOBILE COMMUNICATION            PP 100  40  22    32    54  P
      404182   COMPUTER NETWORKS            PP 100  40  18    32    50  P C     404190   BROADBAND COMMUNICATION SYS.    PP 100  40  16    32    48  P
      404183   MICROWAVE ENGINEERING        PP 100  40  12    32    44  P C     404191C  AUDIO VIDEO ENGINEERING         PP 100  40  12    37    49  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100  40  17    33    50  P C     404192D  WIRELESS NETWORKS               PP 100  40  13    38    51  P
      404185C  PLCS AND AUTOMATION          PP 100  40  07    28    35# P       404193   LAB PRACTICE III(MC & BCS)      TW  50  20                36  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20                32  P C   404193   LAB PRACTICE III(MC & BCS)      OR  50  20                42  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20                39  P C   404194   LAB PRAC. IV(ELECTIVE III)      TW  50  20                35  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20                28  P C   404194   LAB PRAC. IV(ELECTIVE III)      PR  50  20                38  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20                40  P C   404195   PROJECT PHASE II                TW 100  40                90  P
      404188   PROJECT PHASE I              OR  50  20                35  P C   404195   PROJECT PHASE II                PR  50  20                40  P

    GRAND TOTAL = 881/1500 , RESULT:HIGHER SECOND CLASS   [# O.4]
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        B120053136   MANDE SHIVANEE VISHWAS                  ANURADHA            ,71527372J   ,PICT        ,B120053136

              SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  11    31    42  P C     404189   MOBILE COMMUNICATION            PP 100  40  22    37    59  P
      404182   COMPUTER NETWORKS            PP 100  40  26    37    63  P C     404190   BROADBAND COMMUNICATION SYS.    PP 100  40  23    38    61  P
      404183   MICROWAVE ENGINEERING        PP 100  40  21    39    60  P C     404191C  AUDIO VIDEO ENGINEERING         PP 100  40  10    47    57  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100  40  23    53    76  P C     404192A  BIOMEDICAL SIGNAL PROCESSING    PP 100  40  20    46    66  P
      404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  20    45    65  P C     404193   LAB PRACTICE III(MC & BCS)      TW  50  20                42  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20                40  P C   404193   LAB PRACTICE III(MC & BCS)      OR  50  20                34  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20                38  P C   404194   LAB PRAC. IV(ELECTIVE III)      TW  50  20                43  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20                40  P C   404194   LAB PRAC. IV(ELECTIVE III)      PR  50  20                41  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20                34  P C   404195   PROJECT PHASE II                TW 100  40                94  P
      404188   PROJECT PHASE I              OR  50  20                35  P C   404195   PROJECT PHASE II                PR  50  20                44  P

    GRAND TOTAL = 1034 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION   [& O.163]
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        B120053137   MANDORA TEJAS SANDIP                    ARCHANA             ,71527374E   ,PICT        ,B120053137

              SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  14    28    42  P C     404189   MOBILE COMMUNICATION            PP 100  40  19    34    53  P
      404182   COMPUTER NETWORKS            PP 100  40  22    34    56  P C     404190   BROADBAND COMMUNICATION SYS.    PP 100  40  21    49    70  P
      404183   MICROWAVE ENGINEERING        PP 100  40  20    46    66  P C     404191C  AUDIO VIDEO ENGINEERING         PP 100  40  19    44    63  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100  40  24    50    74  P C     404192D  WIRELESS NETWORKS               PP 100  40  27    42    69  P
      404185C  PLCS AND AUTOMATION          PP 100  40  27    47    74  P C     404193   LAB PRACTICE III(MC & BCS)      TW  50  20                35  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20                39  P C   404193   LAB PRACTICE III(MC & BCS)      OR  50  20                34  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20                40  P C   404194   LAB PRAC. IV(ELECTIVE III)      TW  50  20                35  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20                41  P C   404194   LAB PRAC. IV(ELECTIVE III)      PR  50  20                40  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20                40  P C   404195   PROJECT PHASE II                TW 100  40                89  P
      404188   PROJECT PHASE I              OR  50  20                38  P C   404195   PROJECT PHASE II                PR  50  20                39  P

    GRAND TOTAL = 1037/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
     OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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        B120053138   MANGIRE SNEHAL VIHANG              SHAILA              ,71527376M   ,PICT        ,B120053138

                 SEM.:1                                                            SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 17    40   57  P C   404189   MOBILE COMMUNICATION           PP 100 40 19    40   59  P
        404182   COMPUTER NETWORKS            PP 100 40 29    52   81  P C   404190   BROADBAND COMMUNICATION SYS.   PP 100 40 24    56   80  P
        404183   MICROWAVE ENGINEERING        PP 100 40 27    50   77  P C   404191C  AUDIO VIDEO ENGINEERING        PP 100 40 20    52   72  P
        404184B  EMBEDDED SYSTEMS & RTOS      PP 100 40 22    59   81  P C   404192D  WIRELESS NETWORKS              PP 100 40 29    47   76  P
        404185D  ARTIFICIAL INTELLIGENCE      PP 100 40 25    50   75  P C   404193   LAB PRACTICE III(MC & BCS)     TW  50 20              37  P
        404186   LAB PRACTICE I (CN & MWE)    TW  50 20             37  P C   404193   LAB PRACTICE III(MC & BCS)     OR  50 20              34  P
        404186   LAB PRACTICE I (CN & MWE)    OR  50 20             41  P C   404194   LAB PRAC. IV(ELECTIVE III)     TW  50 20              42  P
        404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20             39  P C   404194   LAB PRAC. IV(ELECTIVE III)     PR  50 20              37  P
        404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20             34  P C   404195   PROJECT PHASE II               TW 100 40              93  P
        404188   PROJECT PHASE I              OR  50 20             44  P C   404195   PROJECT PHASE II               PR  50 20              43  P


     GRAND TOTAL = 1139/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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        B120053139   MANTRI NIKITA RAJESH               SAROJ               ,71527380K   ,PICT        ,B120053139

                 SEM.:1                                                            SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 13    39   52  P C   404189   MOBILE COMMUNICATION           PP 100 40 17    31   48  P
        404182   COMPUTER NETWORKS            PP 100 40 26    40   66  P C   404190   BROADBAND COMMUNICATION SYS.   PP 100 40 26    48   74  P
        404183   MICROWAVE ENGINEERING        PP 100 40 20    48   68  P C   404191C  AUDIO VIDEO ENGINEERING        PP 100 40 18    40   58  P
        404184A  DIGITAL IMAGE PROCESSING     PP 100 40 17    56   73  P C   404192D  WIRELESS NETWORKS              PP 100 40 22    48   70  P
        404185D  ARTIFICIAL INTELLIGENCE      PP 100 40 19    49   68  P C   404193   LAB PRACTICE III(MC & BCS)     TW  50 20              42  P
        404186   LAB PRACTICE I (CN & MWE)    TW  50 20             32  P C   404193   LAB PRACTICE III(MC & BCS)     OR  50 20              36  P
        404186   LAB PRACTICE I (CN & MWE)    OR  50 20             37  P C   404194   LAB PRAC. IV(ELECTIVE III)     TW  50 20              38  P
        404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20             32  P C   404194   LAB PRAC. IV(ELECTIVE III)     PR  50 20              39  P
        404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20             34  P C   404195   PROJECT PHASE II               TW 100 40              92  P
        404188   PROJECT PHASE I              OR  50 20             40  P C   404195   PROJECT PHASE II               PR  50 20              43  P


     GRAND TOTAL = 1042/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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        B120053140   MANTRI PRIYANKA SHRINIWAS          SANGEETA            ,71400997M   ,PICT        ,B120053140

                 SEM.:1                                                            SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 08    32   40  P     404189   MOBILE COMMUNICATION           PP 100 40 18    30   48  P
        404182   COMPUTER NETWORKS            PP 100 40 21    37   58  P     404190   BROADBAND COMMUNICATION SYS.   PP 100 40 11    40   51  P
        404183   MICROWAVE ENGINEERING        PP 100 40 20    32   52  P C   404191A  SPEECH & AUDIO SIGNAL PROC.    PP 100 40 AA    43   43  P
        404184B  EMBEDDED SYSTEMS & RTOS      PP 100 40 15    39   54  P C   404192A  BIOMEDICAL SIGNAL PROCESSING   PP 100 40 13    29   42  P
        404185B  ELECTRONIC PRODUCT DESIGN    PP 100 40 17    46   63  P     404193   LAB PRACTICE III(MC & BCS)     TW  50 20              37  P
        404186   LAB PRACTICE I (CN & MWE)    TW  50 20             35  P C   404193   LAB PRACTICE III(MC & BCS)     OR  50 20              32  P
        404186   LAB PRACTICE I (CN & MWE)    OR  50 20             33  P C   404194   LAB PRAC. IV(ELECTIVE III)     TW  50 20              30  P
        404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20             35  P C   404194   LAB PRAC. IV(ELECTIVE III)     PR  50 20              36  P
        404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20             35  P C   404195   PROJECT PHASE II               TW 100 40              84  P
        404188   PROJECT PHASE I              OR  50 20             32  P C   404195   PROJECT PHASE II               PR  50 20              42  P


     GRAND TOTAL = 882/1500 , RESULT:HIGHER SECOND CLASS
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     NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
     OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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      B120053141   MANTRI RUCHI VENUGOPAL               ARCHANA              ,71527381H   ,PICT         ,B120053141

               SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  18    31   49  P C   404189   MOBILE COMMUNICATION         PP 100  40  25    31   56  P
      404182   COMPUTER NETWORKS            PP 100  40  28    43   71  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  20    50   70  P
      404183   MICROWAVE ENGINEERING        PP 100  40  19    47   66  P C   404191D  SOFT COMPUTING               PP 100  40  24    52   76  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100  40  26    56   82  P C   404192D  WIRELESS NETWORKS            PP 100  40  24    43   67  P
      404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  17    59   76  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20             43  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20             41  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20             38  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20             40  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20             39  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20             41  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20             38  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20             45  P C   404195   PROJECT PHASE II             TW 100  40             91  P
      404188   PROJECT PHASE I              OR  50  20             42  P C   404195   PROJECT PHASE II             PR  50  20             41  P

   GRAND TOTAL = 1112/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053142   MANWAR PRASHIK KISHOR                SUJATA               ,71527383D   ,PICT         ,B120053142

               SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  13    29   42  P C   404189   MOBILE COMMUNICATION         PP 100  40  23    36   59  P
      404182   COMPUTER NETWORKS            PP 100  40  25    30   55  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  13    36   49  P
      404183   MICROWAVE ENGINEERING        PP 100  40  20    40   60  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  16    37   53  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100  40  18    41   59  P C   404192D  WIRELESS NETWORKS            PP 100  40  21    38   59  P
      404185C  PLCS AND AUTOMATION          PP 100  40  12    31   43  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20             39  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20             40  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20             40  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20             38  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20             41  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20             43  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20             39  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20             38  P C   404195   PROJECT PHASE II             TW 100  40             85  P
      404188   PROJECT PHASE I              OR  50  20             43  P C   404195   PROJECT PHASE II             PR  50  20             40  P

   GRAND TOTAL = 965/1500 , RESULT:FIRST CLASS
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      B120053143   MARKAD NIKHIL NANDKISHOR             KAMAL                ,71527384B   ,PICT         ,B120053143

               SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  14    33   47  P C   404189   MOBILE COMMUNICATION         PP 100  40  16    35   51  P
      404182   COMPUTER NETWORKS            PP 100  40  17    28   45  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  03    48   51  P
      404183   MICROWAVE ENGINEERING        PP 100  40  21    44   65  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  13    42   55  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100  40  21    48   69  P C   404192D  WIRELESS NETWORKS            PP 100  40  22    42   64  P
      404185C  PLCS AND AUTOMATION          PP 100  40  13    44   57  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20             34  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20             41  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20             28  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20             37  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20             24  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20             39  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20             38  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20             30  P C   404195   PROJECT PHASE II             TW 100  40             96  P
      404188   PROJECT PHASE I              OR  50  20             37  P C   404195   PROJECT PHASE II             PR  50  20             45  P

   GRAND TOTAL = 953 + 10/1500 , RESULT:FIRST CLASS    [& O.163]
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053144   MARLIWAR PRADHYUMNA ANIL              VIBHA              ,71527385L   ,PICT        ,B120053144

            SEM.:1                                                              SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  09    41    50  P       404189   MOBILE COMMUNICATION          PP 100  40  17    31    48  P
    404182   COMPUTER NETWORKS            PP 100  40  19    40    59  P C     404190   BROADBAND COMMUNICATION SYS.  PP 100  40  17    42    59  P
    404183   MICROWAVE ENGINEERING        PP 100  40  13    42    55  P C     404191D  SOFT COMPUTING                PP 100  40  13    51    64  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  13    52    65  P C     404192D  WIRELESS NETWORKS             PP 100  40  22    46    68  P
    404185C  PLCS AND AUTOMATION          PP 100  40  11    41    52  P C     404193   LAB PRACTICE III(MC & BCS)    TW  50  20              42  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              22  P C     404193   LAB PRACTICE III(MC & BCS)    OR  50  20              44  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              33  P C     404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              43  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              21  P C     404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              41  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              43  P       404195   PROJECT PHASE II              TW 100  40              94  P
    404188   PROJECT PHASE I              OR  50  20              43  P C     404195   PROJECT PHASE II              PR  50  20              45  P

  GRAND TOTAL = 991/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053145   MAYURI DNYANESHWAR MAHANDULE          TARA               ,71527389C   ,PICT        ,B120053145

            SEM.:1                                                              SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  03    37$   40  P       404189   MOBILE COMMUNICATION          PP 100  40  14    33    47  P
    404182   COMPUTER NETWORKS            PP 100  40  20    30    50  P C     404190   BROADBAND COMMUNICATION SYS.  PP 100  40  09    40    49  P
    404183   MICROWAVE ENGINEERING        PP 100  40  09    35    44  P C     404191D  SOFT COMPUTING                PP 100  40  11    45    56  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  17    47    64  P C     404192D  WIRELESS NETWORKS             PP 100  40  19    37    56  P
    404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  15    45    60  P C     404193   LAB PRACTICE III(MC & BCS)    TW  50  20              31  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              43  P C     404193   LAB PRACTICE III(MC & BCS)    OR  50  20              34  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              38  P C     404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              31  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              37  P C     404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              27  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              40  P C     404195   PROJECT PHASE II              TW 100  40              93  P
    404188   PROJECT PHASE I              OR  50  20              45  P C     404195   PROJECT PHASE II              PR  50  20              45  P

  GRAND TOTAL = 930/1500 , RESULT:FIRST CLASS   [$ O.1]
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    B120053146   MEHARE VAISHNAVI PRABHAKAR            VIJAYA             ,71527392C   ,PICT        ,B120053146

            SEM.:1                                                              SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  18    35    53  P C     404189   MOBILE COMMUNICATION          PP 100  40  29    42    71  P
    404182   COMPUTER NETWORKS            PP 100  40  29    32    61  P C     404190   BROADBAND COMMUNICATION SYS.  PP 100  40  26    48    74  P
    404183   MICROWAVE ENGINEERING        PP 100  40  24    51    75  P C     404191C  AUDIO VIDEO ENGINEERING       PP 100  40  19    47    66  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  22    60    82  P C     404192D  WIRELESS NETWORKS             PP 100  40  29    44    73  P
    404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  19    43    62  P C     404193   LAB PRACTICE III(MC & BCS)    TW  50  20              41  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              38  P C     404193   LAB PRACTICE III(MC & BCS)    OR  50  20              46  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              42  P C     404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              36  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              40  P C     404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              37  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              37  P C     404195   PROJECT PHASE II              TW 100  40              96  P
    404188   PROJECT PHASE I              OR  50  20              45  P C     404195   PROJECT PHASE II              PR  50  20              47  P

  GRAND TOTAL = 1122/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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  NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
  OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053147   MEHETRE PRIYANKA BHAUSAHEB           ANJALI                ,71527394K   ,PICT        ,B120053147

            SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  15    36   51  P C    404189   MOBILE COMMUNICATION          PP 100  40  23    53   76  P
    404182   COMPUTER NETWORKS            PP 100  40  29    41   70  P C    404190   BROADBAND COMMUNICATION SYS.  PP 100  40  19    56   75  P
    404183   MICROWAVE ENGINEERING        PP 100  40  17    52   69  P C    404191C  AUDIO VIDEO ENGINEERING       PP 100  40  14    47   61  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  23    52   75  P C    404192D  WIRELESS NETWORKS             PP 100  40  27    44   71  P
    404185C  PLCS AND AUTOMATION          PP 100  40  20    41   61  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50  20              47  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              41  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50  20              44  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              46  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              41  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              45  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              38  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              40  P C    404195   PROJECT PHASE II              TW 100  40              91  P
    404188   PROJECT PHASE I              OR  50  20              41  P C    404195   PROJECT PHASE II              PR  50  20              41  P

  GRAND TOTAL = 1124/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053148   MEHTA STUTI PUNIT                    PRITI                 ,71527396F   ,PICT        ,B120053148

            SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  18    45   63  P C    404189   MOBILE COMMUNICATION          PP 100  40  20    45   65  P
    404182   COMPUTER NETWORKS            PP 100  40  20    49   69  P C    404190   BROADBAND COMMUNICATION SYS.  PP 100  40  17    48   65  P
    404183   MICROWAVE ENGINEERING        PP 100  40  24    42   66  P C    404191D  SOFT COMPUTING                PP 100  40  14    51   65  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  18    57   75  P C    404192D  WIRELESS NETWORKS             PP 100  40  23    46   69  P
    404185C  PLCS AND AUTOMATION          PP 100  40  14    51   65  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50  20              39  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              32  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50  20              45  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              34  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              29  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              39  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              40  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              36  P C    404195   PROJECT PHASE II              TW 100  40              94  P
    404188   PROJECT PHASE I              OR  50  20              36  P C    404195   PROJECT PHASE II              PR  50  20              45  P

  GRAND TOTAL = 1071/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053149   MENGAJE PRANAY NILKANTH              VIMAL                 ,71527398B   ,PICT        ,B120053149

            SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  10    35   45  P C    404189   MOBILE COMMUNICATION          PP 100  40  20    35   55  P
    404182   COMPUTER NETWORKS            PP 100  40  21    34   55  P C    404190   BROADBAND COMMUNICATION SYS.  PP 100  40  15    47   62  P
    404183   MICROWAVE ENGINEERING        PP 100  40  17    45   62  P C    404191C  AUDIO VIDEO ENGINEERING       PP 100  40  12    41   53  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  22    39   61  P C    404192D  WIRELESS NETWORKS             PP 100  40  19    39   58  P
    404185C  PLCS AND AUTOMATION          PP 100  40  14    29   43  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50  20              26  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              40  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50  20              31  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              38  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              25  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              31  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              36  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              38  P C    404195   PROJECT PHASE II              TW 100  40              90  P
    404188   PROJECT PHASE I              OR  50  20              33  P C    404195   PROJECT PHASE II              PR  50  20              40  P

  GRAND TOTAL = 922/1500 , RESULT:FIRST CLASS
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053150   MHASKE SAURABH SUKUMAR                 FIONA                ,71527400H    ,PICT         ,B120053150

            SEM.:1                                                            SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  15    30   45  P C    404189   MOBILE COMMUNICATION          PP 100  40  17   33   50  P
     404182   COMPUTER NETWORKS            PP 100  40  21    43   64  P C    404190   BROADBAND COMMUNICATION SYS.  PP 100  40  17   45   62  P
     404183   MICROWAVE ENGINEERING        PP 100  40  15    38   53  P C    404191C  AUDIO VIDEO ENGINEERING       PP 100  40  18   45   63  P
     404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  15    47   62  P C    404192D  WIRELESS NETWORKS             PP 100  40  23   35   58  P
     404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  18    39   57  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50  20             39  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20             33  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50  20             40  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20             24  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             38  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20             32  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             38  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20             32  P C    404195   PROJECT PHASE II              TW 100  40             93  P
     404188   PROJECT PHASE I              OR  50  20             37  P C    404195   PROJECT PHASE II              PR  50  20             42  P

  GRAND TOTAL = 962/1500 , RESULT:FIRST CLASS
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     B120053151   MHATRE AJINKYA AVINASH                 SNEHAL               ,71401005H    ,PICT         ,B120053151

            SEM.:1                                                            SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  12    47   59  P C    404189   MOBILE COMMUNICATION          PP 100  40  19   41   60  P
     404182   COMPUTER NETWORKS            PP 100  40  20    33   53  P C    404190   BROADBAND COMMUNICATION SYS.  PP 100  40  06   48   54  P
     404183   MICROWAVE ENGINEERING        PP 100  40  13    44   57  P C    404191C  AUDIO VIDEO ENGINEERING       PP 100  40  23   44   67  P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  17    50   67  P C    404192D  WIRELESS NETWORKS             PP 100  40  24   43   67  P
     404185C  PLCS AND AUTOMATION          PP 100  40  15    44   59  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50  20             31  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20             22  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50  20             36  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20             33  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             26  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20             21  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             32  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20             25  P      404195   PROJECT PHASE II              TW 100  40             85  P
     404188   PROJECT PHASE I              OR  50  20             42  P C    404195   PROJECT PHASE II              PR  50  20             42  P

  GRAND TOTAL = 938/1500 , RESULT:FIRST CLASS
                                                         RESULT RESERVED FOR BACKLOGS.
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     B120053152   MHETRE SAYALI NARAYAN                  MEERA                ,71527401F    ,PICT         ,B120053152

            SEM.:1                                                            SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  19    62   81  P C    404189   MOBILE COMMUNICATION          PP 100  40  22   43   65  P
     404182   COMPUTER NETWORKS            PP 100  40  26    43   69  P C    404190   BROADBAND COMMUNICATION SYS.  PP 100  40  20   56   76  P
     404183   MICROWAVE ENGINEERING        PP 100  40  21    54   75  P C    404191C  AUDIO VIDEO ENGINEERING       PP 100  40  21   49   70  P
     404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  23    60   83  P C    404192D  WIRELESS NETWORKS             PP 100  40  29   55   84  P
     404185C  PLCS AND AUTOMATION          PP 100  40  27    51   78  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50  20             45  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20             36  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50  20             42  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20             45  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             40  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20             39  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             39  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20             43  P C    404195   PROJECT PHASE II              TW 100  40             91  P
     404188   PROJECT PHASE I              OR  50  20             42  P C    404195   PROJECT PHASE II              PR  50  20             41  P

  GRAND TOTAL = 1184 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION    [& O.163]
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  NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
  OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053153   MINCHANAL VISHVARADHYA SANGANNA       SHANTA              ,71527402D   ,PICT        ,B120053153

             SEM.:1                                                        SEM.:2
      404181  VLSI DESIGN & TECHNOLOGY      PP 100  40  12    56  68  P C   404189  MOBILE COMMUNICATION          PP 100  40  25   46   71  P
      404182  COMPUTER NETWORKS             PP 100  40  20    35  55  P C   404190  BROADBAND COMMUNICATION SYS.  PP 100  40  20   52   72  P
      404183  MICROWAVE ENGINEERING         PP 100  40  20    44  64  P C   404191D SOFT COMPUTING                PP 100  40  19   46   65  P
      404184A DIGITAL IMAGE PROCESSING      PP 100  40  22    61  83  P C   404192D WIRELESS NETWORKS             PP 100  40  28   52   80  P
      404185C PLCS AND AUTOMATION           PP 100  40  13    47  60  P C   404193  LAB PRACTICE III(MC & BCS)    TW  50  20            37  P
      404186  LAB PRACTICE I (CN & MWE)     TW  50  20            34  P C   404193  LAB PRACTICE III(MC & BCS)    OR  50  20            43  P
      404186  LAB PRACTICE I (CN & MWE)     OR  50  20            34  P C   404194  LAB PRAC. IV(ELECTIVE III)    TW  50  20            43  P
      404187  LAB PRAC. II(VLSI &ELEC. I)   TW  50  20            41  P C   404194  LAB PRAC. IV(ELECTIVE III)    PR  50  20            45  P
      404187  LAB PRAC. II(VLSI &ELEC. I)   PR  50  20            40  P C   404195  PROJECT PHASE II              TW 100  40            93  P
      404188  PROJECT PHASE I               OR  50  20            41  P C   404195  PROJECT PHASE II              PR  50  20            41  P


  GRAND TOTAL = 1110/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053154   MISHRA AYUSHI DEEPAK                  SHIPRA              ,71527404L   ,PICT        ,B120053154

             SEM.:1                                                        SEM.:2
      404181  VLSI DESIGN & TECHNOLOGY      PP 100  40  20    51  71  P C   404189  MOBILE COMMUNICATION          PP 100  40  26   59   85  P
      404182  COMPUTER NETWORKS             PP 100  40  23    36  59  P C   404190  BROADBAND COMMUNICATION SYS.  PP 100  40  26   53   79  P
      404183  MICROWAVE ENGINEERING         PP 100  40  26    51  77  P C   404191D SOFT COMPUTING                PP 100  40  24   49   73  P
      404184A DIGITAL IMAGE PROCESSING      PP 100  40  20    60  80  P C   404192D WIRELESS NETWORKS             PP 100  40  27   50   77  P
      404185C PLCS AND AUTOMATION           PP 100  40  20    51  71  P C   404193  LAB PRACTICE III(MC & BCS)    TW  50  20            45  P
      404186  LAB PRACTICE I (CN & MWE)     TW  50  20            40  P C   404193  LAB PRACTICE III(MC & BCS)    OR  50  20            40  P
      404186  LAB PRACTICE I (CN & MWE)     OR  50  20            42  P C   404194  LAB PRAC. IV(ELECTIVE III)    TW  50  20            48  P
      404187  LAB PRAC. II(VLSI &ELEC. I)   TW  50  20            43  P C   404194  LAB PRAC. IV(ELECTIVE III)    PR  50  20            46  P
      404187  LAB PRAC. II(VLSI &ELEC. I)   PR  50  20            35  P C   404195  PROJECT PHASE II              TW 100  40            85  P
      404188  PROJECT PHASE I               OR  50  20            43  P C   404195  PROJECT PHASE II              PR  50  20            40  P


  GRAND TOTAL = 1179/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053155   MISTARI TUSHAR VILAS                  JAYSHREE            ,71527405J   ,PICT        ,B120053155

             SEM.:1                                                        SEM.:2
      404181  VLSI DESIGN & TECHNOLOGY      PP 100  40  14    47  61  P C   404189  MOBILE COMMUNICATION          PP 100  40  21   41   62  P
      404182  COMPUTER NETWORKS             PP 100  40  17    29  46  P C   404190  BROADBAND COMMUNICATION SYS.  PP 100  40  15   47   62  P
      404183  MICROWAVE ENGINEERING         PP 100  40  17    40  57  P C   404191D SOFT COMPUTING                PP 100  40  15   42   57  P
      404184A DIGITAL IMAGE PROCESSING      PP 100  40  14    47  61  P C   404192D WIRELESS NETWORKS             PP 100  40  25   40   65  P
      404185C PLCS AND AUTOMATION           PP 100  40  22    39  61  P C   404193  LAB PRACTICE III(MC & BCS)    TW  50  20            36  P
      404186  LAB PRACTICE I (CN & MWE)     TW  50  20            38  P C   404193  LAB PRACTICE III(MC & BCS)    OR  50  20            42  P
      404186  LAB PRACTICE I (CN & MWE)     OR  50  20            27  P C   404194  LAB PRAC. IV(ELECTIVE III)    TW  50  20            31  P
      404187  LAB PRAC. II(VLSI &ELEC. I)   TW  50  20            38  P C   404194  LAB PRAC. IV(ELECTIVE III)    PR  50  20            40  P
      404187  LAB PRAC. II(VLSI &ELEC. I)   PR  50  20            29  P C   404195  PROJECT PHASE II              TW 100  40            92  P
      404188  PROJECT PHASE I               OR  50  20            33  P C   404195  PROJECT PHASE II              PR  50  20            46  P


  GRAND TOTAL = 984/1500 , RESULT:FIRST CLASS
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053157   MODOKAR SAYALI RAJESH                   VIDYA                ,71527408C   ,PICT        ,B120053157

            SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  18     48    66  P C   404189   MOBILE COMMUNICATION        PP 100  40  24    50    74  P
    404182   COMPUTER NETWORKS           PP 100  40  23     46    69  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  16    52    68  P
    404183   MICROWAVE ENGINEERING       PP 100  40  23     46    69  P C   404191D  SOFT COMPUTING              PP 100  40  19    49    68  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100  40  20     59    79  P C   404192D  WIRELESS NETWORKS           PP 100  40  29    52    81  P
    404185B  ELECTRONIC PRODUCT DESIGN   PP 100  40  21     53    74  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20                34  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20               37  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20                38  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20               42  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20                26  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20               28  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20                39  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20               24  P C   404195   PROJECT PHASE II            TW 100  40                91  P
    404188   PROJECT PHASE I             OR  50  20               36  P C   404195   PROJECT PHASE II            PR  50  20                42  P

 GRAND TOTAL = 1085/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053158   MRADUL BADOLE                           SUDHA                ,71527414H   ,PICT        ,B120053158

            SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  14     45    59  P C   404189   MOBILE COMMUNICATION        PP 100  40  20    34    54  P
    404182   COMPUTER NETWORKS           PP 100  40  24     33    57  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  21    47    68  P
    404183   MICROWAVE ENGINEERING       PP 100  40  19     39    58  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  08    42    50  P
    404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  16     40    56  P     404192D  WIRELESS NETWORKS           PP 100  40  26    33    59  P
    404185D  ARTIFICIAL INTELLIGENCE     PP 100  40  21     33    54  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20                40  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20               36  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20                37  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20               34  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20                36  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20               30  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20                38  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20               35  P C   404195   PROJECT PHASE II            TW 100  40                92  P
    404188   PROJECT PHASE I             OR  50  20               38  P C   404195   PROJECT PHASE II            PR  50  20                46  P

 GRAND TOTAL = 977/1500 , RESULT:FIRST CLASS
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     B120053159   MUGAVE GOPINATH SHRIDHAR                SANGITA              ,71401013J   ,PICT        ,B120053159

            SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  01     AA    --  F     404189   MOBILE COMMUNICATION        PP 100  40  15    31    46  P
    404182   COMPUTER NETWORKS           PP 100  40  08     32$   40  P     404190   BROADBAND COMMUNICATION SYS. PP 100  40  02    AA    --  F
    404183   MICROWAVE ENGINEERING       PP 100  40  09     17    --  F     404191C  AUDIO VIDEO ENGINEERING     PP 100  40  13    38    51  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100  40  02     AA    --  F     404192D  WIRELESS NETWORKS           PP 100  40  20    31    51  P
    404185B  ELECTRONIC PRODUCT DESIGN   PP 100  40  12     44    56  P     404193   LAB PRACTICE III(MC & BCS)  TW  50  20                21  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20               36  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20                25  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20               20  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20                24  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20               25  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20                33  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20               22  P     404195   PROJECT PHASE II            TW 100  40                42  P
    404188   PROJECT PHASE I             OR  50  20               26  P C   404195   PROJECT PHASE II            PR  50  20                22  P

 GRAND TOTAL = --/1500 , RESULT:FAILS   [$ O.1]
                                                         RESULT RESERVED FOR BACKLOGS.
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053160   NAGARE SWAPNALI SANJAY                    PUSHPA              ,71633639B   ,PICT        ,B120053160

             SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100 40 07    48   55  P C    404189   MOBILE COMMUNICATION           PP 100 40 20    53   73  P
      404182   COMPUTER NETWORKS             PP 100 40 24    45   69  P C    404190   BROADBAND COMMUNICATION SYS.   PP 100 40 27    49   76  P
      404183   MICROWAVE ENGINEERING         PP 100 40 21    55   76  P C    404191C  AUDIO VIDEO ENGINEERING        PP 100 40 23    48   71  P
      404184A  DIGITAL IMAGE PROCESSING      PP 100 40 24    57   81  P C    404192D  WIRELESS NETWORKS              PP 100 40 28    53   81  P
      404185B  ELECTRONIC PRODUCT DESIGN     PP 100 40 24    47   71  P C    404193   LAB PRACTICE III(MC & BCS)     TW  50 20             39  P
      404186   LAB PRACTICE I (CN & MWE)     TW  50 20             38  P C    404193   LAB PRACTICE III(MC & BCS)     OR  50 20             39  P
      404186   LAB PRACTICE I (CN & MWE)     OR  50 20             32  P C    404194   LAB PRAC. IV(ELECTIVE III)     TW  50 20             42  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50 20             34  P C    404194   LAB PRAC. IV(ELECTIVE III)     PR  50 20             36  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50 20             33  P C    404195   PROJECT PHASE II               TW 100 40             84  P
      404188   PROJECT PHASE I               OR  50 20             37  P C    404195   PROJECT PHASE II               PR  50 20             42  P

   GRAND TOTAL = 1109/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053161   NANDNAWARE AKASH JAGNNATH                 DRAUPADA            ,71100910E   ,PICT        ,B120053161

             SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100 40 13    36   49  P C    404189   MOBILE COMMUNICATION           PP 100 40 18    33   51  P
      404182   COMPUTER NETWORKS             PP 100 40 09    37   46  P      404190   BROADBAND COMMUNICATION SYS.   PP 100 40 15    42   57  P
      404183   MICROWAVE ENGINEERING         PP 100 40 12    42   54  P C    404191C  AUDIO VIDEO ENGINEERING        PP 100 40 14    45   59  P
      404184A  DIGITAL IMAGE PROCESSING      PP 100 40 19    43   62  P C    404192D  WIRELESS NETWORKS              PP 100 40 26    42   68  P
      404185C  PLCS AND AUTOMATION           PP 100 40 19    45   64  P C    404193   LAB PRACTICE III(MC & BCS)     TW  50 20             27  P
      404186   LAB PRACTICE I (CN & MWE)     TW  50 20             24  P C    404193   LAB PRACTICE III(MC & BCS)     OR  50 20             32  P
      404186   LAB PRACTICE I (CN & MWE)     OR  50 20             32  P C    404194   LAB PRAC. IV(ELECTIVE III)     TW  50 20             25  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50 20             21  P C    404194   LAB PRAC. IV(ELECTIVE III)     PR  50 20             30  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50 20             25  P      404195   PROJECT PHASE II               TW 100 40             92  P
      404188   PROJECT PHASE I               OR  50 20             46  P C    404195   PROJECT PHASE II               PR  50 20             46  P

   GRAND TOTAL = 910/1500 , RESULT:FIRST CLASS
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      B120053162   NANGARE AKSHAY ARUN                       ANITA              ,71527433D   ,PICT        ,B120053162

             SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100 40 09    35   44  P C    404189   MOBILE COMMUNICATION           PP 100 40 20    36   56  P
      404182   COMPUTER NETWORKS             PP 100 40 07    39   46  P      404190   BROADBAND COMMUNICATION SYS.   PP 100 40 22    33   55  P
      404183   MICROWAVE ENGINEERING         PP 100 40 10    32   42  P C    404191D  SOFT COMPUTING                 PP 100 40 18    29   47  P
      404184A  DIGITAL IMAGE PROCESSING      PP 100 40 21    34   55  P C    404192D  WIRELESS NETWORKS              PP 100 40 19    37   56  P
      404185C  PLCS AND AUTOMATION           PP 100 40 04    47   51  P C    404193   LAB PRACTICE III(MC & BCS)     TW  50 20             39  P
      404186   LAB PRACTICE I (CN & MWE)     TW  50 20             38  P C    404193   LAB PRACTICE III(MC & BCS)     OR  50 20             38  P
      404186   LAB PRACTICE I (CN & MWE)     OR  50 20             34  P C    404194   LAB PRAC. IV(ELECTIVE III)     TW  50 20             32  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50 20             38  P C    404194   LAB PRAC. IV(ELECTIVE III)     PR  50 20             37  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50 20             38  P C    404195   PROJECT PHASE II               TW 100 40             94  P
      404188   PROJECT PHASE I               OR  50 20             33  P C    404195   PROJECT PHASE II               PR  50 20             46  P

   GRAND TOTAL = 919/1500 , RESULT:FIRST CLASS
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053163   NATU HRUSHIKESH PRADIP                  SAVITA               ,71401032E   ,PICT        ,B120053163

              SEM.:1                                                        SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  17    36    53  P C    404189   MOBILE COMMUNICATION        PP 100  40  23    38    61  P
      404182   COMPUTER NETWORKS            PP 100  40  16    38    54  P      404190   BROADBAND COMMUNICATION SYS. PP 100  40  23    37    60  P
      404183   MICROWAVE ENGINEERING        PP 100  40  10    36    46  P C    404191C  AUDIO VIDEO ENGINEERING     PP 100  40  13    40    53  P
      404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  21    34    55  P C    404192D  WIRELESS NETWORKS           PP 100  40  28    37    65  P
      404185C  PLCS AND AUTOMATION          PP 100  40  19    32    51  P C    404193   LAB PRACTICE III(MC & BCS)  TW  50  20                29  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20              37  P      404193   LAB PRACTICE III(MC & BCS)  OR  50  20                30  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20              32  P      404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20                32  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              25  P      404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20                33  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              25  P      404195   PROJECT PHASE II            TW 100  40                96  P
      404188   PROJECT PHASE I              OR  50  20              38  P C    404195   PROJECT PHASE II            PR  50  20                45  P

   GRAND TOTAL = 920/1500 , RESULT:FIRST CLASS
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      B120053164   NAVRANGE ROHIT VILASRAO                  VAISHALI             ,71527437G   ,PICT        ,B120053164

              SEM.:1                                                        SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  17    34    51  P C    404189   MOBILE COMMUNICATION        PP 100  40  24    32    56  P
      404182   COMPUTER NETWORKS            PP 100  40  19    32    51  P C    404190   BROADBAND COMMUNICATION SYS. PP 100  40  16    46    62  P
      404183   MICROWAVE ENGINEERING        PP 100  40  20    42    62  P      404191C  AUDIO VIDEO ENGINEERING     PP 100  40  22    48    70  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100  40  15    52    67  P C    404192D  WIRELESS NETWORKS           PP 100  40  28    46    74  P
      404185C  PLCS AND AUTOMATION          PP 100  40  20    38    58  P C    404193   LAB PRACTICE III(MC & BCS)  TW  50  20                27  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20              40  P C    404193   LAB PRACTICE III(MC & BCS)  OR  50  20                38  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20              37  P C    404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20                30  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              28  P C    404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20                38  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              36  P C    404195   PROJECT PHASE II            TW 100  40                90  P
      404188   PROJECT PHASE I              OR  50  20              33  P C    404195   PROJECT PHASE II            PR  50  20                41  P

   GRAND TOTAL = 989/1500 , RESULT:FIRST CLASS
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      B120053165   NEEL PARAG PENDHARKAR                    ILA                  ,71527438E   ,PICT        ,B120053165

              SEM.:1                                                        SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  08    41    49  P C    404189   MOBILE COMMUNICATION        PP 100  40  21    36    57  P
      404182   COMPUTER NETWORKS            PP 100  40  20    28    48  P C    404190   BROADBAND COMMUNICATION SYS. PP 100  40  05    35$   40  P
      404183   MICROWAVE ENGINEERING        PP 100  40  09    33    42  P C    404191C  AUDIO VIDEO ENGINEERING     PP 100  40  13    38    51  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100  40  15    40    55  P C    404192D  WIRELESS NETWORKS           PP 100  40  20    36    56  P
      404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  14    35    49  P C    404193   LAB PRACTICE III(MC & BCS)  TW  50  20                34  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50  20              40  P C    404193   LAB PRACTICE III(MC & BCS)  OR  50  20                36  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50  20              36  P C    404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20                35  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              30  P C    404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20                37  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              41  P C    404195   PROJECT PHASE II            TW 100  40                93  P
      404188   PROJECT PHASE I              OR  50  20              42  P C    404195   PROJECT PHASE II            PR  50  20                44  P

   GRAND TOTAL = 915/1500 , RESULT:FIRST CLASS   [$ O.1]
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      NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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      B120053166   NEHA                          NILU                 ,71527439C   ,PICT        ,B120053166

              SEM.:1                                                   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  13    29    42  P C   404189   MOBILE COMMUNICATION         PP 100  40  21    30    51  P
      404182   COMPUTER NETWORKS           PP 100  40  15    28    43  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  08    28    --  F
      404183   MICROWAVE ENGINEERING       PP 100  40  17    32    49  P C   404191D  SOFT COMPUTING               PP 100  40  07    33$   40  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  12    42    54  P C   404192D  WIRELESS NETWORKS            PP 100  40  21    45    66  P
      404185B  ELECTRONIC PRODUCT DESIGN   PP 100  40  14    31    45  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              35  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              35  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              36  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              35  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              29  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              40  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              33  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              33  P C   404195   PROJECT PHASE II             TW 100  40              85  P
      404188   PROJECT PHASE I             OR  50  20              34  P C   404195   PROJECT PHASE II             PR  50  20              38  P

   GRAND TOTAL = --/1500 , RESULT:FAILS   [$ O.1]
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      B120053167   NIMBALKAR SNEHAL DIPAK        VANDANA              ,71633640F   ,PICT        ,B120053167

              SEM.:1                                                   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  14    40    54  P C   404189   MOBILE COMMUNICATION         PP 100  40  24    43    67  P
      404182   COMPUTER NETWORKS           PP 100  40  29    37    66  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  27    50    77  P
      404183   MICROWAVE ENGINEERING       PP 100  40  22    37    59  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  21    48    69  P
      404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  19    53    72  P C   404192D  WIRELESS NETWORKS            PP 100  40  29    48    77  P
      404185C  PLCS AND AUTOMATION         PP 100  40  25    41    66  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              39  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              38  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              43  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              39  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              24  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              35  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              38  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              39  P C   404195   PROJECT PHASE II             TW 100  40              80  P
      404188   PROJECT PHASE I             OR  50  20              35  P C   404195   PROJECT PHASE II             PR  50  20              34  P

   GRAND TOTAL = 1051/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053168   NIMJE PIYUSH MADAN            RANJANA              ,71527445H   ,PICT        ,B120053168

              SEM.:1                                                   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  17    58    75  P C   404189   MOBILE COMMUNICATION         PP 100  40  26    55    81  P
      404182   COMPUTER NETWORKS           PP 100  40  29    42    71  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  25    52    77  P
      404183   MICROWAVE ENGINEERING       PP 100  40  21    50    71  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  23    47    70  P
      404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  24    59    83  P C   404192D  WIRELESS NETWORKS            PP 100  40  29    51    80  P
      404185C  PLCS AND AUTOMATION         PP 100  40  12    53    65  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              45  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              48  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              35  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              32  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              40  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              39  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              35  P C   404195   PROJECT PHASE II             TW 100  40              87  P
      404188   PROJECT PHASE I             OR  50  20              42  P C   404195   PROJECT PHASE II             PR  50  20              40  P

   GRAND TOTAL = 1158/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053169   NITNAWARE VISHWAJIT MADANLAL           NIRMALA              ,71633641D   ,PICT        ,B120053169

             SEM.:1                                                           SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  14    41  55  P C   404189   MOBILE COMMUNICATION         PP 100  40  17   29   46  P
      404182   COMPUTER NETWORKS             PP 100  40  17    28  45  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  11   32   43  P
      404183   MICROWAVE ENGINEERING         PP 100  40  18    33  51  P     404191C  AUDIO VIDEO ENGINEERING      PP 100  40  22   37   59  P
      404184A  DIGITAL IMAGE PROCESSING      PP 100  40  08    32$ 40  P     404192D  WIRELESS NETWORKS            PP 100  40  23   36   59  P
      404185C  PLCS AND AUTOMATION           PP 100  40  16    28  44  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20            37  P
      404186   LAB PRACTICE I (CN & MWE)     TW  50  20            40  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20            33  P
      404186   LAB PRACTICE I (CN & MWE)     OR  50  20            20  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20            30  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20            39  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20            40  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20            33  P C   404195   PROJECT PHASE II             TW 100  40            89  P
      404188   PROJECT PHASE I               OR  50  20            36  P C   404195   PROJECT PHASE II             PR  50  20            42  P

   GRAND TOTAL = 881/1500 , RESULT:HIGHER SECOND CLASS   [$ O.1]
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      B120053170   OSWAL NEHUL VIKRAM                     MEENA                ,71527449L   ,PICT        ,B120053170

             SEM.:1                                                           SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  18    56  74  P C   404189   MOBILE COMMUNICATION         PP 100  40  27   48   75  P
      404182   COMPUTER NETWORKS             PP 100  40  29    29  58  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  27   56   83  P
      404183   MICROWAVE ENGINEERING         PP 100  40  20    51  71  P C   404191D  SOFT COMPUTING               PP 100  40  13   52   65  P
      404184B  EMBEDDED SYSTEMS & RTOS       PP 100  40  21    51  72  P C   404192D  WIRELESS NETWORKS            PP 100  40  23   49   72  P
      404185C  PLCS AND AUTOMATION           PP 100  40  13    32  45  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20            42  P
      404186   LAB PRACTICE I (CN & MWE)     TW  50  20            35  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20            42  P
      404186   LAB PRACTICE I (CN & MWE)     OR  50  20            32  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20            39  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20            38  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20            42  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20            41  P C   404195   PROJECT PHASE II             TW 100  40            95  P
      404188   PROJECT PHASE I               OR  50  20            40  P C   404195   PROJECT PHASE II             PR  50  20            45  P

   GRAND TOTAL = 1106/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053171   OSWAL RUSHABHKUMAR RAJESHKUMAR         NEETA                ,71527450D   ,PICT        ,B120053171

             SEM.:1                                                           SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  18    49  67  P C   404189   MOBILE COMMUNICATION         PP 100  40  24   32   56  P
      404182   COMPUTER NETWORKS             PP 100  40  27    40  67  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  23   43   66  P
      404183   MICROWAVE ENGINEERING         PP 100  40  22    38  60  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  21   45   66  P
      404184B  EMBEDDED SYSTEMS & RTOS       PP 100  40  21    57  78  P C   404192D  WIRELESS NETWORKS            PP 100  40  29   47   76  P
      404185C  PLCS AND AUTOMATION           PP 100  40  25    44  69  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20            35  P
      404186   LAB PRACTICE I (CN & MWE)     TW  50  20            38  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20            42  P
      404186   LAB PRACTICE I (CN & MWE)     OR  50  20            31  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20            42  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20            33  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20            39  P
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20            41  P C   404195   PROJECT PHASE II             TW 100  40            95  P
      404188   PROJECT PHASE I               OR  50  20            45  P C   404195   PROJECT PHASE II             PR  50  20            46  P

   GRAND TOTAL = 1092/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,   PREVIOUS SEAT NO.,   COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053172   PAHADE ABHIJEETKUMAR VIJAY              HEMLATA              ,71401047C   ,PICT         ,B120053172

          SEM.:1                                                      SEM.:2
    404181  VLSI DESIGN & TECHNOLOGY    PP 100 40 09    41   50  P C    404189  MOBILE COMMUNICATION         PP 100 40 17    33   50  P
    404182  COMPUTER NETWORKS           PP 100 40 13    38   51  P      404190  BROADBAND COMMUNICATION SYS. PP 100 40 19    29   48  P
    404183  MICROWAVE ENGINEERING       PP 100 40 14    28   42  P C    404191C AUDIO VIDEO ENGINEERING      PP 100 40 11    40   51  P
    404184A DIGITAL IMAGE PROCESSING    PP 100 40 10    36   46  P C    404192D WIRELESS NETWORKS            PP 100 40 25    41   66  P
    404185C PLCS AND AUTOMATION         PP 100 40 23    28   51  P C    404193  LAB PRACTICE III(MC & BCS)   TW  50 20              35  P
    404186  LAB PRACTICE I (CN & MWE)   TW  50 20              22  P C    404193  LAB PRACTICE III(MC & BCS)   OR  50 20              40  P
    404186  LAB PRACTICE I (CN & MWE)   OR  50 20              32  P C    404194  LAB PRAC. IV(ELECTIVE III)   TW  50 20              24  P
    404187  LAB PRAC. II(VLSI &ELEC. I) TW  50 20              20  P C    404194  LAB PRAC. IV(ELECTIVE III)   PR  50 20              40  P
    404187  LAB PRAC. II(VLSI &ELEC. I) PR  50 20              33  P      404195  PROJECT PHASE II             TW 100 40              85  P
    404188  PROJECT PHASE I             OR  50 20              32  P C    404195  PROJECT PHASE II             PR  50 20              43  P

GRAND TOTAL = 861/1500 , RESULT:HIGHER SECOND CLASS
                                                    RESULT RESERVED FOR BACKLOGS.
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    B120053173   PANDAGALE SHRUTI JAIWANT               ANITA                ,71527456C   ,PICT         ,B120053173

          SEM.:1                                                      SEM.:2
    404181  VLSI DESIGN & TECHNOLOGY    PP 100 40 15    61   76  P C    404189  MOBILE COMMUNICATION         PP 100 40 26    43   69  P
    404182  COMPUTER NETWORKS           PP 100 40 18    44   62  P C    404190  BROADBAND COMMUNICATION SYS. PP 100 40 20    54   74  P
    404183  MICROWAVE ENGINEERING       PP 100 40 27    39   66  P C    404191C AUDIO VIDEO ENGINEERING      PP 100 40 23    45   68  P
    404184A DIGITAL IMAGE PROCESSING    PP 100 40 18    44   62  P C    404192D WIRELESS NETWORKS            PP 100 40 29    48   77  P
    404185C PLCS AND AUTOMATION         PP 100 40 18    44   62  P C    404193  LAB PRACTICE III(MC & BCS)   TW  50 20              28  P
    404186  LAB PRACTICE I (CN & MWE)   TW  50 20              39  P C    404193  LAB PRACTICE III(MC & BCS)   OR  50 20              40  P
    404186  LAB PRACTICE I (CN & MWE)   OR  50 20              34  P C    404194  LAB PRAC. IV(ELECTIVE III)   TW  50 20              31  P
    404187  LAB PRAC. II(VLSI &ELEC. I) TW  50 20              34  P C    404194  LAB PRAC. IV(ELECTIVE III)   PR  50 20              40  P
    404187  LAB PRAC. II(VLSI &ELEC. I) PR  50 20              36  P C    404195  PROJECT PHASE II             TW 100 40              87  P
    404188  PROJECT PHASE I             OR  50 20              40  P C    404195  PROJECT PHASE II             PR  50 20              41  P

GRAND TOTAL = 1066/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053174   PANDE ADITI UMAKANT                    ANJALI               ,71527458K   ,PICT         ,B120053174

          SEM.:1                                                      SEM.:2
    404181  VLSI DESIGN & TECHNOLOGY    PP 100 40 16    43   59  P C    404189  MOBILE COMMUNICATION         PP 100 40 22    31   53  P
    404182  COMPUTER NETWORKS           PP 100 40 26    39   65  P C    404190  BROADBAND COMMUNICATION SYS. PP 100 40 19    48   67  P
    404183  MICROWAVE ENGINEERING       PP 100 40 20    44   64  P C    404191D SOFT COMPUTING               PP 100 40 22    40   62  P
    404184A DIGITAL IMAGE PROCESSING    PP 100 40 24    61   85  P C    404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 29    45   74  P
    404185D ARTIFICIAL INTELLIGENCE     PP 100 40 20    53   73  P C    404193  LAB PRACTICE III(MC & BCS)   TW  50 20              37  P
    404186  LAB PRACTICE I (CN & MWE)   TW  50 20              38  P C    404193  LAB PRACTICE III(MC & BCS)   OR  50 20              40  P
    404186  LAB PRACTICE I (CN & MWE)   OR  50 20              43  P C    404194  LAB PRAC. IV(ELECTIVE III)   TW  50 20              36  P
    404187  LAB PRAC. II(VLSI &ELEC. I) TW  50 20              40  P C    404194  LAB PRAC. IV(ELECTIVE III)   PR  50 20              42  P
    404187  LAB PRAC. II(VLSI &ELEC. I) PR  50 20              36  P C    404195  PROJECT PHASE II             TW 100 40              96  P
    404188  PROJECT PHASE I             OR  50 20              43  P C    404195  PROJECT PHASE II             PR  50 20              46  P

GRAND TOTAL = 1099/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
     OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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       B120053175   PARATE DEVYANI VIJAY                 VANDANA             ,71527461K   ,PICT        ,B120053175

             SEM.:1                                                          SEM.:2
       404181  VLSI DESIGN & TECHNOLOGY       PP 100  40  13    40   53  P C   404189  MOBILE COMMUNICATION          PP 100  40  24   44   68  P
       404182  COMPUTER NETWORKS              PP 100  40  27    37   64  P C   404190  BROADBAND COMMUNICATION SYS.  PP 100  40  20   53   73  P
       404183  MICROWAVE ENGINEERING          PP 100  40  25    49   74  P C   404191C AUDIO VIDEO ENGINEERING       PP 100  40  21   47   68  P
       404184B EMBEDDED SYSTEMS & RTOS        PP 100  40  25    52   77  P C   404192D WIRELESS NETWORKS             PP 100  40  29   45   74  P
       404185C PLCS AND AUTOMATION            PP 100  40  18    43   61  P C   404193  LAB PRACTICE III(MC & BCS)    TW  50  20             36  P
       404186  LAB PRACTICE I (CN & MWE)      TW  50  20             37  P C   404193  LAB PRACTICE III(MC & BCS)    OR  50  20             46  P
       404186  LAB PRACTICE I (CN & MWE)      OR  50  20             44  P C   404194  LAB PRAC. IV(ELECTIVE III)    TW  50  20             40  P
       404187  LAB PRAC. II(VLSI &ELEC. I)    TW  50  20             41  P C   404194  LAB PRAC. IV(ELECTIVE III)    PR  50  20             38  P
       404187  LAB PRAC. II(VLSI &ELEC. I)    PR  50  20             41  P C   404195  PROJECT PHASE II              TW 100  40             94  P
       404188  PROJECT PHASE I                OR  50  20             42  P C   404195  PROJECT PHASE II              PR  50  20             44  P

     GRAND TOTAL = 1115/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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       B120053176   PARDESHI ABHIJEET VIJAYSINGH         ARCHANA             ,71633643L   ,PICT        ,B120053176

             SEM.:1                                                          SEM.:2
       404181  VLSI DESIGN & TECHNOLOGY       PP 100  40  18    54   72  P C   404189  MOBILE COMMUNICATION          PP 100  40  21   35   56  P
       404182  COMPUTER NETWORKS              PP 100  40  27    34   61  P C   404190  BROADBAND COMMUNICATION SYS.  PP 100  40  22   41   63  P
       404183  MICROWAVE ENGINEERING          PP 100  40  23    40   63  P C   404191C AUDIO VIDEO ENGINEERING       PP 100  40  24   41   65  P
       404184A DIGITAL IMAGE PROCESSING       PP 100  40  13    58   71  P C   404192D WIRELESS NETWORKS             PP 100  40  29   42   71  P
       404185C PLCS AND AUTOMATION            PP 100  40  19    36   55  P C   404193  LAB PRACTICE III(MC & BCS)    TW  50  20             40  P
       404186  LAB PRACTICE I (CN & MWE)      TW  50  20             40  P C   404193  LAB PRACTICE III(MC & BCS)    OR  50  20             46  P
       404186  LAB PRACTICE I (CN & MWE)      OR  50  20             43  P C   404194  LAB PRAC. IV(ELECTIVE III)    TW  50  20             40  P
       404187  LAB PRAC. II(VLSI &ELEC. I)    TW  50  20             40  P C   404194  LAB PRAC. IV(ELECTIVE III)    PR  50  20             36  P
       404187  LAB PRAC. II(VLSI &ELEC. I)    PR  50  20             28  P     404195  PROJECT PHASE II              TW 100  40             93  P
       404188  PROJECT PHASE I                OR  50  20             38  P C   404195  PROJECT PHASE II              PR  50  20             45  P

     GRAND TOTAL = 1066/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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       B120053177   PARDESHI SHUBHAM DINESH              DEEPIKA             ,71633644J   ,PICT        ,B120053177

             SEM.:1                                                          SEM.:2
       404181  VLSI DESIGN & TECHNOLOGY       PP 100  40  15    47   62  P C   404189  MOBILE COMMUNICATION          PP 100  40  22   36   58  P
       404182  COMPUTER NETWORKS              PP 100  40  17    40   57  P C   404190  BROADBAND COMMUNICATION SYS.  PP 100  40  22   53   75  P
       404183  MICROWAVE ENGINEERING          PP 100  40  24    44   68  P C   404191C AUDIO VIDEO ENGINEERING       PP 100  40  21   46   67  P
       404184A DIGITAL IMAGE PROCESSING       PP 100  40  14    64   78  P C   404192D WIRELESS NETWORKS             PP 100  40  27   40   67  P
       404185C PLCS AND AUTOMATION            PP 100  40  18    46   64  P C   404193  LAB PRACTICE III(MC & BCS)    TW  50  20             38  P
       404186  LAB PRACTICE I (CN & MWE)      TW  50  20             43  P C   404193  LAB PRACTICE III(MC & BCS)    OR  50  20             43  P
       404186  LAB PRACTICE I (CN & MWE)      OR  50  20             44  P C   404194  LAB PRAC. IV(ELECTIVE III)    TW  50  20             24  P
       404187  LAB PRAC. II(VLSI &ELEC. I)    TW  50  20             37  P C   404194  LAB PRAC. IV(ELECTIVE III)    PR  50  20             39  P
       404187  LAB PRAC. II(VLSI &ELEC. I)    PR  50  20             29  P C   404195  PROJECT PHASE II              TW 100  40             91  P
       404188  PROJECT PHASE I                OR  50  20             38  P C   404195  PROJECT PHASE II              PR  50  20             44  P

     GRAND TOTAL = 1066/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053178   PATHAK NISHANT MANOJ                 MINAKSHI            ,71527466L   ,PICT       ,B120053178

            SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  12    36   48  P C   404189   MOBILE COMMUNICATION          PP 100  40  25    40   65  P
    404182   COMPUTER NETWORKS            PP 100  40  18    29   47  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  21    46   67  P
    404183   MICROWAVE ENGINEERING        PP 100  40  13    37   50  P C   404191D  SOFT COMPUTING                PP 100  40  24    43   67  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  22    58   80  P C   404192D  WIRELESS NETWORKS             PP 100  40  29    38   67  P
    404185C  PLCS AND AUTOMATION          PP 100  40  12    37   49  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              36  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              38  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              46  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              44  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              41  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              39  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              31  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              43  P C   404195   PROJECT PHASE II              TW 100  40              93  P
    404188   PROJECT PHASE I              OR  50  20              43  P C   404195   PROJECT PHASE II              PR  50  20              43  P

    GRAND TOTAL = 1037/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053179   PATIL HRUSHIKESH PRAMOD              NIVEDITA            ,71527470J   ,PICT       ,B120053179

            SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  19    47   66  P C   404189   MOBILE COMMUNICATION          PP 100  40  24    41   65  P
    404182   COMPUTER NETWORKS            PP 100  40  26    38   64  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  24    40   64  P
    404183   MICROWAVE ENGINEERING        PP 100  40  25    51   76  P C   404191D  SOFT COMPUTING                PP 100  40  24    47   71  P
    404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  23    54   77  P C   404192D  WIRELESS NETWORKS             PP 100  40  29    50   79  P
    404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  18    48   66  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              48  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              36  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              43  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              43  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              46  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              45  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              45  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              37  P C   404195   PROJECT PHASE II              TW 100  40              97  P
    404188   PROJECT PHASE I              OR  50  20              42  P C   404195   PROJECT PHASE II              PR  50  20              47  P

    GRAND TOTAL = 1157/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053180   PATIL PRAJAKTA BHAUSAHEB             VIJAYA             ,71527472E   ,PICT       ,B120053180

            SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  23    49   72  P C   404189   MOBILE COMMUNICATION          PP 100  40  25    40   65  P
    404182   COMPUTER NETWORKS            PP 100  40  19    44   63  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  19    48   67  P
    404183   MICROWAVE ENGINEERING        PP 100  40  20    53   73  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  20    46   66  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  18    58   76  P C   404192A  BIOMEDICAL SIGNAL PROCESSING  PP 100  40  29    58   87  P
    404185C  PLCS AND AUTOMATION          PP 100  40  17    51   68  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              47  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              41  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              44  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              44  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              45  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              43  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              34  P C   404195   PROJECT PHASE II              TW 100  40              95  P
    404188   PROJECT PHASE I              OR  50  20              41  P C   404195   PROJECT PHASE II              PR  50  20              45  P

    GRAND TOTAL = 1158/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053181   PATIL ROHINI JIVAN                       RENUKA              ,71633645G   ,PICT        ,B120053181

             SEM.:1                                                           SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  20     59   79  P C   404189   MOBILE COMMUNICATION         PP 100  40  28     46   74  P
     404182   COMPUTER NETWORKS           PP 100  40  27     43   70  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  20     60   80  P
     404183   MICROWAVE ENGINEERING       PP 100  40  25     68   93  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  24     46   70  P
     404184A  DIGITAL IMAGE PROCESSING    PP 100  40  25     58   83  P C   404192D  WIRELESS NETWORKS            PP 100  40  29     48   77  P
     404185C  PLCS AND AUTOMATION         PP 100  40  24     49   73  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20               39  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50  20               41  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20               42  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50  20               44  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20               38  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20               36  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20               36  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20               34  P C   404195   PROJECT PHASE II             TW 100  40               93  P
     404188   PROJECT PHASE I             OR  50  20               44  P C   404195   PROJECT PHASE II             PR  50  20               43  P

  GRAND TOTAL = 1189/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053182   PATIL SHRIKANT SUNIL                     BHARATI             ,71527476H   ,PICT        ,B120053182

             SEM.:1                                                           SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  16     55   71  P C   404189   MOBILE COMMUNICATION         PP 100  40  22     39   61  P
     404182   COMPUTER NETWORKS           PP 100  40  21     36   57  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  20     48   68  P
     404183   MICROWAVE ENGINEERING       PP 100  40  15     46   61  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  21     50   71  P
     404184A  DIGITAL IMAGE PROCESSING    PP 100  40  23     63   86  P C   404192D  WIRELESS NETWORKS            PP 100  40  29     40   69  P
     404185C  PLCS AND AUTOMATION         PP 100  40  11     50   61  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20               36  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50  20               38  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20               38  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50  20               42  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20               24  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20               36  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20               39  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20               36  P C   404195   PROJECT PHASE II             TW 100  40               91  P
     404188   PROJECT PHASE I             OR  50  20               35  P C   404195   PROJECT PHASE II             PR  50  20               42  P

  GRAND TOTAL = 1062/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053183   PATIL SHRUTI BABASAHEB                   REKHA               ,71633646E   ,PICT        ,B120053183

             SEM.:1                                                           SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  16     58   74  P C   404189   MOBILE COMMUNICATION         PP 100  40  25     59   84  P
     404182   COMPUTER NETWORKS           PP 100  40  28     53   81  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  29     57   86  P
     404183   MICROWAVE ENGINEERING       PP 100  40  22     55   77  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  23     52   75  P
     404184A  DIGITAL IMAGE PROCESSING    PP 100  40  24     66   90  P C   404192D  WIRELESS NETWORKS            PP 100  40  30     48   78  P
     404185C  PLCS AND AUTOMATION         PP 100  40  19     53   72  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20               41  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50  20               48  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20               42  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50  20               43  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20               38  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20               39  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20               38  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20               38  P C   404195   PROJECT PHASE II             TW 100  40               93  P
     404188   PROJECT PHASE I             OR  50  20               43  P C   404195   PROJECT PHASE II             PR  50  20               45  P

  GRAND TOTAL = 1225/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,  SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053184   PAWAR ANKUSH SANJAY                   RANJANA            ,71633647C   ,PICT        ,B120053184

            SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  18    57    75  P C   404189   MOBILE COMMUNICATION          PP 100  40  22    47    69  P
      404182   COMPUTER NETWORKS           PP 100  40  29    46    75  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  19    54    73  P
      404183   MICROWAVE ENGINEERING       PP 100  40  24    59    83  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  24    45    69  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  20    55    75  P C   404192D  WIRELESS NETWORKS             PP 100  40  28    50    78  P
      404185C  PLCS AND AUTOMATION         PP 100  40  19    49    68  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              43  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              40  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              40  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              43  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              38  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              35  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              38  P     404195   PROJECT PHASE II              TW 100  40              90  P
      404188   PROJECT PHASE I             OR  50  20              38  P C   404195   PROJECT PHASE II              PR  50  20              45  P


  GRAND TOTAL = 1157/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053185   PAWAR GAUTAMI RAMCHANDRA              BHAGYASHREE        ,71527480F   ,PICT        ,B120053185

            SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  19    59    78  P C   404189   MOBILE COMMUNICATION          PP 100  40  26    42    68  P
      404182   COMPUTER NETWORKS           PP 100  40  29    45    74  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  16    51    67  P
      404183   MICROWAVE ENGINEERING       PP 100  40  25    65    90  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  24    54    78  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  20    65    85  P C   404192D  WIRELESS NETWORKS             PP 100  40  29    48    77  P
      404185C  PLCS AND AUTOMATION         PP 100  40  20    52    72  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              45  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              42  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              43  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              38  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              40  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              40  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              24  P C   404195   PROJECT PHASE II              TW 100  40              85  P
      404188   PROJECT PHASE I             OR  50  20              40  P C   404195   PROJECT PHASE II              PR  50  20              38  P


  GRAND TOTAL = 1166/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053186   PAWAR NIKITA NIVAS                    NEHA               ,71527482B   ,PICT        ,B120053186

            SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  09    45    54  P C   404189   MOBILE COMMUNICATION          PP 100  40  15    30    45  P
      404182   COMPUTER NETWORKS           PP 100  40  15    41    56  P     404190   BROADBAND COMMUNICATION SYS.  PP 100  40  09    42    51  P
      404183   MICROWAVE ENGINEERING       PP 100  40  14    38    52  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  14    33    47  P
      404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  07    45    52  P C   404192A  BIOMEDICAL SIGNAL PROCESSING  PP 100  40  13    28    41  P
      404185D  ARTIFICIAL INTELLIGENCE     PP 100  40  05    36    41  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              43  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              30  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              41  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              38  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              24  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              34  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              38  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              28  P C   404195   PROJECT PHASE II              TW 100  40              90  P
      404188   PROJECT PHASE I             OR  50  20              36  P C   404195   PROJECT PHASE II              PR  50  20              36  P


  GRAND TOTAL = 877/1500 , RESULT:HIGHER SECOND CLASS
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053187   PAWAR POOJA YASHWANT                SAVITA            ,71633648M   ,PICT        ,B120053187

              SEM.:1                                                       SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  15    56   71  P C   404189   MOBILE COMMUNICATION         PP 100  40  20    46   66  P
      404182   COMPUTER NETWORKS           PP 100  40  25    40   65  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  22    50   72  P
      404183   MICROWAVE ENGINEERING       PP 100  40  20    62   82  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  23    48   71  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  22    60   82  P C   404192A  BIOMEDICAL SIGNAL PROCESSING PP 100  40  25    43   68  P
      404185C  PLCS AND AUTOMATION         PP 100  40  19    44   63  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              40  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              43  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              42  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              37  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              38  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              40  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              37  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              33  P C   404195   PROJECT PHASE II             TW 100  40              90  P
      404188   PROJECT PHASE I             OR  50  20              35  P C   404195   PROJECT PHASE II             PR  50  20              38  P


   GRAND TOTAL = 1113/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053188   PAWAR VAIBHAV BHAGAWAN              GAUTAMI           ,71527484J   ,PICT        ,B120053188

              SEM.:1                                                       SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  12    53   65  P C   404189   MOBILE COMMUNICATION         PP 100  40  22    39   61  P
      404182   COMPUTER NETWORKS           PP 100  40  15    44   59  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  20    42   62  P
      404183   MICROWAVE ENGINEERING       PP 100  40  20    45   65  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  16    33   49  P
      404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  18    49   67  P C   404192D  WIRELESS NETWORKS            PP 100  40  19    37   56  P
      404185C  PLCS AND AUTOMATION         PP 100  40  17    40   57  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              39  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              37  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              40  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              38  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              39  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              37  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              37  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              30  P C   404195   PROJECT PHASE II             TW 100  40              88  P
      404188   PROJECT PHASE I             OR  50  20              37  P C   404195   PROJECT PHASE II             PR  50  20              43  P


   GRAND TOTAL = 1006/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053189   PAWAR. MAYURI GOKUL                 JYOTI             ,71633649K   ,PICT        ,B120053189

              SEM.:1                                                       SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  15    54   69  P C   404189   MOBILE COMMUNICATION         PP 100  40  22    45   67  P
      404182   COMPUTER NETWORKS           PP 100  40  27    44   71  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  15    49   64  P
      404183   MICROWAVE ENGINEERING       PP 100  40  20    66   86  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  23    47   70  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  17    63   80  P C   404192D  WIRELESS NETWORKS            PP 100  40  26    46   72  P
      404185C  PLCS AND AUTOMATION         PP 100  40  25    50   75  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              44  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              44  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              41  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              36  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              40  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              36  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              35  P C   404195   PROJECT PHASE II             TW 100  40              93  P
      404188   PROJECT PHASE I             OR  50  20              38  P C   404195   PROJECT PHASE II             PR  50  20              45  P


   GRAND TOTAL = 1148/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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                      SAVITRIBAI PHULE PUNE UNIVERSITY, B.E.(2012 COURSE) EXAMINATION,MAY 2018
      BRANCH:71-B.E. (2012 PAT.)(ELECTRONICS &TELECOMUNICATION)
      DATE : 04 JUL 2018   CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 63
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   NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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      B120053190 PETHKAR KAUSTUBH VIJAY   VIDYA   ,71633650C   ,PICT   ,B120053190

              SEM.:1   SEM.:2
      404181 VLSI DESIGN & TECHNOLOGY   PP 100 40 13   43   56 P C   404189 MOBILE COMMUNICATION   PP 100 40 22   40   62 P
      404182 COMPUTER NETWORKS   PP 100 40 25   41   66 P C   404190 BROADBAND COMMUNICATION SYS. PP 100 40 24   48   72 P
      404183 MICROWAVE ENGINEERING   PP 100 40 22   59   81 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 19   38   57 P
      404184A DIGITAL IMAGE PROCESSING   PP 100 40 15   63   78 P C   404192D WIRELESS NETWORKS   PP 100 40 27   36   63 P
      404185C PLCS AND AUTOMATION   PP 100 40 07   42   49 P C   404193 LAB PRACTICE III(MC & BCS)   TW 50 20   44 P
      404186 LAB PRACTICE I (CN & MWE)   TW 50 20   41 P C   404193 LAB PRACTICE III(MC & BCS)   OR 50 20   42 P
      404186 LAB PRACTICE I (CN & MWE)   OR 50 20   37 P C   404194 LAB PRAC. IV(ELECTIVE III)   TW 50 20   34 P
      404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20   43 P C   404194 LAB PRAC. IV(ELECTIVE III)   PR 50 20   40 P
      404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20   35 P C   404195 PROJECT PHASE II   TW 100 40   92 P
      404188 PROJECT PHASE I   OR 50 20   43 P C   404195 PROJECT PHASE II   PR 50 20   43 P

   GRAND TOTAL = 1078/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053191 PHASE SIDHANT RAJKUMAR   SAVITRI   ,71633651M   ,PICT   ,B120053191

              SEM.:1   SEM.:2
      404181 VLSI DESIGN & TECHNOLOGY   PP 100 40 14   50   64 P C   404189 MOBILE COMMUNICATION   PP 100 40 23   41   64 P
      404182 COMPUTER NETWORKS   PP 100 40 26   44   70 P C   404190 BROADBAND COMMUNICATION SYS. PP 100 40 18   46   64 P
      404183 MICROWAVE ENGINEERING   PP 100 40 13   56   69 P C   404191D SOFT COMPUTING   PP 100 40 17   47   64 P
      404184B EMBEDDED SYSTEMS & RTOS   PP 100 40 20   58   78 P C   404192D WIRELESS NETWORKS   PP 100 40 22   40   62 P
      404185C PLCS AND AUTOMATION   PP 100 40 22   49   71 P C   404193 LAB PRACTICE III(MC & BCS)   TW 50 20   38 P
      404186 LAB PRACTICE I (CN & MWE)   TW 50 20   38 P C   404193 LAB PRACTICE III(MC & BCS)   OR 50 20   45 P
      404186 LAB PRACTICE I (CN & MWE)   OR 50 20   42 P C   404194 LAB PRAC. IV(ELECTIVE III)   TW 50 20   30 P
      404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20   34 P C   404194 LAB PRAC. IV(ELECTIVE III)   PR 50 20   40 P
      404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20   28 P C   404195 PROJECT PHASE II   TW 100 40   88 P
      404188 PROJECT PHASE I   OR 50 20   43 P C   404195 PROJECT PHASE II   PR 50 20   40 P

   GRAND TOTAL = 1072/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053192 POORSWANI NISHANT ARUN   MEENA   ,71527487C   ,PICT   ,B120053192

              SEM.:1   SEM.:2
      404181 VLSI DESIGN & TECHNOLOGY   PP 100 40 14   57   71 P C   404189 MOBILE COMMUNICATION   PP 100 40 26   53   79 P
      404182 COMPUTER NETWORKS   PP 100 40 29   54   83 P C   404190 BROADBAND COMMUNICATION SYS. PP 100 40 25   51   76 P
      404183 MICROWAVE ENGINEERING   PP 100 40 23   66   89 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 21   38   59 P
      404184B EMBEDDED SYSTEMS & RTOS   PP 100 40 21   55   76 P C   404192D WIRELESS NETWORKS   PP 100 40 27   33   60 P
      404185C PLCS AND AUTOMATION   PP 100 40 18   44   62 P C   404193 LAB PRACTICE III(MC & BCS)   TW 50 20   45 P
      404186 LAB PRACTICE I (CN & MWE)   TW 50 20   45 P C   404193 LAB PRACTICE III(MC & BCS)   OR 50 20   46 P
      404186 LAB PRACTICE I (CN & MWE)   OR 50 20   46 P C   404194 LAB PRAC. IV(ELECTIVE III)   TW 50 20   44 P
      404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20   47 P C   404194 LAB PRAC. IV(ELECTIVE III)   PR 50 20   45 P
      404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20   47 P C   404195 PROJECT PHASE II   TW 100 40   94 P
      404188 PROJECT PHASE I   OR 50 20   43 P C   404195 PROJECT PHASE II   PR 50 20   44 P

   GRAND TOTAL = 1201/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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  NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
  OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053193   PRACHI KADU                      ARUNA              ,71527490C   ,PICT        ,B120053193

            SEM.:1                                                   SEM.:2
   404181  VLSI DESIGN & TECHNOLOGY   PP 100  40  07   39   46  P C   404189   MOBILE COMMUNICATION        PP 100  40  20    22#  42  P
   404182  COMPUTER NETWORKS          PP 100  40  21   30   51  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  16    35   51  P
   404183  MICROWAVE ENGINEERING      PP 100  40  15   36   51  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  13    32   45  P
   404184A DIGITAL IMAGE PROCESSING   PP 100  40  16   45   61  P C   404192D  WIRELESS NETWORKS           PP 100  40  16    28   44  P
   404185D ARTIFICIAL INTELLIGENCE    PP 100  40  11   29   40  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20              42  P
   404186  LAB PRACTICE I (CN & MWE)  TW  50  20           37  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20              43  P
   404186  LAB PRACTICE I (CN & MWE)  OR  50  20           46  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              42  P
   404187  LAB PRAC. II(VLSI &ELEC. I) TW  50  20           37  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              38  P
   404187  LAB PRAC. II(VLSI &ELEC. I) PR  50  20           23  P C   404195   PROJECT PHASE II            TW 100  40              92  P
   404188  PROJECT PHASE I            OR  50  20           41  P C   404195   PROJECT PHASE II            PR  50  20              43  P

 GRAND TOTAL = 915/1500 , RESULT:FIRST CLASS   [# O.4]
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    B120053194   PRAKASH KUMAR                    MANJUDEVI          ,71547751L   ,PICT        ,B120053194

            SEM.:1                                                   SEM.:2
   404181  VLSI DESIGN & TECHNOLOGY   PP 100  40  04   46   50  P C   404189   MOBILE COMMUNICATION        PP 100  40  18    37   55  P
   404182  COMPUTER NETWORKS          PP 100  40  16   30   46  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  12    34   46  P
   404183  MICROWAVE ENGINEERING      PP 100  40  13   49   62  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  17    33   50  P
   404184A DIGITAL IMAGE PROCESSING   PP 100  40  18   56   74  P C   404192A  BIOMEDICAL SIGNAL PROCESSING PP 100  40  21    40   61  P
   404185D ARTIFICIAL INTELLIGENCE    PP 100  40  13   39   52  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20              38  P
   404186  LAB PRACTICE I (CN & MWE)  TW  50  20           45  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20              38  P
   404186  LAB PRACTICE I (CN & MWE)  OR  50  20           43  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              24  P
   404187  LAB PRAC. II(VLSI &ELEC. I) TW  50  20           34  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              37  P
   404187  LAB PRAC. II(VLSI &ELEC. I) PR  50  20           38  P C   404195   PROJECT PHASE II            TW 100  40              92  P
   404188  PROJECT PHASE I            OR  50  20           39  P C   404195   PROJECT PHASE II            PR  50  20              45  P

 GRAND TOTAL = 969/1500 , RESULT:FIRST CLASS
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    B120053195   PRANAV DILIP BOROLE              SANGITA            ,71527492K   ,PICT        ,B120053195

            SEM.:1                                                   SEM.:2
   404181  VLSI DESIGN & TECHNOLOGY   PP 100  40  13   38   51  P C   404189   MOBILE COMMUNICATION        PP 100  40  21    36   57  P
   404182  COMPUTER NETWORKS          PP 100  40  22   28   50  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  15    32   47  P
   404183  MICROWAVE ENGINEERING      PP 100  40  18   42   60  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100  40  12    35   47  P
   404184B EMBEDDED SYSTEMS & RTOS    PP 100  40  19   36   55  P C   404192D  WIRELESS NETWORKS           PP 100  40  19    28   47  P
   404185C PLCS AND AUTOMATION        PP 100  40  12   34   46  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50  20              31  P
   404186  LAB PRACTICE I (CN & MWE)  TW  50  20           36  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50  20              44  P
   404186  LAB PRACTICE I (CN & MWE)  OR  50  20           41  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50  20              28  P
   404187  LAB PRAC. II(VLSI &ELEC. I) TW  50  20           35  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50  20              39  P
   404187  LAB PRAC. II(VLSI &ELEC. I) PR  50  20           40  P C   404195   PROJECT PHASE II            TW 100  40              90  P
   404188  PROJECT PHASE I            OR  50  20           42  P C   404195   PROJECT PHASE II            PR  50  20              40  P

 GRAND TOTAL = 926/1500 , RESULT:FIRST CLASS
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053196   PURVA KISHOR DESHMUKH                NEHA                 ,71527501B   ,PICT         ,B120053196

            SEM.:1                                                           SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  12     46   58  P C   404189   MOBILE COMMUNICATION          PP 100  40  18     43   61  P
    404182   COMPUTER NETWORKS             PP 100  40  21     42   63  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  19     42   61  P
    404183   MICROWAVE ENGINEERING         PP 100  40  16     41   57  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  18     39   57  P
    404184A  DIGITAL IMAGE PROCESSING      PP 100  40  14     42   56  P C   404192D  WIRELESS NETWORKS             PP 100  40  18     40   58  P
    404185C  PLCS AND AUTOMATION           PP 100  40  14     39   53  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20             39  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20              36  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20             41  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             36  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              39  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             37  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              40  P C   404195   PROJECT PHASE II              TW 100  40             94  P
    404188   PROJECT PHASE I               OR  50  20              41  P C   404195   PROJECT PHASE II              PR  50  20             44  P

  GRAND TOTAL = 1013/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053197   RACHIT KUMAR THAKUR                 DEOKANYA             ,71527503J   ,PICT         ,B120053197

            SEM.:1                                                           SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  12     37   49  P C   404189   MOBILE COMMUNICATION          PP 100  40  23     41   64  P
    404182   COMPUTER NETWORKS             PP 100  40  23     43   66  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  23     40   63  P
    404183   MICROWAVE ENGINEERING         PP 100  40  21     59   80  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  18     34   52  P
    404184A  DIGITAL IMAGE PROCESSING      PP 100  40  11     50   61  P C   404192D  WIRELESS NETWORKS             PP 100  40  26     39   65  P
    404185B  ELECTRONIC PRODUCT DESIGN     PP 100  40  17     29   46  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20             42  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20              39  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20             40  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             36  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              40  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             39  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              30  P C   404195   PROJECT PHASE II              TW 100  40             88  P
    404188   PROJECT PHASE I               OR  50  20              39  P C   404195   PROJECT PHASE II              PR  50  20             43  P

  GRAND TOTAL = 1024/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053198   RAKSHE VEDANT SHASHIKANT            MADHURI              ,71527509H   ,PICT         ,B120053198

            SEM.:1                                                           SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  15     51   66  P C   404189   MOBILE COMMUNICATION          PP 100  40  24     44   68  P
    404182   COMPUTER NETWORKS             PP 100  40  12     37   49  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  20     52   72  P
    404183   MICROWAVE ENGINEERING         PP 100  40  23     56   79  P C   404191D  SOFT COMPUTING                PP 100  40  20     52   72  P
    404184A  DIGITAL IMAGE PROCESSING      PP 100  40  23     58   81  P C   404192D  WIRELESS NETWORKS             PP 100  40  24     39   63  P
    404185C  PLCS AND AUTOMATION           PP 100  40  12     47   59  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20             45  P
    404186   LAB PRACTICE I (CN & MWE)     TW  50  20              43  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20             36  P
    404186   LAB PRACTICE I (CN & MWE)     OR  50  20              39  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             35  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              43  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             37  P
    404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              35  P C   404195   PROJECT PHASE II              TW 100  40             94  P
    404188   PROJECT PHASE I               OR  50  20              34  P C   404195   PROJECT PHASE II              PR  50  20             46  P

  GRAND TOTAL = 1096/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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      B120053199   RAMAIYA APURVA UMESH   KAVITA   ,71633652K   ,PICT   ,B120053199

              SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 11   48   59 P C 404189   MOBILE COMMUNICATION   PP 100 40 23   33   56 P
      404182   COMPUTER NETWORKS   PP 100 40 24   38   62 P C 404190   BROADBAND COMMUNICATION SYS. PP 100 40 23   55   78 P
      404183   MICROWAVE ENGINEERING   PP 100 40 27   46   73 P C 404191C AUDIO VIDEO ENGINEERING   PP 100 40 22   47   69 P
      404184A DIGITAL IMAGE PROCESSING   PP 100 40 23   49   72 P C 404192D WIRELESS NETWORKS   PP 100 40 26   39   65 P
      404185C PLCS AND AUTOMATION   PP 100 40 09   47   56 P C 404193   LAB PRACTICE III(MC & BCS)   TW 50 20   40 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   46 P C 404193   LAB PRACTICE III(MC & BCS)   OR 50 20   40 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   38 P C 404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   42 P
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW 50 20   40 P C 404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   39 P
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR 50 20   31 P C 404195   PROJECT PHASE II   TW 100 40   93 P
      404188   PROJECT PHASE I   OR 50 20   43 P C 404195   PROJECT PHASE II   PR 50 20   45 P

   GRAND TOTAL = 1087/1500 , RESULT:FIRST CLASS WITH DISTINCTION
....
      B120053200   RANADE TANVI UDAY   PURWA   ,71527512H   ,PICT   ,B120053200

              SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 13   34   47 P C 404189   MOBILE COMMUNICATION   PP 100 40 24   31   55 P
      404182   COMPUTER NETWORKS   PP 100 40 16   32   48 P C 404190   BROADBAND COMMUNICATION SYS. PP 100 40 25   47   72 P
      404183   MICROWAVE ENGINEERING   PP 100 40 21   35   56 P C 404191D SOFT COMPUTING   PP 100 40 18   37   55 P
      404184B EMBEDDED SYSTEMS & RTOS   PP 100 40 18   36   54 P C 404192D WIRELESS NETWORKS   PP 100 40 19   28   47 P
      404185D ARTIFICIAL INTELLIGENCE   PP 100 40 13   44   57 P C 404193   LAB PRACTICE III(MC & BCS)   TW 50 20   33 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   35 P C 404193   LAB PRACTICE III(MC & BCS)   OR 50 20   46 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   40 P C 404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   26 P
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW 50 20   35 P C 404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   39 P
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR 50 20   34 P C 404195   PROJECT PHASE II   TW 100 40   90 P
      404188   PROJECT PHASE I   OR 50 20   42 P C 404195   PROJECT PHASE II   PR 50 20   45 P

   GRAND TOTAL = 956/1500 , RESULT:FIRST CLASS
....
      B120053201   RANDAD PRATIK PRAKASH   SANGITA   ,71527513F   ,PICT   ,B120053201

              SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 13   50   63 P C 404189   MOBILE COMMUNICATION   PP 100 40 25   41   66 P
      404182   COMPUTER NETWORKS   PP 100 40 28   44   72 P C 404190   BROADBAND COMMUNICATION SYS. PP 100 40 25   49   74 P
      404183   MICROWAVE ENGINEERING   PP 100 40 23   58   81 P C 404191C AUDIO VIDEO ENGINEERING   PP 100 40 21   43   64 P
      404184A DIGITAL IMAGE PROCESSING   PP 100 40 21   58   79 P C 404192D WIRELESS NETWORKS   PP 100 40 23   39   62 P
      404185B ELECTRONIC PRODUCT DESIGN   PP 100 40 20   35   55 P C 404193   LAB PRACTICE III(MC & BCS)   TW 50 20   41 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   34 P C 404193   LAB PRACTICE III(MC & BCS)   OR 50 20   38 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   39 P C 404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   40 P
      404187   LAB PRAC. II(VLSI &ELEC. I)   TW 50 20   34 P C 404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   38 P
      404187   LAB PRAC. II(VLSI &ELEC. I)   PR 50 20   35 P C 404195   PROJECT PHASE II   TW 100 40   94 P
      404188   PROJECT PHASE I   OR 50 20   45 P C 404195   PROJECT PHASE II   PR 50 20   45 P

   GRAND TOTAL = 1099/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    B120053202   RANE MITESH MANOHAR   PRADNYA   ,71527515B   ,PICT   ,B120053202

            SEM.:1   SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 14   49   63 P C   404189   MOBILE COMMUNICATION   PP 100 40 21   46   67 P
    404182   COMPUTER NETWORKS   PP 100 40 27   45   72 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 21   53   74 P
    404183   MICROWAVE ENGINEERING   PP 100 40 24   58   82 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 15   39   54 P
    404184B EMBEDDED SYSTEMS & RTOS   PP 100 40 24   40   64 P C   404192D WIRELESS NETWORKS   PP 100 40 25   36   61 P
    404185C PLCS AND AUTOMATION   PP 100 40 24   49   73 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   37 P
    404186   LAB PRACTICE I (CN & MWE)   TW 50 20   41 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   40 P
    404186   LAB PRACTICE I (CN & MWE)   OR 50 20   40 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   35 P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   41 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   37 P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   36 P C   404195   PROJECT PHASE II   TW 100 40   93 P
    404188   PROJECT PHASE I   OR 50 20   42 P C   404195   PROJECT PHASE II   PR 50 20   42 P

  GRAND TOTAL = 1094/1500 , RESULT:FIRST CLASS WITH DISTINCTION
....
    B120053203   RASIKA HEMANT KULKARNI   BHAVANA   ,71527521G   ,PICT   ,B120053203

            SEM.:1   SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 10   35   45 P C   404189   MOBILE COMMUNICATION   PP 100 40 20   21# 41 P
    404182   COMPUTER NETWORKS   PP 100 40 19   43   62 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 07   40   47 P
    404183   MICROWAVE ENGINEERING   PP 100 40 13   29   42 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 13   36   49 P
    404184B EMBEDDED SYSTEMS & RTOS   PP 100 40 21   38   59 P C   404192D WIRELESS NETWORKS   PP 100 40 22   38   60 P
    404185D ARTIFICIAL INTELLIGENCE   PP 100 40 16   38   54 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   37 P
    404186   LAB PRACTICE I (CN & MWE)   TW 50 20   36 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   38 P
    404186   LAB PRACTICE I (CN & MWE)   OR 50 20   38 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   32 P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   39 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   35 P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   25 P C   404195   PROJECT PHASE II   TW 100 40   94 P
    404188   PROJECT PHASE I   OR 50 20   44 P C   404195   PROJECT PHASE II   PR 50 20   45 P

  GRAND TOTAL = 922/1500 , RESULT:FIRST CLASS   [# O.4]
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    B120053204   RATHOD AISHWARYA RAJESH   SUPRIYA   ,71633653H   ,PICT   ,B120053204

            SEM.:1   SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 12   46   58 P C   404189   MOBILE COMMUNICATION   PP 100 40 22   49   71 P
    404182   COMPUTER NETWORKS   PP 100 40 24   44   68 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 19   48   67 P
    404183   MICROWAVE ENGINEERING   PP 100 40 17   56   73 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 20   51   71 P
    404184A DIGITAL IMAGE PROCESSING   PP 100 40 21   54   75 P C   404192D WIRELESS NETWORKS   PP 100 40 25   41   66 P
    404185B ELECTRONIC PRODUCT DESIGN   PP 100 40 21   43   64 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   38 P
    404186   LAB PRACTICE I (CN & MWE)   TW 50 20   41 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   41 P
    404186   LAB PRACTICE I (CN & MWE)   OR 50 20   44 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   42 P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   33 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   38 P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   33 P C   404195   PROJECT PHASE II   TW 100 40   94 P
    404188   PROJECT PHASE I   OR 50 20   41 P C   404195   PROJECT PHASE II   PR 50 20   44 P

  GRAND TOTAL = 1102/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053206   RAUT HRISHIKESH PURUSHOTTAM             SUNITA             ,71527530F   ,PICT        ,B120053206

            SEM.:1                                                    SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 15    53    68  P C   404189   MOBILE COMMUNICATION          PP 100 40 28    42    70  P
    404182   COMPUTER NETWORKS           PP 100 40 23    45    68  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100 40 21    46    67  P
    404183   MICROWAVE ENGINEERING       PP 100 40 20    58    78  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100 40 21    41    62  P
    404184B  EMBEDDED SYSTEMS & RTOS     PP 100 40 22    46    68  P C   404192D  WIRELESS NETWORKS             PP 100 40 27    39    66  P
    404185C  PLCS AND AUTOMATION         PP 100 40 16    49    65  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50 20                37  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50 20             40  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50 20                42  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50 20             39  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50 20                43  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20             45  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50 20                40  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20             45  P C   404195   PROJECT PHASE II              TW 100 40                95  P
    404188   PROJECT PHASE I             OR  50 20             45  P C   404195   PROJECT PHASE II              PR  50 20                45  P

GRAND TOTAL = 1128/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053207   RAUT RINKOO POPAT                       CHHAYA             ,71633654F   ,PICT        ,B120053207

            SEM.:1                                                    SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 16    49    65  P C   404189   MOBILE COMMUNICATION          PP 100 40 21    38    59  P
    404182   COMPUTER NETWORKS           PP 100 40 24    38    62  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100 40 20    31    51  P
    404183   MICROWAVE ENGINEERING       PP 100 40 18    55    73  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100 40 20    45    65  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100 40 17    50    67  P C   404192D  WIRELESS NETWORKS             PP 100 40 24    42    66  P
    404185C  PLCS AND AUTOMATION         PP 100 40 10    49    59  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50 20                39  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50 20             43  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50 20                39  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50 20             39  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50 20                30  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20             38  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50 20                36  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20             24  P     404195   PROJECT PHASE II              TW 100 40                78  P
    404188   PROJECT PHASE I             OR  50 20             36  P C   404195   PROJECT PHASE II              PR  50 20                33  P

GRAND TOTAL = 1002/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053208   RAUTMARE VISHWAJIT LAXMIKANT            SHUBHANGI          ,71527532B   ,PICT        ,B120053208

            SEM.:1                                                    SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 07    28    35# P   404189   MOBILE COMMUNICATION          PP 100 40 15    32    47  P
    404182   COMPUTER NETWORKS           PP 100 40 19    30    49  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100 40 21    34    55  P
    404183   MICROWAVE ENGINEERING       PP 100 40 01    47    48  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100 40 07    34    41  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100 40 13    45    58  P C   404192D  WIRELESS NETWORKS             PP 100 40 21    30    51  P
    404185C  PLCS AND AUTOMATION         PP 100 40 05    40    45  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50 20                21  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50 20             22  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50 20                23  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50 20             41  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50 20                24  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20             21  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50 20                36  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20             36  P     404195   PROJECT PHASE II              TW 100 40                90  P
    404188   PROJECT PHASE I             OR  50 20             40  P C   404195   PROJECT PHASE II              PR  50 20                40  P

GRAND TOTAL = 823/1500 , RESULT:SECOND CLASS   [# O.4]
                                                    RESULT RESERVED FOR BACKLOGS.
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    NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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      B120053209   RAVIRAJ KUMAR   RAGINI   ,71527534J   ,PICT   ,B120053209

             SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 16   53   69 P C   404189   MOBILE COMMUNICATION   PP 100 40 23   41   64 P
      404182   COMPUTER NETWORKS   PP 100 40 26   46   72 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 21   48   69 P
      404183   MICROWAVE ENGINEERING   PP 100 40 23   61   84 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 22   46   68 P
      404184A DIGITAL IMAGE PROCESSING   PP 100 40 20   52   72 P C   404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 29   53   82 P
      404185C PLCS AND AUTOMATION   PP 100 40 17   53   70 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   41 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   34 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   38 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   44 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   36 P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   39 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   38 P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   44 P C   404195   PROJECT PHASE II   TW 100 40   90 P
      404188   PROJECT PHASE I   OR 50 20   39 P C   404195   PROJECT PHASE II   PR 50 20   42 P

   GRAND TOTAL = 1135/1500 , RESULT:FIRST CLASS WITH DISTINCTION
....
      B120053210   RIPUL DUBEY   VARSHA   ,71401116K   ,PICT   ,B120053210

             SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 12   36   48 P   404189   MOBILE COMMUNICATION   PP 100 40 15   36   51 P
      404182   COMPUTER NETWORKS   PP 100 40 16   35   51 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 19   36   55 P
      404183   MICROWAVE ENGINEERING   PP 100 40 04   45   49 P C   404191D SOFT COMPUTING   PP 100 40 11   41   52 P
      404184A DIGITAL IMAGE PROCESSING   PP 100 40 12   32   44 P C   404192D WIRELESS NETWORKS   PP 100 40 20   37   57 P
      404185D ARTIFICIAL INTELLIGENCE   PP 100 40 17   40   57 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   20 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   30 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   36 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   43 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   26 P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   31 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   25 P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   28 P C   404195   PROJECT PHASE II   TW 100 40   85 P
      404188   PROJECT PHASE I   OR 50 20   31 P C   404195   PROJECT PHASE II   PR 50 20   30 P

   GRAND TOTAL = 849/1500 , RESULT:HIGHER SECOND CLASS
....
      B120053211   RUPAWATE RAHUL RAVINDRA   KRISHNA   ,71527543H   ,PICT   ,B120053211

             SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 03   32   35# P   404189   MOBILE COMMUNICATION   PP 100 40 24   29   53 P
      404182   COMPUTER NETWORKS   PP 100 40 17   38   55 P   404190   BROADBAND COMMUNICATION SYS. PP 100 40 09   41   50 P
      404183   MICROWAVE ENGINEERING   PP 100 40 22   29   51 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 16   40   56 P
      404184A DIGITAL IMAGE PROCESSING   PP 100 40 14   32   46 P C   404192D WIRELESS NETWORKS   PP 100 40 13   34   47 P
      404185C PLCS AND AUTOMATION   PP 100 40 16   38   54 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   34 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   41 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   34 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   35 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   24 P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   32 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   37 P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   35 P C   404195   PROJECT PHASE II   TW 100 40   87 P
      404188   PROJECT PHASE I   OR 50 20   41 P C   404195   PROJECT PHASE II   PR 50 20   41 P

   GRAND TOTAL = 888/1500 , RESULT:HIGHER SECOND CLASS   [# O.4]
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053212   RUSHABH UTTAM KAMBLE                 REKHA UTTAM KAMBLE  ,71527545D   ,PICT       ,B120053212

              SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  11    38    49  P      404189   MOBILE COMMUNICATION         PP 100  40  24    32    56  P
      404182   COMPUTER NETWORKS           PP 100  40  16    44    60  P C    404190   BROADBAND COMMUNICATION SYS. PP 100  40  08    43    51  P
      404183   MICROWAVE ENGINEERING       PP 100  40  23    32    55  P C    404191C  AUDIO VIDEO ENGINEERING      PP 100  40  16    47    63  P
      404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  14    38    52  P C    404192D  WIRELESS NETWORKS            PP 100  40  20    36    56  P
      404185D  ARTIFICIAL INTELLIGENCE     PP 100  40  13    40    53  P C    404193   LAB PRACTICE III(MC & BCS)   TW  50  20              29  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              34  P C    404193   LAB PRACTICE III(MC & BCS)   OR  50  20              39  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              39  P C    404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              33  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              35  P C    404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              34  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              33  P C    404195   PROJECT PHASE II             TW 100  40              87  P
      404188   PROJECT PHASE I             OR  50  20              38  P C    404195   PROJECT PHASE II             PR  50  20              42  P

    GRAND TOTAL = 938/1500 , RESULT:FIRST CLASS
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      B120053213   SAKAT TANAYA VASANT                  SHARDA              ,71527552G   ,PICT       ,B120053213

              SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  18    39    57  P C    404189   MOBILE COMMUNICATION         PP 100  40  18    36    54  P
      404182   COMPUTER NETWORKS           PP 100  40  14    39    53  P C    404190   BROADBAND COMMUNICATION SYS. PP 100  40  14    46    60  P
      404183   MICROWAVE ENGINEERING       PP 100  40  25    38    63  P C    404191C  AUDIO VIDEO ENGINEERING      PP 100  40  14    35    49  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  17    30    47  P C    404192D  WIRELESS NETWORKS            PP 100  40  17    30    47  P
      404185C  PLCS AND AUTOMATION         PP 100  40  11    38    49  P C    404193   LAB PRACTICE III(MC & BCS)   TW  50  20              40  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              40  P C    404193   LAB PRACTICE III(MC & BCS)   OR  50  20              32  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              32  P C    404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              40  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              34  P C    404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              38  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              40  P C    404195   PROJECT PHASE II             TW 100  40              95  P
      404188   PROJECT PHASE I             OR  50  20              43  P C    404195   PROJECT PHASE II             PR  50  20              42  P

    GRAND TOTAL = 955/1500 , RESULT:FIRST CLASS
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      B120053214   SAKET M SIDDHESHWAR                  ANJALI              ,71527553E   ,PICT       ,B120053214

              SEM.:1                                                          SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  15    48    63  P C    404189   MOBILE COMMUNICATION         PP 100  40  17    44    61  P
      404182   COMPUTER NETWORKS           PP 100  40  23    48    71  P C    404190   BROADBAND COMMUNICATION SYS. PP 100  40  22    48    70  P
      404183   MICROWAVE ENGINEERING       PP 100  40  23    44    67  P C    404191C  AUDIO VIDEO ENGINEERING      PP 100  40  16    41    57  P
      404184A  DIGITAL IMAGE PROCESSING    PP 100  40  22    35    57  P C    404192D  WIRELESS NETWORKS            PP 100  40  27    36    63  P
      404185C  PLCS AND AUTOMATION         PP 100  40  19    38    57  P C    404193   LAB PRACTICE III(MC & BCS)   TW  50  20              44  P
      404186   LAB PRACTICE I (CN & MWE)   TW  50  20              42  P C    404193   LAB PRACTICE III(MC & BCS)   OR  50  20              40  P
      404186   LAB PRACTICE I (CN & MWE)   OR  50  20              43  P C    404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              24  P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              43  P C    404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              38  P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              41  P C    404195   PROJECT PHASE II             TW 100  40              96  P
      404188   PROJECT PHASE I             OR  50  20              43  P C    404195   PROJECT PHASE II             PR  50  20              45  P

    GRAND TOTAL = 1065/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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        B120053215   SAKSHI GUPTA                      ASHOO                ,71527555M   ,PICT        ,B120053215

                SEM.:1                                                      SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  12    34    46  P C   404189   MOBILE COMMUNICATION         PP 100  40  29    37    66  P
        404182   COMPUTER NETWORKS           PP 100  40  17    44    61  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  26    41    67  P
        404183   MICROWAVE ENGINEERING       PP 100  40  22    45    67  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  18    47    65  P
        404184A  DIGITAL IMAGE PROCESSING    PP 100  40  22    31    53  P C   404192D  WIRELESS NETWORKS            PP 100  40  25    34    59  P
        404185C  PLCS AND AUTOMATION         PP 100  40  18    36    54  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20                45  P
        404186   LAB PRACTICE I (CN & MWE)   TW  50  20                38  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20                42  P
        404186   LAB PRACTICE I (CN & MWE)   OR  50  20                42  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20                38  P
        404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20                33  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20                39  P
        404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20                37  P C   404195   PROJECT PHASE II             TW 100  40                92  P
        404188   PROJECT PHASE I             OR  50  20                37  P C   404195   PROJECT PHASE II             PR  50  20                35  P

    GRAND TOTAL = 1016/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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        B120053216   SALI GANESH TUKARAM               SUNDAR               ,71633655D   ,PICT        ,B120053216

                SEM.:1                                                      SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  15    42    57  P C   404189   MOBILE COMMUNICATION         PP 100  40  22    41    63  P
        404182   COMPUTER NETWORKS           PP 100  40  27    49    76  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  28    46    74  P
        404183   MICROWAVE ENGINEERING       PP 100  40  13    52    65  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  16    39    55  P
        404184A  DIGITAL IMAGE PROCESSING    PP 100  40  21    60    81  P C   404192D  WIRELESS NETWORKS            PP 100  40  23    28    51  P
        404185D  ARTIFICIAL INTELLIGENCE     PP 100  40  15    46    61  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20                36  P
        404186   LAB PRACTICE I (CN & MWE)   TW  50  20                46  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20                40  P
        404186   LAB PRACTICE I (CN & MWE)   OR  50  20                37  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20                34  P
        404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20                37  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20                37  P
        404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20                40  P C   404195   PROJECT PHASE II             TW 100  40                85  P
        404188   PROJECT PHASE I             OR  50  20                38  P C   404195   PROJECT PHASE II             PR  50  20                30  P

    GRAND TOTAL = 1043/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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        B120053217   SALUNKE RUTUJA DEELIP             MANGAL               ,71527557H   ,PICT        ,B120053217

                SEM.:1                                                      SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  15    43    58  P C   404189   MOBILE COMMUNICATION         PP 100  40  22    33    55  P
        404182   COMPUTER NETWORKS           PP 100  40  24    41    65  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  24    42    66  P
        404183   MICROWAVE ENGINEERING       PP 100  40  19    44    63  P C   404191D  SOFT COMPUTING               PP 100  40  22    39    61  P
        404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  21    44    65  P C   404192D  WIRELESS NETWORKS            PP 100  40  23    41    64  P
        404185B  ELECTRONIC PRODUCT DESIGN   PP 100  40  13    43    56  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20                35  P
        404186   LAB PRACTICE I (CN & MWE)   TW  50  20                35  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20                42  P
        404186   LAB PRACTICE I (CN & MWE)   OR  50  20                38  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20                29  P
        404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20                41  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20                28  P
        404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20                34  P C   404195   PROJECT PHASE II             TW 100  40                93  P
        404188   PROJECT PHASE I             OR  50  20                45  P C   404195   PROJECT PHASE II             PR  50  20                44  P

    GRAND TOTAL = 1017/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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  NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
  OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    B120053218   SALUNKHE PRASHANTI PRAKASH            RASHMI            ,71633656B   ,PICT        ,B120053218

            SEM.:1                                                      SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 15    48   63  P C   404189   MOBILE COMMUNICATION         PP 100 40 29    41   70  P
    404182   COMPUTER NETWORKS            PP 100 40 20    49   69  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 26    54   80  P
    404183   MICROWAVE ENGINEERING        PP 100 40 22    49   71  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100 40 19    50   69  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100 40 16    62   78  P C   404192D  WIRELESS NETWORKS            PP 100 40 28    41   69  P
    404185B  ELECTRONIC PRODUCT DESIGN    PP 100 40 24    37   61  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50 20         42  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50 20         39  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50 20         44  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50 20         43  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50 20         42  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20         39  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50 20         39  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20         39  P C   404195   PROJECT PHASE II             TW 100 40         92  P
    404188   PROJECT PHASE I              OR  50 20         42  P C   404195   PROJECT PHASE II             PR  50 20         43  P

GRAND TOTAL = 1134/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053219   SAMARTH SARANG PRAVIN                 MINAL             ,71527558F   ,PICT        ,B120053219

            SEM.:1                                                      SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 10    34   44  P C   404189   MOBILE COMMUNICATION         PP 100 40 21    35   56  P
    404182   COMPUTER NETWORKS            PP 100 40 12    31   43  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 29    41   70  P
    404183   MICROWAVE ENGINEERING        PP 100 40 15    32   47  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100 40 10    35   45  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100 40 15    43   58  P C   404192D  WIRELESS NETWORKS            PP 100 40 18    23#  41  P
    404185B  ELECTRONIC PRODUCT DESIGN    PP 100 40 11    29   40  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50 20         22  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50 20         33  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50 20         32  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50 20         27  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50 20         38  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20         31  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50 20         40  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20         23  P C   404195   PROJECT PHASE II             TW 100 40         92  P
    404188   PROJECT PHASE I              OR  50 20         37  P C   404195   PROJECT PHASE II             PR  50 20         43  P

GRAND TOTAL = 862/1500 , RESULT:HIGHER SECOND CLASS   [# O.4]
                                                      RESULT RESERVED FOR BACKLOGS.
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    B120053220   SAMDURKAR SHARDUL ANIL               JAYASHREE         ,71527559D   ,PICT        ,B120053220

            SEM.:1                                                      SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 12    37   49  P C   404189   MOBILE COMMUNICATION         PP 100 40 17    35   52  P
    404182   COMPUTER NETWORKS            PP 100 40 20    40   60  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 24    36   60  P
    404183   MICROWAVE ENGINEERING        PP 100 40 19    39   58  P C   404191D  SOFT COMPUTING               PP 100 40 23    39   62  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100 40 16    48   64  P C   404192D  WIRELESS NETWORKS            PP 100 40 23    29   52  P
    404185D  ARTIFICIAL INTELLIGENCE      PP 100 40 15    39   54  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50 20         42  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50 20         47  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50 20         42  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50 20         33  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50 20         34  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20         39  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50 20         33  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20         46  P C   404195   PROJECT PHASE II             TW 100 40         95  P
    404188   PROJECT PHASE I              OR  50 20         41  P C   404195   PROJECT PHASE II             PR  50 20         46  P

GRAND TOTAL = 1009/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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     B120053221   SANAGAR SNEHA PRAMOD   PRAMODINI   ,71527561F   ,PICT   ,B120053221

              SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 15   39   54 P C   404189   MOBILE COMMUNICATION   PP 100 40 19   43   62 P
     404182   COMPUTER NETWORKS   PP 100 40 22   45   67 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 27   50   77 P
     404183   MICROWAVE ENGINEERING   PP 100 40 22   39   61 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 15   45   60 P
     404184A DIGITAL IMAGE PROCESSING   PP 100 40 24   46   70 P C   404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 16   50   66 P
     404185B ELECTRONIC PRODUCT DESIGN   PP 100 40 18   37   55 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   44 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   43 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   44 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   32 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   44 P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   43 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   43 P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   43 P C   404195   PROJECT PHASE II   TW 100 40   93 P
     404188   PROJECT PHASE I   OR 50 20   33 P C   404195   PROJECT PHASE II   PR 50 20   43 P

  GRAND TOTAL = 1077 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION   [& O.163]
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     B120053222   SANSARE TEJASVINI NANDKISHOR   MAYA   ,71527569M   ,PICT   ,B120053222

              SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 12   44   56 P C   404189   MOBILE COMMUNICATION   PP 100 40 26   48   74 P
     404182   COMPUTER NETWORKS   PP 100 40 22   49   71 P   404190   BROADBAND COMMUNICATION SYS. PP 100 40 22   47   69 P
     404183   MICROWAVE ENGINEERING   PP 100 40 23   45   68 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 20   50   70 P
     404184A DIGITAL IMAGE PROCESSING   PP 100 40 19   67   86 P C   404192D WIRELESS NETWORKS   PP 100 40 21   42   63 P
     404185C PLCS AND AUTOMATION   PP 100 40 17   37   54 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   39 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   41 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   41 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   33 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   42 P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   39 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   37 P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   39 P C   404195   PROJECT PHASE II   TW 100 40   50 P
     404188   PROJECT PHASE I   OR 50 20   34 P C   404195   PROJECT PHASE II   PR 50 20   25 P

  GRAND TOTAL = 1031/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053223   SANYASI SARTHAK DILIP   SHUBHANGI   ,71527570E   ,PICT   ,B120053223

              SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 14   49   63 P C   404189   MOBILE COMMUNICATION   PP 100 40 23   49   72 P
     404182   COMPUTER NETWORKS   PP 100 40 10   42   52 P   404190   BROADBAND COMMUNICATION SYS. PP 100 40 26   45   71 P
     404183   MICROWAVE ENGINEERING   PP 100 40 25   44   69 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 17   54   71 P
     404184A DIGITAL IMAGE PROCESSING   PP 100 40 22   52   74 P C   404192D WIRELESS NETWORKS   PP 100 40 28   48   76 P
     404185C PLCS AND AUTOMATION   PP 100 40 13   46   59 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   37 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   39 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   40 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   30 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   35 P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   42 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   38 P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   38 P C   404195   PROJECT PHASE II   TW 100 40   93 P
     404188   PROJECT PHASE I   OR 50 20   38 P C   404195   PROJECT PHASE II   PR 50 20   43 P

  GRAND TOTAL = 1080/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
  OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    B120053224   SAOJI AADITI VIVEK                      ANAGHA              ,71527571C   ,PICT        ,B120053224

              SEM.:1                                                    SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  10     53   63  P C   404189   MOBILE COMMUNICATION           PP 100  40  26     53   79  P
    404182   COMPUTER NETWORKS            PP 100  40  22     44   66  P C   404190   BROADBAND COMMUNICATION SYS.   PP 100  40  26     50   76  P
    404183   MICROWAVE ENGINEERING        PP 100  40  23     47   70  P C   404191C  AUDIO VIDEO ENGINEERING        PP 100  40  21     48   69  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  23     60   83  P C   404192A  BIOMEDICAL SIGNAL PROCESSING   PP 100  40  26     46   72  P
    404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  19     60   79  P C   404193   LAB PRACTICE III(MC & BCS)     TW  50  20                45  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              41  P C   404193   LAB PRACTICE III(MC & BCS)     OR  50  20                45  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)     TW  50  20                45  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              45  P C   404194   LAB PRAC. IV(ELECTIVE III)     PR  50  20                45  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              47  P C   404195   PROJECT PHASE II               TW 100  40                97  P
    404188   PROJECT PHASE I              OR  50  20              47  P C   404195   PROJECT PHASE II               PR  50  20                47  P

  GRAND TOTAL = 1203/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053225   SARDA YASH RAMANKUMAR                   ARCHANA             ,71527573K   ,PICT        ,B120053225

              SEM.:1                                                    SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  15     44   59  P C   404189   MOBILE COMMUNICATION           PP 100  40  23     49   72  P
    404182   COMPUTER NETWORKS            PP 100  40  23     29   52  P C   404190   BROADBAND COMMUNICATION SYS.   PP 100  40  27     46   73  P
    404183   MICROWAVE ENGINEERING        PP 100  40  22     52   74  P C   404191C  AUDIO VIDEO ENGINEERING        PP 100  40  16     48   64  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  20     53   73  P C   404192D  WIRELESS NETWORKS              PP 100  40  28     39   67  P
    404185C  PLCS AND AUTOMATION          PP 100  40  17     50   67  P C   404193   LAB PRACTICE III(MC & BCS)     TW  50  20                43  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              39  P C   404193   LAB PRACTICE III(MC & BCS)     OR  50  20                40  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              39  P C   404194   LAB PRAC. IV(ELECTIVE III)     TW  50  20                42  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              43  P C   404194   LAB PRAC. IV(ELECTIVE III)     PR  50  20                36  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              34  P C   404195   PROJECT PHASE II               TW 100  40                85  P
    404188   PROJECT PHASE I              OR  50  20              42  P C   404195   PROJECT PHASE II               PR  50  20                40  P

  GRAND TOTAL = 1084/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053226   SHAH YASH NARESH                        SUREKHA             ,71527583G   ,PICT        ,B120053226

              SEM.:1                                                    SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  19     43   62  P C   404189   MOBILE COMMUNICATION           PP 100  40  25     40   65  P
    404182   COMPUTER NETWORKS            PP 100  40  20     31   51  P C   404190   BROADBAND COMMUNICATION SYS.   PP 100  40  24     42   66  P
    404183   MICROWAVE ENGINEERING        PP 100  40  23     40   63  P C   404191D  SOFT COMPUTING                 PP 100  40  24     37   61  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  15     52   67  P C   404192A  BIOMEDICAL SIGNAL PROCESSING   PP 100  40  21     36   57  P
    404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  15     50   65  P C   404193   LAB PRACTICE III(MC & BCS)     TW  50  20                35  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              42  P C   404193   LAB PRACTICE III(MC & BCS)     OR  50  20                42  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              37  P C   404194   LAB PRAC. IV(ELECTIVE III)     TW  50  20                31  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              31  P C   404194   LAB PRAC. IV(ELECTIVE III)     PR  50  20                35  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              45  P C   404195   PROJECT PHASE II               TW 100  40                96  P
    404188   PROJECT PHASE I              OR  50  20              40  P C   404195   PROJECT PHASE II               PR  50  20                46  P

  GRAND TOTAL = 1037/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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      B120053227   SHAIKH AMIR AQUEEL   IFFAT   ,71527584E   ,PICT   ,B120053227

              SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 12   46   58 P C   404189   MOBILE COMMUNICATION   PP 100 40 23   46   69 P
      404182   COMPUTER NETWORKS   PP 100 40 26   34   60 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 29   41   70 P
      404183   MICROWAVE ENGINEERING   PP 100 40 17   48   65 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 19   51   70 P
      404184A DIGITAL IMAGE PROCESSING   PP 100 40 22   53   75 P C   404192D WIRELESS NETWORKS   PP 100 40 28   47   75 P
      404185D ARTIFICIAL INTELLIGENCE   PP 100 40 20   53   73 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   44 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   44 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   40 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   38 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   40 P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   43 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   37 P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   35 P C   404195   PROJECT PHASE II   TW 100 40   88 P
      404188   PROJECT PHASE I   OR 50 20   40 P C   404195   PROJECT PHASE II   PR 50 20   40 P

 GRAND TOTAL = 1104/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053228   SHANU KUMAR   MANJU DEVI   ,71527590K   ,PICT   ,B120053228

              SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 13   59   72 P C   404189   MOBILE COMMUNICATION   PP 100 40 25   49   74 P
      404182   COMPUTER NETWORKS   PP 100 40 26   39   65 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 26   52   78 P
      404183   MICROWAVE ENGINEERING   PP 100 40 22   53   75 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 22   52   74 P
      404184B EMBEDDED SYSTEMS & RTOS   PP 100 40 22   51   73 P C   404192D WIRELESS NETWORKS   PP 100 40 30   46   76 P
      404185C PLCS AND AUTOMATION   PP 100 40 19   57   76 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   40 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   42 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   41 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   43 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   35 P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   38 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   41 P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   48 P C   404195   PROJECT PHASE II   TW 100 40   96 P
      404188   PROJECT PHASE I   OR 50 20   42 P C   404195   PROJECT PHASE II   PR 50 20   45 P

 GRAND TOTAL = 1174/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053229   SHEFALI GARG   VANDNA   ,71527595L   ,PICT   ,B120053229

              SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 15   48   63 P C   404189   MOBILE COMMUNICATION   PP 100 40 29   47   76 P
      404182   COMPUTER NETWORKS   PP 100 40 17   42   59 P   404190   BROADBAND COMMUNICATION SYS. PP 100 40 22   53   75 P
      404183   MICROWAVE ENGINEERING   PP 100 40 21   45   66 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 19   50   69 P
      404184A DIGITAL IMAGE PROCESSING   PP 100 40 25   54   79 P C   404192D WIRELESS NETWORKS   PP 100 40 23   41   64 P
      404185C PLCS AND AUTOMATION   PP 100 40 17   54   71 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   46 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   41 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   42 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   43 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   44 P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   39 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   44 P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   38 P C   404195   PROJECT PHASE II   TW 100 40   94 P
      404188   PROJECT PHASE I   OR 50 20   40 P C   404195   PROJECT PHASE II   PR 50 20   43 P

 GRAND TOTAL = 1136/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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       B120053230   SHEJULE RUPALI ARJUNRAO              NANDA              ,71633657L   ,PICT       ,B120053230

              SEM.:1                                                          SEM.:2
       404181  VLSI DESIGN & TECHNOLOGY      PP 100  40  16    57   73  P C   404189  MOBILE COMMUNICATION          PP 100  40  22    46   68  P
       404182  COMPUTER NETWORKS             PP 100  40  26    30   56  P C   404190  BROADBAND COMMUNICATION SYS.  PP 100  40  26    48   74  P
       404183  MICROWAVE ENGINEERING         PP 100  40  18    48   66  P C   404191C AUDIO VIDEO ENGINEERING       PP 100  40  21    53   74  P
       404184A DIGITAL IMAGE PROCESSING      PP 100  40  21    49   70  P C   404192D WIRELESS NETWORKS             PP 100  40  26    43   69  P
       404185C PLCS AND AUTOMATION           PP 100  40  12    58   70  P C   404193  LAB PRACTICE III(MC & BCS)    TW  50  20             40  P
       404186  LAB PRACTICE I (CN & MWE)     TW  50  20             46  P C   404193  LAB PRACTICE III(MC & BCS)    OR  50  20             42  P
       404186  LAB PRACTICE I (CN & MWE)     OR  50  20             44  P C   404194  LAB PRAC. IV(ELECTIVE III)    TW  50  20             34  P
       404187  LAB PRAC. II(VLSI &ELEC. I)   TW  50  20             39  P C   404194  LAB PRAC. IV(ELECTIVE III)    PR  50  20             37  P
       404187  LAB PRAC. II(VLSI &ELEC. I)   PR  50  20             37  P C   404195  PROJECT PHASE II              TW 100  40             93  P
       404188  PROJECT PHASE I               OR  50  20             43  P C   404195  PROJECT PHASE II              PR  50  20             45  P

    GRAND TOTAL = 1120/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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       B120053231   SHEJWAL SAYALI VINAYAK               ANITA              ,71633658J   ,PICT       ,B120053231

              SEM.:1                                                          SEM.:2
       404181  VLSI DESIGN & TECHNOLOGY      PP 100  40  16    51   67  P C   404189  MOBILE COMMUNICATION          PP 100  40  25    53   78  P
       404182  COMPUTER NETWORKS             PP 100  40  27    48   75  P C   404190  BROADBAND COMMUNICATION SYS.  PP 100  40  28    50   78  P
       404183  MICROWAVE ENGINEERING         PP 100  40  25    49   74  P C   404191C AUDIO VIDEO ENGINEERING       PP 100  40  21    56   77  P
       404184A DIGITAL IMAGE PROCESSING      PP 100  40  23    46   69  P C   404192D WIRELESS NETWORKS             PP 100  40  29    44   73  P
       404185C PLCS AND AUTOMATION           PP 100  40  15    54   69  P C   404193  LAB PRACTICE III(MC & BCS)    TW  50  20             41  P
       404186  LAB PRACTICE I (CN & MWE)     TW  50  20             45  P C   404193  LAB PRACTICE III(MC & BCS)    OR  50  20             42  P
       404186  LAB PRACTICE I (CN & MWE)     OR  50  20             41  P C   404194  LAB PRAC. IV(ELECTIVE III)    TW  50  20             42  P
       404187  LAB PRAC. II(VLSI &ELEC. I)   TW  50  20             44  P C   404194  LAB PRAC. IV(ELECTIVE III)    PR  50  20             39  P
       404187  LAB PRAC. II(VLSI &ELEC. I)   PR  50  20             44  P C   404195  PROJECT PHASE II              TW 100  40             93  P
       404188  PROJECT PHASE I               OR  50  20             40  P C   404195  PROJECT PHASE II              PR  50  20             45  P

    GRAND TOTAL = 1176/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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       B120053232   SHELKE AMIT KIRAN                    KANCHAN            ,71527598E   ,PICT       ,B120053232

              SEM.:1                                                          SEM.:2
       404181  VLSI DESIGN & TECHNOLOGY      PP 100  40  13    46   59  P C   404189  MOBILE COMMUNICATION          PP 100  40  22    48   70  P
       404182  COMPUTER NETWORKS             PP 100  40  17    34   51  P C   404190  BROADBAND COMMUNICATION SYS.  PP 100  40  27    43   70  P
       404183  MICROWAVE ENGINEERING         PP 100  40  21    45   66  P C   404191C AUDIO VIDEO ENGINEERING       PP 100  40  26    45   71  P
       404184B EMBEDDED SYSTEMS & RTOS       PP 100  40  21    41   62  P C   404192D WIRELESS NETWORKS             PP 100  40  27    39   66  P
       404185B ELECTRONIC PRODUCT DESIGN     PP 100  40  16    46   62  P C   404193  LAB PRACTICE III(MC & BCS)    TW  50  20             48  P
       404186  LAB PRACTICE I (CN & MWE)     TW  50  20             36  P C   404193  LAB PRACTICE III(MC & BCS)    OR  50  20             40  P
       404186  LAB PRACTICE I (CN & MWE)     OR  50  20             36  P C   404194  LAB PRAC. IV(ELECTIVE III)    TW  50  20             45  P
       404187  LAB PRAC. II(VLSI &ELEC. I)   TW  50  20             41  P C   404194  LAB PRAC. IV(ELECTIVE III)    PR  50  20             40  P
       404187  LAB PRAC. II(VLSI &ELEC. I)   PR  50  20             31  P C   404195  PROJECT PHASE II              TW 100  40             88  P
       404188  PROJECT PHASE I               OR  50  20             35  P C   404195  PROJECT PHASE II              PR  50  20             33  P

    GRAND TOTAL = 1050/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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      B120053233   SHETI SWAPNIL KAILAS   PUSHPA   ,71527601J   ,PICT   ,B120053233

              SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 12   48   60 P C   404189   MOBILE COMMUNICATION   PP 100 40 22   29   51 P
      404182   COMPUTER NETWORKS   PP 100 40 22   32   54 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 25   48   73 P
      404183   MICROWAVE ENGINEERING   PP 100 40 17   42   59 P C   404191D SOFT COMPUTING   PP 100 40 24   43   67 P
      404184B EMBEDDED SYSTEMS & RTOS   PP 100 40 11   34   45 P C   404192D WIRELESS NETWORKS   PP 100 40 25   32   57 P
      404185D ARTIFICIAL INTELLIGENCE   PP 100 40 11   49   60 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   37 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   37 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   37 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   39 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   39 P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   44 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   39 P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   36 P C   404195   PROJECT PHASE II   TW 100 40   93 P
      404188   PROJECT PHASE I   OR 50 20   40 P C   404195   PROJECT PHASE II   PR 50 20   42 P

   GRAND TOTAL = 1009/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053234   SHINDE PRANALI SURYAKANT   SAROJINI   ,71633659G   ,PICT   ,B120053234

              SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 13   43   56 P C   404189   MOBILE COMMUNICATION   PP 100 40 18   45   63 P
      404182   COMPUTER NETWORKS   PP 100 40 26   35   61 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 26   54   80 P
      404183   MICROWAVE ENGINEERING   PP 100 40 15   55   70 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 20   50   70 P
      404184A DIGITAL IMAGE PROCESSING   PP 100 40 22   43   65 P C   404192D WIRELESS NETWORKS   PP 100 40 28   48   76 P
      404185C PLCS AND AUTOMATION   PP 100 40 19   51   70 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   38 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   42 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   40 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   41 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   41 P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   40 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   40 P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   40 P C   404195   PROJECT PHASE II   TW 100 40   90 P
      404188   PROJECT PHASE I   OR 50 20   33 P C   404195   PROJECT PHASE II   PR 50 20   42 P

   GRAND TOTAL = 1098/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053235   SHINDE SHAMAL VIKRAM   POOJA   ,71633660L   ,PICT   ,B120053235

              SEM.:1   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 17   51   68 P C   404189   MOBILE COMMUNICATION   PP 100 40 25   49   74 P
      404182   COMPUTER NETWORKS   PP 100 40 28   43   71 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 29   59   88 P
      404183   MICROWAVE ENGINEERING   PP 100 40 26   51   77 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 23   52   75 P
      404184A DIGITAL IMAGE PROCESSING   PP 100 40 23   51   74 P C   404192D WIRELESS NETWORKS   PP 100 40 29   48   77 P
      404185C PLCS AND AUTOMATION   PP 100 40 22   54   76 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   40 P
      404186   LAB PRACTICE I (CN & MWE)   TW 50 20   45 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   40 P
      404186   LAB PRACTICE I (CN & MWE)   OR 50 20   43 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   42 P
      404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   40 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   42 P
      404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   40 P C   404195   PROJECT PHASE II   TW 100 40   95 P
      404188   PROJECT PHASE I   OR 50 20   43 P C   404195   PROJECT PHASE II   PR 50 20   44 P

   GRAND TOTAL = 1194/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053236   SHINDE SNEHAL RAMESH                   NANDA              ,71527604C   ,PICT       ,B120053236

            SEM.:1                                                 SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  18    44   62  P C   404189   MOBILE COMMUNICATION         PP 100  40  24   54   78  P
    404182   COMPUTER NETWORKS           PP 100  40  22    53   75  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  28   59   87  P
    404183   MICROWAVE ENGINEERING       PP 100  40  19    56   75  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  18   47   65  P
    404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  21    40   61  P C   404192D  WIRELESS NETWORKS            PP 100  40  28   47   75  P
    404185C  PLCS AND AUTOMATION         PP 100  40  14    49   63  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20             38  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20             38  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20             41  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20             42  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20             42  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             43  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20             38  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             42  P C   404195   PROJECT PHASE II             TW 100  40             95  P
    404188   PROJECT PHASE I             OR  50  20             40  P C   404195   PROJECT PHASE II             PR  50  20             46  P

  GRAND TOTAL = 1146/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053237   SHINDE SUMEET CHANDRAKANT              ANITA              ,71527606K   ,PICT       ,B120053237

            SEM.:1                                                 SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  15    48   63  P C   404189   MOBILE COMMUNICATION         PP 100  40  20   47   67  P
    404182   COMPUTER NETWORKS           PP 100  40  19    29   48  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  21   44   65  P
    404183   MICROWAVE ENGINEERING       PP 100  40  AA    49   49  P     404191D  SOFT COMPUTING               PP 100  40  20   37   57  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100  40  24    43   67  P C   404192D  WIRELESS NETWORKS            PP 100  40  22   36   58  P
    404185D  ARTIFICIAL INTELLIGENCE     PP 100  40  18    41   59  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20             41  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20             40  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20             38  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20             43  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20             30  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             28  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20             40  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             36  P C   404195   PROJECT PHASE II             TW 100  40             95  P
    404188   PROJECT PHASE I             OR  50  20             42  P C   404195   PROJECT PHASE II             PR  50  20             46  P

  GRAND TOTAL = 1012/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053238   SHIRALKAR APURVA ASHOK                 SUNITA             ,71633661J   ,PICT       ,B120053238

            SEM.:1                                                 SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  19    51   70  P C   404189   MOBILE COMMUNICATION         PP 100  40  21   44   65  P
    404182   COMPUTER NETWORKS           PP 100  40  28    46   74  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  25   51   76  P
    404183   MICROWAVE ENGINEERING       PP 100  40  21    53   74  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  19   51   70  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100  40  22    43   65  P C   404192D  WIRELESS NETWORKS            PP 100  40  30   49   79  P
    404185B  ELECTRONIC PRODUCT DESIGN   PP 100  40  22    52   74  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20             36  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20             44  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20             38  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20             40  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20             38  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             40  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20             37  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             35  P C   404195   PROJECT PHASE II             TW 100  40             80  P
    404188   PROJECT PHASE I             OR  50  20             37  P C   404195   PROJECT PHASE II             PR  50  20             32  P

  GRAND TOTAL = 1104/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053239   SHIRWAR GLOBAL NAVINKUMAR              ESTHER              ,71633662G   ,PICT        ,B120053239

             SEM.:1                                                      SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  21    52    73  P C   404189   MOBILE COMMUNICATION            PP 100  40  22    49    71  P
     404182   COMPUTER NETWORKS             PP 100  40  27    32    59  P C   404190   BROADBAND COMMUNICATION SYS.    PP 100  40  26    50    76  P
     404183   MICROWAVE ENGINEERING         PP 100  40  22    53    75  P C   404191C  AUDIO VIDEO ENGINEERING         PP 100  40  22    55    77  P
     404184A  DIGITAL IMAGE PROCESSING      PP 100  40  19    48    67  P C   404192D  WIRELESS NETWORKS               PP 100  40  30    49    79  P
     404185B  ELECTRONIC PRODUCT DESIGN     PP 100  40  20    54    74  P C   404193   LAB PRACTICE III(MC & BCS)      TW  50  20                39  P
     404186   LAB PRACTICE I (CN & MWE)     TW  50  20              43  P C   404193   LAB PRACTICE III(MC & BCS)      OR  50  20                40  P
     404186   LAB PRACTICE I (CN & MWE)     OR  50  20              38  P C   404194   LAB PRAC. IV(ELECTIVE III)      TW  50  20                24  P
     404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              40  P C   404194   LAB PRAC. IV(ELECTIVE III)      PR  50  20                39  P
     404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              36  P C   404195   PROJECT PHASE II                TW 100  40                96  P
     404188   PROJECT PHASE I               OR  50  20              43  P C   404195   PROJECT PHASE II                PR  50  20                45  P

   GRAND TOTAL = 1134/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053240   SHITOLE ANMOL SUNIL                   VAISHALI            ,71633663E   ,PICT        ,B120053240

             SEM.:1                                                      SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  16    51    67  P C   404189   MOBILE COMMUNICATION            PP 100  40  24    49    73  P
     404182   COMPUTER NETWORKS             PP 100  40  20    46    66  P C   404190   BROADBAND COMMUNICATION SYS.    PP 100  40  19    54    73  P
     404183   MICROWAVE ENGINEERING         PP 100  40  18    55    73  P C   404191C  AUDIO VIDEO ENGINEERING         PP 100  40  25    49    74  P
     404184B  EMBEDDED SYSTEMS & RTOS       PP 100  40  14    44    58  P C   404192D  WIRELESS NETWORKS               PP 100  40  29    34    63  P
     404185C  PLCS AND AUTOMATION           PP 100  40  21    57    78  P C   404193   LAB PRACTICE III(MC & BCS)      TW  50  20                40  P
     404186   LAB PRACTICE I (CN & MWE)     TW  50  20              42  P C   404193   LAB PRACTICE III(MC & BCS)      OR  50  20                38  P
     404186   LAB PRACTICE I (CN & MWE)     OR  50  20              36  P C   404194   LAB PRAC. IV(ELECTIVE III)      TW  50  20                42  P
     404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              37  P C   404194   LAB PRAC. IV(ELECTIVE III)      PR  50  20                39  P
     404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              33  P C   404195   PROJECT PHASE II                TW 100  40                95  P
     404188   PROJECT PHASE I               OR  50  20              40  P C   404195   PROJECT PHASE II                PR  50  20                44  P

   GRAND TOTAL = 1111/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053241   SHIYALE SACHIT SHRAWANJI              CHHABBUTAI          ,71633664C   ,PICT        ,B120053241

             SEM.:1                                                      SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  14    31    45  P C   404189   MOBILE COMMUNICATION            PP 100  40  22    44    66  P
     404182   COMPUTER NETWORKS             PP 100  40  25    49    74  P C   404190   BROADBAND COMMUNICATION SYS.    PP 100  40  21    38    59  P
     404183   MICROWAVE ENGINEERING         PP 100  40  20    43    63  P C   404191C  AUDIO VIDEO ENGINEERING         PP 100  40  17    50    67  P
     404184A  DIGITAL IMAGE PROCESSING      PP 100  40  18    43    61  P C   404192D  WIRELESS NETWORKS               PP 100  40  28    36    64  P
     404185C  PLCS AND AUTOMATION           PP 100  40  13    53    66  P C   404193   LAB PRACTICE III(MC & BCS)      TW  50  20                42  P
     404186   LAB PRACTICE I (CN & MWE)     TW  50  20              42  P C   404193   LAB PRACTICE III(MC & BCS)      OR  50  20                38  P
     404186   LAB PRACTICE I (CN & MWE)     OR  50  20              37  P C   404194   LAB PRAC. IV(ELECTIVE III)      TW  50  20                34  P
     404187   LAB PRAC. II(VLSI &ELEC. I)   TW  50  20              42  P C   404194   LAB PRAC. IV(ELECTIVE III)      PR  50  20                38  P
     404187   LAB PRAC. II(VLSI &ELEC. I)   PR  50  20              29  P C   404195   PROJECT PHASE II                TW 100  40                86  P
     404188   PROJECT PHASE I               OR  50  20              38  P C   404195   PROJECT PHASE II                PR  50  20                40  P

   GRAND TOTAL = 1031/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053242   SHREYAS HEMANT NAZARE              KALPANA              ,71527613B   ,PICT       ,B120053242

             SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  15     60   75  P C   404189   MOBILE COMMUNICATION         PP 100  40  27     54   81  P
    404182   COMPUTER NETWORKS           PP 100  40  26     44   70  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  28     49   77  P
    404183   MICROWAVE ENGINEERING       PP 100  40  23     44   67  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  25     53   78  P
    404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  22     44   66  P C   404192D  WIRELESS NETWORKS            PP 100  40  27     50   77  P
    404185D  ARTIFICIAL INTELLIGENCE     PP 100  40  18     56   74  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              44  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20               45  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              40  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20               43  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              34  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20               47  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              40  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20               34  P C   404195   PROJECT PHASE II             TW 100  40              96  P
    404188   PROJECT PHASE I             OR  50  20               46  P C   404195   PROJECT PHASE II             PR  50  20              44  P

 GRAND TOTAL = 1178/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053243   SHRUTI ATUL MALI                  JAYSHREE MALI        ,71527615J   ,PICT       ,B120053243

             SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  13     43   56  P C   404189   MOBILE COMMUNICATION         PP 100  40  25     48   73  P
    404182   COMPUTER NETWORKS           PP 100  40  24     51   75  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  28     39   67  P
    404183   MICROWAVE ENGINEERING       PP 100  40  13     35   48  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  19     54   73  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100  40  18     39   57  P C   404192A  BIOMEDICAL SIGNAL PROCESSING PP 100  40  27     57   84  P
    404185C  PLCS AND AUTOMATION         PP 100  40  25     44   69  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              40  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20               38  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              41  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20               36  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              38  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20               32  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              40  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20               34  P C   404195   PROJECT PHASE II             TW 100  40              94  P
    404188   PROJECT PHASE I             OR  50  20               41  P C   404195   PROJECT PHASE II             PR  50  20              45  P

 GRAND TOTAL = 1081/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053244   SHUBHAM JAKHOTIYA                 ANJU                 ,71527616G   ,PICT       ,B120053244

             SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  10     41   51  P C   404189   MOBILE COMMUNICATION         PP 100  40  18     28   46  P
    404182   COMPUTER NETWORKS           PP 100  40  05     41   46  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  12     36   48  P
    404183   MICROWAVE ENGINEERING       PP 100  40  15     29   44  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  12     51   63  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100  40  17     42   59  P C   404192D  WIRELESS NETWORKS            PP 100  40  22     31   53  P
    404185C  PLCS AND AUTOMATION         PP 100  40  14     31   45  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              28  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20               42  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              39  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20               35  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              28  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20               32  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              33  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20               28  P C   404195   PROJECT PHASE II             TW 100  40              90  P
    404188   PROJECT PHASE I             OR  50  20               35  P C   404195   PROJECT PHASE II             PR  50  20              40  P

 GRAND TOTAL = 885/1500 , RESULT:HIGHER SECOND CLASS
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     NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
     OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053245   SHUKLA MAMTA RAMENDRA                   CHANDA              ,71527618C   ,PICT       ,B120053245

             SEM.:1                                                          SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  14    55    69  P C   404189   MOBILE COMMUNICATION           PP 100  40  29    37    66  P
     404182   COMPUTER NETWORKS           PP 100  40  26    43    69  P C   404190   BROADBAND COMMUNICATION SYS.   PP 100  40  27    54    81  P
     404183   MICROWAVE ENGINEERING       PP 100  40  19    37    56  P C   404191C  AUDIO VIDEO ENGINEERING        PP 100  40  22    48    70  P
     404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  25    49    74  P C   404192A  BIOMEDICAL SIGNAL PROCESSING   PP 100  40  28    54    82  P
     404185B  ELECTRONIC PRODUCT DESIGN   PP 100  40  23    49    72  P C   404193   LAB PRACTICE III(MC & BCS)     TW  50  20              43  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50  20              36  P C   404193   LAB PRACTICE III(MC & BCS)     OR  50  20              40  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50  20              45  P C   404194   LAB PRAC. IV(ELECTIVE III)     TW  50  20              43  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              37  P C   404194   LAB PRAC. IV(ELECTIVE III)     PR  50  20              42  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              42  P C   404195   PROJECT PHASE II               TW 100  40              95  P
     404188   PROJECT PHASE I             OR  50  20              44  P C   404195   PROJECT PHASE II               PR  50  20              45  P

     GRAND TOTAL = 1151/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053246   SHWETA IYER                             GITA                ,71527621C   ,PICT       ,B120053246

             SEM.:1                                                          SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  12    50    62  P C   404189   MOBILE COMMUNICATION           PP 100  40  25    42    67  P
     404182   COMPUTER NETWORKS           PP 100  40  26    45    71  P C   404190   BROADBAND COMMUNICATION SYS.   PP 100  40  24    53    77  P
     404183   MICROWAVE ENGINEERING       PP 100  40  23    33    56  P C   404191C  AUDIO VIDEO ENGINEERING        PP 100  40  21    48    69  P
     404184A  DIGITAL IMAGE PROCESSING    PP 100  40  27    38    65  P C   404192D  WIRELESS NETWORKS              PP 100  40  26    44    70  P
     404185C  PLCS AND AUTOMATION         PP 100  40  22    41    63  P C   404193   LAB PRACTICE III(MC & BCS)     TW  50  20              40  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50  20              45  P C   404193   LAB PRACTICE III(MC & BCS)     OR  50  20              42  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50  20              38  P C   404194   LAB PRAC. IV(ELECTIVE III)     TW  50  20              42  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              39  P C   404194   LAB PRAC. IV(ELECTIVE III)     PR  50  20              42  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              45  P C   404195   PROJECT PHASE II               TW 100  40              95  P
     404188   PROJECT PHASE I             OR  50  20              45  P C   404195   PROJECT PHASE II               PR  50  20              46  P

     GRAND TOTAL = 1119/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053247   SINGH NAVIN BACHCHAN SINGH              LALSA               ,71527623K   ,PICT       ,B120053247

             SEM.:1                                                          SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  19    40    59  P C   404189   MOBILE COMMUNICATION           PP 100  40  18    42    60  P
     404182   COMPUTER NETWORKS           PP 100  40  21    38    59  P C   404190   BROADBAND COMMUNICATION SYS.   PP 100  40  24    50    74  P
     404183   MICROWAVE ENGINEERING       PP 100  40  18    34    52  P C   404191C  AUDIO VIDEO ENGINEERING        PP 100  40  19    41    60  P
     404184A  DIGITAL IMAGE PROCESSING    PP 100  40  16    43    59  P C   404192D  WIRELESS NETWORKS              PP 100  40  28    36    64  P
     404185C  PLCS AND AUTOMATION         PP 100  40  19    39    58  P C   404193   LAB PRACTICE III(MC & BCS)     TW  50  20              38  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50  20              40  P C   404193   LAB PRACTICE III(MC & BCS)     OR  50  20              39  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50  20              36  P C   404194   LAB PRAC. IV(ELECTIVE III)     TW  50  20              40  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20              36  P C   404194   LAB PRAC. IV(ELECTIVE III)     PR  50  20              37  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20              38  P C   404195   PROJECT PHASE II               TW 100  40              50  P
     404188   PROJECT PHASE I             OR  50  20              31  P C   404195   PROJECT PHASE II               PR  50  20              25  P

     GRAND TOTAL = 955/1500 , RESULT:FIRST CLASS
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     NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
     OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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       B120053248   SINGH VIKAS RAJESH                    SUNITA              ,71527624H   ,PICT       ,B120053248

                  SEM.:1                                                    SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 12     41   53  P C   404189   MOBILE COMMUNICATION        PP 100 40 20     40   60  P
        404182   COMPUTER NETWORKS           PP 100 40 24     38   62  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 27     41   68  P
        404183   MICROWAVE ENGINEERING       PP 100 40 19     33   52  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100 40 20     43   63  P
        404184A  DIGITAL IMAGE PROCESSING    PP 100 40 14     34   48  P C   404192D  WIRELESS NETWORKS           PP 100 40 25     33   58  P
        404185C  PLCS AND AUTOMATION         PP 100 40 12     39   51  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50 20               43  P
        404186   LAB PRACTICE I (CN & MWE)   TW  50 20             38  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50 20               38  P
        404186   LAB PRACTICE I (CN & MWE)   OR  50 20             37  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50 20               34  P
        404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20             42  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50 20               39  P
        404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20             36  P C   404195   PROJECT PHASE II            TW 100 40               94  P
        404188   PROJECT PHASE I             OR  50 20             42  P C   404195   PROJECT PHASE II            PR  50 20               44  P

     GRAND TOTAL = 1002/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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       B120053249   SOMVANSHI KETAN VISHVANATH            SUNITA              ,71527628L   ,PICT       ,B120053249

                  SEM.:1                                                    SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 12     50   62  P C   404189   MOBILE COMMUNICATION        PP 100 40 29     45   74  P
        404182   COMPUTER NETWORKS           PP 100 40 26     40   66  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 23     46   69  P
        404183   MICROWAVE ENGINEERING       PP 100 40 21     41   62  P C   404191D  SOFT COMPUTING              PP 100 40 22     45   67  P
        404184B  EMBEDDED SYSTEMS & RTOS     PP 100 40 18     38   56  P C   404192D  WIRELESS NETWORKS           PP 100 40 28     45   73  P
        404185D  ARTIFICIAL INTELLIGENCE     PP 100 40 19     52   71  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50 20               41  P
        404186   LAB PRACTICE I (CN & MWE)   TW  50 20             34  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50 20               42  P
        404186   LAB PRACTICE I (CN & MWE)   OR  50 20             42  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50 20               34  P
        404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20             37  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50 20               41  P
        404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20             39  P C   404195   PROJECT PHASE II            TW 100 40               97  P
        404188   PROJECT PHASE I             OR  50 20             43  P C   404195   PROJECT PHASE II            PR  50 20               48  P

     GRAND TOTAL = 1098/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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       B120053250   SREERADH SREEKUMAR                    RADHIKA             ,71527634E   ,PICT       ,B120053250

                  SEM.:1                                                    SEM.:2
        404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 04     32   36# P   404189   MOBILE COMMUNICATION        PP 100 40 19     33   52  P
        404182   COMPUTER NETWORKS           PP 100 40 21     35   56  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 16     35   51  P
        404183   MICROWAVE ENGINEERING       PP 100 40 13     28   41  P C   404191C  AUDIO VIDEO ENGINEERING     PP 100 40 12     38   50  P
        404184A  DIGITAL IMAGE PROCESSING    PP 100 40 04     40   44  P     404192D  WIRELESS NETWORKS           PP 100 40 21     31   52  P
        404185C  PLCS AND AUTOMATION         PP 100 40 08     34   42  P C   404193   LAB PRACTICE III(MC & BCS)  TW  50 20               36  P
        404186   LAB PRACTICE I (CN & MWE)   TW  50 20             36  P C   404193   LAB PRACTICE III(MC & BCS)  OR  50 20               30  P
        404186   LAB PRACTICE I (CN & MWE)   OR  50 20             37  P C   404194   LAB PRAC. IV(ELECTIVE III)  TW  50 20               36  P
        404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20             38  P C   404194   LAB PRAC. IV(ELECTIVE III)  PR  50 20               34  P
        404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20             36  P C   404195   PROJECT PHASE II            TW 100 40               43  P
        404188   PROJECT PHASE I             OR  50 20             29  P C   404195   PROJECT PHASE II            PR  50 20               21  P

     GRAND TOTAL = 800/1500 , RESULT:SECOND CLASS   [# O.4]
                                                     RESULT RESERVED FOR BACKLOGS.
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    NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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     B120053251   SRINJAY SOUMITRA SARKAR   BHAKTI   ,71527635C   ,PICT   ,B120053251

              SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 17   44   61 P C   404189   MOBILE COMMUNICATION   PP 100 40 17   32   49 P
     404182   COMPUTER NETWORKS   PP 100 40 19   41   60 P   404190   BROADBAND COMMUNICATION SYS. PP 100 40 21   40   61 P
     404183   MICROWAVE ENGINEERING   PP 100 40 17   37   54 P C   404191D SOFT COMPUTING   PP 100 40 14   28   42 P
     404184B EMBEDDED SYSTEMS & RTOS   PP 100 40 16   36   52 P C   404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 21   35   56 P
     404185D ARTIFICIAL INTELLIGENCE   PP 100 40 15   43   58 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   32 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   33 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   40 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   38 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   28 P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   35 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   33 P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   32 P C   404195   PROJECT PHASE II   TW 100 40   96 P
     404188   PROJECT PHASE I   OR 50 20   41 P C   404195   PROJECT PHASE II   PR 50 20   46 P

  GRAND TOTAL = 947/1500 , RESULT:FIRST CLASS
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     B120053252   SURADKAR NAIRUTI MANOJ   DARSHANA   ,71527640K   ,PICT   ,B120053252

              SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 14   40   54 P C   404189   MOBILE COMMUNICATION   PP 100 40 24   44   68 P
     404182   COMPUTER NETWORKS   PP 100 40 27   28   55 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 26   50   76 P
     404183   MICROWAVE ENGINEERING   PP 100 40 23   33   56 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 24   51   75 P
     404184B EMBEDDED SYSTEMS & RTOS   PP 100 40 22   42   64 P C   404192D WIRELESS NETWORKS   PP 100 40 28   36   64 P
     404185C PLCS AND AUTOMATION   PP 100 40 18   43   61 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   34 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   34 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   41 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   41 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   36 P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   36 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   39 P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   37 P C   404195   PROJECT PHASE II   TW 100 40   93 P
     404188   PROJECT PHASE I   OR 50 20   39 P C   404195   PROJECT PHASE II   PR 50 20   44 P

  GRAND TOTAL = 1047/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053253   SURUDWAD MANOJKUMAR BALAJIRAO   ARUNABAI   ,71527642F   ,PICT   ,B120053253

              SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 12   52   64 P C   404189   MOBILE COMMUNICATION   PP 100 40 20   42   62 P
     404182   COMPUTER NETWORKS   PP 100 40 19   33   52 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 26   41   67 P
     404183   MICROWAVE ENGINEERING   PP 100 40 12   32   44 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 20   45   65 P
     404184A DIGITAL IMAGE PROCESSING   PP 100 40 23   49   72 P C   404192D WIRELESS NETWORKS   PP 100 40 29   37   66 P
     404185B ELECTRONIC PRODUCT DESIGN   PP 100 40 13   46   59 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   34 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   42 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   39 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   39 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   33 P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   32 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   35 P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   38 P C   404195   PROJECT PHASE II   TW 100 40   94 P
     404188   PROJECT PHASE I   OR 50 20   40 P C   404195   PROJECT PHASE II   PR 50 20   45 P

  GRAND TOTAL = 1022/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053254   SUSHIL PATIL                       MANJUSHA           ,71527643D   ,PICT       ,B120053254

              SEM.:1                                                         SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  13    45   58  P C   404189   MOBILE COMMUNICATION          PP 100  40  22    40   62  P
    404182   COMPUTER NETWORKS           PP 100  40  21    31   52  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  23    50   73  P
    404183   MICROWAVE ENGINEERING       PP 100  40  18    28   46  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  18    51   69  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100  40  28    44   72  P C   404192D  WIRELESS NETWORKS             PP 100  40  25    46   71  P
    404185C  PLCS AND AUTOMATION         PP 100  40  20    48   68  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20             35  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20             33  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20             39  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20             37  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             24  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             31  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             37  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             42  P C   404195   PROJECT PHASE II              TW 100  40             97  P
    404188   PROJECT PHASE I             OR  50  20             47  P C   404195   PROJECT PHASE II              PR  50  20             47  P

   GRAND TOTAL = 1040/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053255   SUSHIL S. KHAIRNAR                 KIRAN              ,71527644B   ,PICT       ,B120053255

              SEM.:1                                                         SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  15    54   69  P C   404189   MOBILE COMMUNICATION          PP 100  40  25    51   76  P
    404182   COMPUTER NETWORKS           PP 100  40  21    33   54  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  27    54   81  P
    404183   MICROWAVE ENGINEERING       PP 100  40  23    42   65  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  18    52   70  P
    404184B  EMBEDDED SYSTEMS & RTOS     PP 100  40  20    49   69  P C   404192D  WIRELESS NETWORKS             PP 100  40  28    45   73  P
    404185C  PLCS AND AUTOMATION         PP 100  40  21    53   74  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20             47  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20             36  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20             42  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20             42  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             45  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             38  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             42  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             39  P C   404195   PROJECT PHASE II              TW 100  40             95  P
    404188   PROJECT PHASE I             OR  50  20             46  P C   404195   PROJECT PHASE II              PR  50  20             47  P

   GRAND TOTAL = 1150/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053256   SUTKAR ADITYA SUBHASH              VARSHA             ,71527645L   ,PICT       ,B120053256

              SEM.:1                                                         SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY    PP 100  40  13    44   57  P C   404189   MOBILE COMMUNICATION          PP 100  40  19    37   56  P
    404182   COMPUTER NETWORKS           PP 100  40  20    37   57  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  22    50   72  P
    404183   MICROWAVE ENGINEERING       PP 100  40  19    40   59  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  16    28   44  P
    404184A  DIGITAL IMAGE PROCESSING    PP 100  40  22    45   67  P C   404192D  WIRELESS NETWORKS             PP 100  40  22    53   75  P
    404185D  ARTIFICIAL INTELLIGENCE     PP 100  40  18    44   62  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20             41  P
    404186   LAB PRACTICE I (CN & MWE)   TW  50  20             39  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20             41  P
    404186   LAB PRACTICE I (CN & MWE)   OR  50  20             37  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             24  P
    404187   LAB PRAC. II(VLSI &ELEC. I) TW  50  20             41  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             36  P
    404187   LAB PRAC. II(VLSI &ELEC. I) PR  50  20             36  P C   404195   PROJECT PHASE II              TW 100  40             93  P
    404188   PROJECT PHASE I             OR  50  20             44  P C   404195   PROJECT PHASE II              PR  50  20             44  P

   GRAND TOTAL = 1025/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    B120053257   SUTRAVE SUSHANT SUDHIR                  SARITA            ,71527646J   ,PICT       ,B120053257

             SEM.:1                                                           SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  11    35    46  P C    404189   MOBILE COMMUNICATION          PP 100  40  18    34    52  P
    404182   COMPUTER NETWORKS            PP 100  40  13    34    47  P      404190   BROADBAND COMMUNICATION SYS.  PP 100  40  21    41    62  P
    404183   MICROWAVE ENGINEERING        PP 100  40  18    31    49  P C    404191C  AUDIO VIDEO ENGINEERING       PP 100  40  14    37    51  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  21    35    56  P C    404192D  WIRELESS NETWORKS             PP 100  40  17    34    51  P
    404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  20    36    56  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50  20              28  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              42  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50  20              43  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              36  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              25  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              30  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              35  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              28  P C    404195   PROJECT PHASE II              TW 100  40              92  P
    404188   PROJECT PHASE I              OR  50  20              41  P C    404195   PROJECT PHASE II              PR  50  20              44  P

 GRAND TOTAL = 914/1500 , RESULT:FIRST CLASS
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    B120053258   TAKALIKAR SUPRIYA SUBHASH              KALAVATI          ,71527647G   ,PICT       ,B120053258

             SEM.:1                                                           SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  15    44    59  P C    404189   MOBILE COMMUNICATION          PP 100  40  18    47    65  P
    404182   COMPUTER NETWORKS            PP 100  40  24    30    54  P C    404190   BROADBAND COMMUNICATION SYS.  PP 100  40  29    47    76  P
    404183   MICROWAVE ENGINEERING        PP 100  40  18    39    57  P C    404191C  AUDIO VIDEO ENGINEERING       PP 100  40  17    39    56  P
    404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  22    37    59  P C    404192D  WIRELESS NETWORKS             PP 100  40  24    29    53  P
    404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  17    46    63  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50  20              38  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              34  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50  20              43  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              42  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              40  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              38  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              37  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              37  P C    404195   PROJECT PHASE II              TW 100  40              95  P
    404188   PROJECT PHASE I              OR  50  20              41  P C    404195   PROJECT PHASE II              PR  50  20              46  P

 GRAND TOTAL = 1033/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053259   TAMBADE PRANAV PRAFULLA                SHITAL            ,71527649C   ,PICT       ,B120053259

             SEM.:1                                                           SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  08    41    49  P C    404189   MOBILE COMMUNICATION          PP 100  40  10    30$   40  P
    404182   COMPUTER NETWORKS            PP 100  40  16    34    50  P      404190   BROADBAND COMMUNICATION SYS.  PP 100  40  23    34    57  P
    404183   MICROWAVE ENGINEERING        PP 100  40  17    39    56  P C    404191D  SOFT COMPUTING                PP 100  40  19    30    49  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  06    44    50  P C    404192D  WIRELESS NETWORKS             PP 100  40  18    28    46  P
    404185C  PLCS AND AUTOMATION          PP 100  40  14    38    52  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50  20              36  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              38  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50  20              41  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              37  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              35  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              38  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              38  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              30  P C    404195   PROJECT PHASE II              TW 100  40              93  P
    404188   PROJECT PHASE I              OR  50  20              35  P C    404195   PROJECT PHASE II              PR  50  20              42  P

 GRAND TOTAL = 912/1500 , RESULT:FIRST CLASS   [$ O.1]
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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      B120053260   TAMBE RUSHIKESH DILIP                 SHAILA              ,71633665M   ,PICT        ,B120053260

              SEM.:1                                                   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 17   56   73  P C   404189   MOBILE COMMUNICATION         PP 100 40 20   47   67  P
      404182   COMPUTER NETWORKS            PP 100 40 27   41   68  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 27   56   83  P
      404183   MICROWAVE ENGINEERING        PP 100 40 24   35   59  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100 40 22   52   74  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100 40 22   58   80  P C   404192D  WIRELESS NETWORKS            PP 100 40 27   47   74  P
      404185C  PLCS AND AUTOMATION          PP 100 40 16   53   69  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50 20             44  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50 20            44  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50 20             38  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50 20            37  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50 20             40  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20            45  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50 20             41  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20            40  P C   404195   PROJECT PHASE II             TW 100 40             94  P
      404188   PROJECT PHASE I              OR  50 20            43  P C   404195   PROJECT PHASE II             PR  50 20             46  P

   GRAND TOTAL = 1159/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053261   TANAY SUDARSHAN CHOUGULE              JAYASHRI            ,71527652C   ,PICT        ,B120053261

              SEM.:1                                                   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 14   37   51  P C   404189   MOBILE COMMUNICATION         PP 100 40 25   40   65  P
      404182   COMPUTER NETWORKS            PP 100 40 22   33   55  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 25   40   65  P
      404183   MICROWAVE ENGINEERING        PP 100 40 20   31   51  P C   404191D  SOFT COMPUTING               PP 100 40 22   34   56  P
      404184A  DIGITAL IMAGE PROCESSING     PP 100 40 24   48   72  P C   404192D  WIRELESS NETWORKS            PP 100 40 21   28   49  P
      404185D  ARTIFICIAL INTELLIGENCE      PP 100 40 15   43   58  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50 20             35  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50 20            45  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50 20             36  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50 20            39  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50 20             28  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20            30  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50 20             39  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20            41  P C   404195   PROJECT PHASE II             TW 100 40             95  P
      404188   PROJECT PHASE I              OR  50 20            42  P C   404195   PROJECT PHASE II             PR  50 20             46  P

   GRAND TOTAL = 998/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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      B120053262   TANSMAY VIJAY DESHPANDE               SUMITA             ,71401225E   ,PICT        ,B120053262

              SEM.:1                                                   SEM.:2
      404181   VLSI DESIGN & TECHNOLOGY     PP 100 40 05   38   43  P     404189   MOBILE COMMUNICATION         PP 100 40 16   18   --  F
      404182   COMPUTER NETWORKS            PP 100 40 16   29   45  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 23   37   60  P
      404183   MICROWAVE ENGINEERING        PP 100 40 06   45   51  P     404191C  AUDIO VIDEO ENGINEERING      PP 100 40 16   50   66  P
      404184B  EMBEDDED SYSTEMS & RTOS      PP 100 40 08   48   56  P C   404192D  WIRELESS NETWORKS            PP 100 40 18   33   51  P
      404185D  ARTIFICIAL INTELLIGENCE      PP 100 40 10   39   49  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50 20             36  P
      404186   LAB PRACTICE I (CN & MWE)    TW  50 20            36  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50 20             39  P
      404186   LAB PRACTICE I (CN & MWE)    OR  50 20            39  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50 20             30  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50 20            32  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50 20             34  P
      404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50 20            20  P C   404195   PROJECT PHASE II             TW 100 40             95  P
      404188   PROJECT PHASE I              OR  50 20            40  P C   404195   PROJECT PHASE II             PR  50 20             45  P

   GRAND TOTAL = --/1500 , RESULT:FAILS
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     NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
     OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053263   THAKARE BHAGYASHREE RAJENDRA          SUNITA               ,71045645L   ,PICT         ,B120053263

             SEM.:1                                                           SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  07      AA  -- F      404189   MOBILE COMMUNICATION         PP 100  40  15      AA  -- F
     404182   COMPUTER NETWORKS            PP 100  40  17      AA  -- F      404190   BROADBAND COMMUNICATION SYS. PP 100  40  16      AA  -- F
     404183   MICROWAVE ENGINEERING        PP 100  40  03      AA  -- F      404191D  SOFT COMPUTING               PP 100  40  20      AA  -- F
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  14      32  46 P C    404192D  WIRELESS NETWORKS            PP 100  40  21      AA  -- F
     404185C  PLCS AND AUTOMATION          PP 100  40  14      AA  -- F      404193   LAB PRACTICE III(MC & BCS)   TW  50  20                  AA  F
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              29  P      404193   LAB PRACTICE III(MC & BCS)   OR  50  20                  AA  F
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              25  P      404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20                  26  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              AA  F      404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20                  24  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              AA  F      404195   PROJECT PHASE II             TW 100  40                  70  P
     404188   PROJECT PHASE I              OR  50  20              36  P C    404195   PROJECT PHASE II             PR  50  20                  35  P

GRAND TOTAL = --/1500 , RESULT:FAILS
                                                                    RESULT RESERVED FOR BACKLOGS.
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     B120053264   THAKUR NUPUR SHEKHAR                  MEENAL               ,71527665E   ,PICT         ,B120053264

             SEM.:1                                                           SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  16      52  68 P C    404189   MOBILE COMMUNICATION         PP 100  40  26      44  70 P
     404182   COMPUTER NETWORKS            PP 100  40  25      38  63 P C    404190   BROADBAND COMMUNICATION SYS. PP 100  40  25      52  77 P
     404183   MICROWAVE ENGINEERING        PP 100  40  25      36  61 P C    404191D  SOFT COMPUTING               PP 100  40  17      44  61 P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  26      49  75 P C    404192D  WIRELESS NETWORKS            PP 100  40  25      40  65 P
     404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  18      49  67 P C    404193   LAB PRACTICE III(MC & BCS)   TW  50  20                  35  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              36  P C    404193   LAB PRACTICE III(MC & BCS)   OR  50  20                  41  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              40  P C    404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20                  28  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              37  P C    404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20                  40  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              47  P C    404195   PROJECT PHASE II             TW 100  40                  91  P
     404188   PROJECT PHASE I              OR  50  20              38  P C    404195   PROJECT PHASE II             PR  50  20                  40  P

GRAND TOTAL = 1080/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053265   THATTE SNEHA UDAY                     TANVI                ,71527667M   ,PICT         ,B120053265

             SEM.:1                                                           SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY      PP 100  40  18      65  83 P C    404189   MOBILE COMMUNICATION         PP 100  40  23      46  69 P
     404182   COMPUTER NETWORKS            PP 100  40  28      39  67 P C    404190   BROADBAND COMMUNICATION SYS. PP 100  40  29      58  87 P
     404183   MICROWAVE ENGINEERING        PP 100  40  22      45  67 P C    404191C  AUDIO VIDEO ENGINEERING      PP 100  40  17      61  78 P
     404184A  DIGITAL IMAGE PROCESSING     PP 100  40  24      59  83 P C    404192D  WIRELESS NETWORKS            PP 100  40  29      59  88 P
     404185C  PLCS AND AUTOMATION          PP 100  40  23      50  73 P C    404193   LAB PRACTICE III(MC & BCS)   TW  50  20                  42  P
     404186   LAB PRACTICE I (CN & MWE)    TW  50  20              42  P C    404193   LAB PRACTICE III(MC & BCS)   OR  50  20                  42  P
     404186   LAB PRACTICE I (CN & MWE)    OR  50  20              41  P C    404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20                  40  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              43  P C    404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20                  40  P
     404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              40  P C    404195   PROJECT PHASE II             TW 100  40                  94  P
     404188   PROJECT PHASE I              OR  50  20              44  P C    404195   PROJECT PHASE II             PR  50  20                  44  P

GRAND TOTAL = 1207/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053266   THOKE SHREYAS ANIRUDHA              APURVA              ,71527668K   ,PICT         ,B120053266

           SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  15    53   68  P C   404189   MOBILE COMMUNICATION          PP 100  40  22   46   68  P
    404182   COMPUTER NETWORKS            PP 100  40  18    47   65  P     404190   BROADBAND COMMUNICATION SYS.  PP 100  40  23   60   83  P
    404183   MICROWAVE ENGINEERING        PP 100  40  28    38   66  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  25   56   81  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  23    61   84  P C   404192D  WIRELESS NETWORKS             PP 100  40  24   48   72  P
    404185C  PLCS AND AUTOMATION          PP 100  40  24    54   78  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20             45  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              46  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20             42  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              40  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             42  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              44  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             40  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              40  P C   404195   PROJECT PHASE II              TW 100  40             92  P
    404188   PROJECT PHASE I              OR  50  20              43  P C   404195   PROJECT PHASE II              PR  50  20             40  P

  GRAND TOTAL = 1179/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053267   THOMBARE SAYALI VIJAYKUMAR          SANGITA             ,71527669H   ,PICT         ,B120053267

           SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  05    20   --  F     404189   MOBILE COMMUNICATION          PP 100  40  10   31   41  P
    404182   COMPUTER NETWORKS            PP 100  40  15    36   51  P     404190   BROADBAND COMMUNICATION SYS.  PP 100  40  13   15   --  F
    404183   MICROWAVE ENGINEERING        PP 100  40  09    11   --  F     404191C  AUDIO VIDEO ENGINEERING       PP 100  40  10   39   49  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  00    27   --  F     404192D  WIRELESS NETWORKS             PP 100  40  09   14   --  F
    404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  12    41   53  P     404193   LAB PRACTICE III(MC & BCS)    TW  50  20             36  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              36  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20             38  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              32  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             34  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              33  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             36  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              30  P C   404195   PROJECT PHASE II              TW 100  40             90  P
    404188   PROJECT PHASE I              OR  50  20              34  P C   404195   PROJECT PHASE II              PR  50  20             42  P

  GRAND TOTAL = --/1500 , RESULT:FAILS
                                                          RESULT RESERVED FOR BACKLOGS.
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    B120053268   THOMBRE UTKARSHA SANJAY             LEENA               ,71633666K   ,PICT         ,B120053268

           SEM.:1                                                        SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  19    49   68  P C   404189   MOBILE COMMUNICATION          PP 100  40  24   48   72  P
    404182   COMPUTER NETWORKS            PP 100  40  29    36   65  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  28   54   82  P
    404183   MICROWAVE ENGINEERING        PP 100  40  25    42   67  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  17   54   71  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  20    54   74  P C   404192D  WIRELESS NETWORKS             PP 100  40  29   46   75  P
    404185C  PLCS AND AUTOMATION          PP 100  40  19    48   67  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20             39  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20              45  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20             37  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20              39  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20             42  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              40  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20             38  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              40  P C   404195   PROJECT PHASE II              TW 100  40             86  P
    404188   PROJECT PHASE I              OR  50  20              38  P C   404195   PROJECT PHASE II              PR  50  20             40  P

  GRAND TOTAL = 1125/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,   PREVIOUS SEAT NO.,   COLLEGE,    SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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   B120053269   THORAT RUGVED DEVENDRA                SUREKHA              ,71527670M   ,PICT        ,B120053269

           SEM.:1                                                    SEM.:2
   404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  11    41  52  P C   404189   MOBILE COMMUNICATION         PP 100  40  15    28   43  P
   404182   COMPUTER NETWORKS            PP 100  40  17    29  46  P     404190   BROADBAND COMMUNICATION SYS. PP 100  40  25    31   56  P
   404183   MICROWAVE ENGINEERING        PP 100  40  15    28  43  P C   404191D  SOFT COMPUTING               PP 100  40  12    28   40  P
   404184A  DIGITAL IMAGE PROCESSING     PP 100  40  11    35  46  P C   404192D  WIRELESS NETWORKS            PP 100  40  17    28   45  P
   404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  10    42  52  P     404193   LAB PRACTICE III(MC & BCS)   TW  50  20              30  P
   404186   LAB PRACTICE I (CN & MWE)    TW  50  20            42  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              38  P
   404186   LAB PRACTICE I (CN & MWE)    OR  50  20            39  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              28  P
   404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20            31  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              36  P
   404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20            32  P C   404195   PROJECT PHASE II             TW 100  40              85  P
   404188   PROJECT PHASE I              OR  50  20            40  P C   404195   PROJECT PHASE II             PR  50  20              40  P

GRAND TOTAL = 864/1500 , RESULT:HIGHER SECOND CLASS
                                                        RESULT RESERVED FOR BACKLOGS.
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   B120053270   TIWARI MITHUN RAMESHCHANDRA           SANTOSH             ,71527672H   ,PICT        ,B120053270

           SEM.:1                                                    SEM.:2
   404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  13    42  55  P C   404189   MOBILE COMMUNICATION         PP 100  40  24    39   63  P
   404182   COMPUTER NETWORKS            PP 100  40  27    31  58  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  27    42   69  P
   404183   MICROWAVE ENGINEERING        PP 100  40  21    40  61  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  24    35   59  P
   404184A  DIGITAL IMAGE PROCESSING     PP 100  40  17    52  69  P C   404192D  WIRELESS NETWORKS            PP 100  40  28    43   71  P
   404185C  PLCS AND AUTOMATION          PP 100  40  14    39  53  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              42  P
   404186   LAB PRACTICE I (CN & MWE)    TW  50  20            39  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              39  P
   404186   LAB PRACTICE I (CN & MWE)    OR  50  20            41  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              36  P
   404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20            41  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              40  P
   404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20            40  P C   404195   PROJECT PHASE II             TW 100  40              93  P
   404188   PROJECT PHASE I              OR  50  20            45  P C   404195   PROJECT PHASE II             PR  50  20              42  P

GRAND TOTAL = 1056/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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   B120053271   UDIT MENDIRATTA                       REENU MENDIRATTA    ,71527677J   ,PICT        ,B120053271

           SEM.:1                                                    SEM.:2
   404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  07    34  41  P     404189   MOBILE COMMUNICATION         PP 100  40  20    37   57  P
   404182   COMPUTER NETWORKS            PP 100  40  15    41  56  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  25    39   64  P
   404183   MICROWAVE ENGINEERING        PP 100  40  16    45  61  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  18    37   55  P
   404184A  DIGITAL IMAGE PROCESSING     PP 100  40  18    34  52  P C   404192D  WIRELESS NETWORKS            PP 100  40  21    36   57  P
   404185C  PLCS AND AUTOMATION          PP 100  40  18    34  52  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20              35  P
   404186   LAB PRACTICE I (CN & MWE)    TW  50  20            38  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20              39  P
   404186   LAB PRACTICE I (CN & MWE)    OR  50  20            41  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20              34  P
   404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20            28  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20              39  P
   404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20            30  P C   404195   PROJECT PHASE II             TW 100  40              96  P
   404188   PROJECT PHASE I              OR  50  20            48  P C   404195   PROJECT PHASE II             PR  50  20              47  P

GRAND TOTAL = 970/1500 , RESULT:FIRST CLASS
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   NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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     B120053272   UTKARSH   SARITA   ,71527680J   ,PICT   ,B120053272

             SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 08   39   47 P C   404189   MOBILE COMMUNICATION   PP 100 40 17   41   58 P
     404182   COMPUTER NETWORKS   PP 100 40 20   35   55 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 23   48   71 P
     404183   MICROWAVE ENGINEERING   PP 100 40 23   46   69 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 16   36   52 P
     404184B EMBEDDED SYSTEMS & RTOS   PP 100 40 16   42   58 P C   404192A BIOMEDICAL SIGNAL PROCESSING PP 100 40 24   48   72 P
     404185C PLCS AND AUTOMATION   PP 100 40 12   39   51 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   38 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   38 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   45 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   35 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   40 P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   31 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   39 P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   35 P C   404195   PROJECT PHASE II   TW 100 40   93 P
     404188   PROJECT PHASE I   OR 50 20   38 P C   404195   PROJECT PHASE II   PR 50 20   45 P

   GRAND TOTAL = 1010/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053273   VALECHA SAKSHI SATISH   ARCHANA   ,71527683C   ,PICT   ,B120053273

             SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 13   35   48 P C   404189   MOBILE COMMUNICATION   PP 100 40 22   36   58 P
     404182   COMPUTER NETWORKS   PP 100 40 18   42   60 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 24   42   66 P
     404183   MICROWAVE ENGINEERING   PP 100 40 22   45   67 P C   404191D SOFT COMPUTING   PP 100 40 22   36   58 P
     404184A DIGITAL IMAGE PROCESSING   PP 100 40 21   53   74 P C   404192D WIRELESS NETWORKS   PP 100 40 20   31   51 P
     404185D ARTIFICIAL INTELLIGENCE   PP 100 40 20   45   65 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   43 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   34 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   42 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   37 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   34 P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   39 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   38 P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   40 P C   404195   PROJECT PHASE II   TW 100 40   92 P
     404188   PROJECT PHASE I   OR 50 20   41 P C   404195   PROJECT PHASE II   PR 50 20   44 P

   GRAND TOTAL = 1031/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053274   VANAVE AKASH RAVSAHEB   PUNJABAI   ,71633667H   ,PICT   ,B120053274

             SEM.:1   SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY   PP 100 40 14   38   52 P C   404189   MOBILE COMMUNICATION   PP 100 40 14   42   56 P
     404182   COMPUTER NETWORKS   PP 100 40 25   48   73 P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 26   53   79 P
     404183   MICROWAVE ENGINEERING   PP 100 40 22   51   73 P C   404191C AUDIO VIDEO ENGINEERING   PP 100 40 14   44   58 P
     404184A DIGITAL IMAGE PROCESSING   PP 100 40 16   50   66 P C   404192D WIRELESS NETWORKS   PP 100 40 20   34   54 P
     404185B ELECTRONIC PRODUCT DESIGN   PP 100 40 19   48   67 P C   404193   LAB PRACTICE III(MC & BCS)   TW 50 20   38 P
     404186   LAB PRACTICE I (CN & MWE)   TW 50 20   43 P C   404193   LAB PRACTICE III(MC & BCS)   OR 50 20   44 P
     404186   LAB PRACTICE I (CN & MWE)   OR 50 20   33 P C   404194   LAB PRAC. IV(ELECTIVE III)   TW 50 20   38 P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW 50 20   34 P C   404194   LAB PRAC. IV(ELECTIVE III)   PR 50 20   39 P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR 50 20   35 P C   404195   PROJECT PHASE II   TW 100 40   95 P
     404188   PROJECT PHASE I   OR 50 20   35 P C   404195   PROJECT PHASE II   PR 50 20   42 P

   GRAND TOTAL = 1054/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
    OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053275   VANGARI MAMTA GOVIND                 NAGMANI             ,71633668F   ,PICT        ,B120053275

            SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  16    42   58  P C   404189   MOBILE COMMUNICATION         PP 100  40  23    50   73  P
    404182   COMPUTER NETWORKS            PP 100  40  29    37   66  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  24    61   85  P
    404183   MICROWAVE ENGINEERING        PP 100  40  15    46   61  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  17    52   69  P
    404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  21    57   78  P C   404192D  WIRELESS NETWORKS            PP 100  40  28    49   77  P
    404185C  PLCS AND AUTOMATION          PP 100  40  23    50   73  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20             40  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20             38  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20             42  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20             40  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20             30  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20             37  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20             38  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20             33  P C   404195   PROJECT PHASE II             TW 100  40             79  P
    404188   PROJECT PHASE I              OR  50  20             38  P C   404195   PROJECT PHASE II             PR  50  20             37  P

    GRAND TOTAL = 1092/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053276   VARMA SHRUTI SANJAY                  SANGEETA            ,71527685K   ,PICT        ,B120053276

            SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  14    30   44  P C   404189   MOBILE COMMUNICATION         PP 100  40  20    47   67  P
    404182   COMPUTER NETWORKS            PP 100  40  20    42   62  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  24    54   78  P
    404183   MICROWAVE ENGINEERING        PP 100  40  26    49   75  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  24    47   71  P
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  21    50   71  P C   404192A  BIOMEDICAL SIGNAL PROCESSING PP 100  40  21    56   77  P
    404185B  ELECTRONIC PRODUCT DESIGN    PP 100  40  22    45   67  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20             40  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20             38  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20             43  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20             41  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20             34  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20             41  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20             40  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20             32  P C   404195   PROJECT PHASE II             TW 100  40             93  P
    404188   PROJECT PHASE I              OR  50  20             40  P C   404195   PROJECT PHASE II             PR  50  20             45  P

    GRAND TOTAL = 1099/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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    B120053277   VASAGADE SUMEDHA SUNIL               SHWETA              ,71633669D   ,PICT        ,B120053277

            SEM.:1                                                          SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  16    34   50  P C   404189   MOBILE COMMUNICATION         PP 100  40  25    55   80  P
    404182   COMPUTER NETWORKS            PP 100  40  27    39   66  P C   404190   BROADBAND COMMUNICATION SYS. PP 100  40  24    59   83  P
    404183   MICROWAVE ENGINEERING        PP 100  40  22    47   69  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100  40  18    49   67  P
    404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  17    52   69  P C   404192D  WIRELESS NETWORKS            PP 100  40  27    44   71  P
    404185C  PLCS AND AUTOMATION          PP 100  40  19    47   66  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50  20             39  P
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20             44  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50  20             43  P
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20             42  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50  20             42  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20             41  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50  20             40  P
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20             39  P C   404195   PROJECT PHASE II             TW 100  40             95  P
    404188   PROJECT PHASE I              OR  50  20             41  P C   404195   PROJECT PHASE II             PR  50  20             45  P

    GRAND TOTAL = 1132/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
     OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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     B120053278   VELANKAR SIDDHI SANJAY                SANDHYA             ,71527687F   ,PICT        ,B120053278

             SEM.:1                                                  SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 15    30    45  P C   404189   MOBILE COMMUNICATION         PP 100 40 24    50    74  P
     404182   COMPUTER NETWORKS           PP 100 40 24    43    67  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 25    54    79  P
     404183   MICROWAVE ENGINEERING       PP 100 40 24    44    68  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100 40 13    39    52  P
     404184A  DIGITAL IMAGE PROCESSING    PP 100 40 19    53    72  P C   404192A  BIOMEDICAL SIGNAL PROCESSING PP 100 40 25    54    79  P
     404185B  ELECTRONIC PRODUCT DESIGN   PP 100 40 21    40    61  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50 20             39  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50 20             34  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50 20             44  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50 20             42  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50 20             42  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20             38  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50 20             41  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20             42  P C   404195   PROJECT PHASE II             TW 100 40             92  P
     404188   PROJECT PHASE I             OR  50 20             42  P C   404195   PROJECT PHASE II             PR  50 20             44  P

  GRAND TOTAL = 1097/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053279   WADIKAR PINAK SUHAS                   SMITA               ,71527693L   ,PICT        ,B120053279

             SEM.:1                                                  SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 16    36    52  P C   404189   MOBILE COMMUNICATION         PP 100 40 26    47    73  P
     404182   COMPUTER NETWORKS           PP 100 40 26    44    70  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 27    51    78  P
     404183   MICROWAVE ENGINEERING       PP 100 40 23    50    73  P C   404191D  SOFT COMPUTING               PP 100 40 27    45    72  P
     404184B  EMBEDDED SYSTEMS & RTOS     PP 100 40 21    60    81  P C   404192D  WIRELESS NETWORKS            PP 100 40 28    52    80  P
     404185D  ARTIFICIAL INTELLIGENCE     PP 100 40 19    54    73  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50 20             43  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50 20             44  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50 20             45  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50 20             42  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50 20             35  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20             42  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50 20             43  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20             42  P C   404195   PROJECT PHASE II             TW 100 40             97  P
     404188   PROJECT PHASE I             OR  50 20             40  P C   404195   PROJECT PHASE II             PR  50 20             48  P

  GRAND TOTAL = 1173/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053280   WAGHMARE ASHLESHA NAMDEV              VINAYA              ,71527696E   ,PICT        ,B120053280

             SEM.:1                                                  SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 12    34    46  P C   404189   MOBILE COMMUNICATION         PP 100 40 23    44    67  P
     404182   COMPUTER NETWORKS           PP 100 40 13    38    51  P C   404190   BROADBAND COMMUNICATION SYS. PP 100 40 26    51    77  P
     404183   MICROWAVE ENGINEERING       PP 100 40 20    48    68  P C   404191C  AUDIO VIDEO ENGINEERING      PP 100 40 19    41    60  P
     404184A  DIGITAL IMAGE PROCESSING    PP 100 40 23    56    79  P C   404192A  BIOMEDICAL SIGNAL PROCESSING PP 100 40 23    48    71  P
     404185B  ELECTRONIC PRODUCT DESIGN   PP 100 40 18    41    59  P C   404193   LAB PRACTICE III(MC & BCS)   TW  50 20             45  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50 20             32  P C   404193   LAB PRACTICE III(MC & BCS)   OR  50 20             38  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50 20             39  P C   404194   LAB PRAC. IV(ELECTIVE III)   TW  50 20             43  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20             41  P C   404194   LAB PRAC. IV(ELECTIVE III)   PR  50 20             39  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20             39  P C   404195   PROJECT PHASE II             TW 100 40             95  P
     404188   PROJECT PHASE I             OR  50 20             32  P C   404195   PROJECT PHASE II             PR  50 20             44  P

  GRAND TOTAL = 1065 + 10/1500 , RESULT:FIRST CLASS WITH DISTINCTION    [& O.163]
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     NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
     OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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     B120053281   WAIGAONKAR SAKSHI RAJESH              MANJIRI            ,71527698M   ,PICT        ,B120053281

            SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 06    35    41  P C    404189   MOBILE COMMUNICATION          PP 100 40 19    45    64  P
     404182   COMPUTER NETWORKS           PP 100 40 08    43    51  P C    404190   BROADBAND COMMUNICATION SYS.  PP 100 40 24    48    72  P
     404183   MICROWAVE ENGINEERING       PP 100 40 17    43    60  P C    404191D  SOFT COMPUTING                PP 100 40 09    45    54  P
     404184A  DIGITAL IMAGE PROCESSING    PP 100 40 13    52    65  P C    404192D  WIRELESS NETWORKS             PP 100 40 16    37    53  P
     404185C  PLCS AND AUTOMATION         PP 100 40 12    49    61  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50 20             39  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50 20             38  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50 20             37  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50 20             35  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50 20             34  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20             34  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50 20             32  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20             37  P C    404195   PROJECT PHASE II              TW 100 40             96  P
     404188   PROJECT PHASE I             OR  50 20             47  P C    404195   PROJECT PHASE II              PR  50 20             47  P

 GRAND TOTAL = 997/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053282   WAKODKAR VARAD SANJEEV                SUNITA             ,71527701E   ,PICT        ,B120053282

            SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 15    28    43  P C    404189   MOBILE COMMUNICATION          PP 100 40 29    47    76  P
     404182   COMPUTER NETWORKS           PP 100 40 18    43    61  P C    404190   BROADBAND COMMUNICATION SYS.  PP 100 40 24    35    59  P
     404183   MICROWAVE ENGINEERING       PP 100 40 20    53    73  P C    404191C  AUDIO VIDEO ENGINEERING       PP 100 40 18    44    62  P
     404184B  EMBEDDED SYSTEMS & RTOS     PP 100 40 18    52    70  P C    404192D  WIRELESS NETWORKS             PP 100 40 22    39    61  P
     404185C  PLCS AND AUTOMATION         PP 100 40 10    39    49  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50 20             42  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50 20             42  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50 20             42  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50 20             40  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50 20             34  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20             42  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50 20             40  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20             36  P C    404195   PROJECT PHASE II              TW 100 40             85  P
     404188   PROJECT PHASE I             OR  50 20             41  P C    404195   PROJECT PHASE II              PR  50 20             40  P

 GRAND TOTAL = 1038/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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     B120053283   WIDHATE RAHUL                         ARUNA              ,71527705H   ,PICT        ,B120053283

            SEM.:1                                                        SEM.:2
     404181   VLSI DESIGN & TECHNOLOGY    PP 100 40 07    36    43  P      404189   MOBILE COMMUNICATION          PP 100 40 22    43    65  P
     404182   COMPUTER NETWORKS           PP 100 40 18    32    50  P C    404190   BROADBAND COMMUNICATION SYS.  PP 100 40 28    36    64  P
     404183   MICROWAVE ENGINEERING       PP 100 40 22    42    64  P C    404191C  AUDIO VIDEO ENGINEERING       PP 100 40 18    32    50  P
     404184A  DIGITAL IMAGE PROCESSING    PP 100 40 25    50    75  P C    404192D  WIRELESS NETWORKS             PP 100 40 20    35    55  P
     404185C  PLCS AND AUTOMATION         PP 100 40 16    36    52  P C    404193   LAB PRACTICE III(MC & BCS)    TW  50 20             34  P
     404186   LAB PRACTICE I (CN & MWE)   TW  50 20             33  P C    404193   LAB PRACTICE III(MC & BCS)    OR  50 20             39  P
     404186   LAB PRACTICE I (CN & MWE)   OR  50 20             37  P C    404194   LAB PRAC. IV(ELECTIVE III)    TW  50 20             30  P
     404187   LAB PRAC. II(VLSI &ELEC. I) TW  50 20             28  P C    404194   LAB PRAC. IV(ELECTIVE III)    PR  50 20             36  P
     404187   LAB PRAC. II(VLSI &ELEC. I) PR  50 20             36  P C    404195   PROJECT PHASE II              TW 100 40             92  P
     404188   PROJECT PHASE I             OR  50 20             40  P C    404195   PROJECT PHASE II              PR  50 20             42  P

 GRAND TOTAL = 965/1500 , RESULT:FIRST CLASS
                                                        RESULT RESERVED FOR BACKLOGS.
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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
   OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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       B120053284   YENGE SAURAV NIVRUTTI                 SUNITA              ,71527711B   ,PICT        ,B120053284

                 SEM.:1                                                       SEM.:2
       404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  13    32    45  P C   404189   MOBILE COMMUNICATION          PP 100  40  20    46   66  P
       404182   COMPUTER NETWORKS            PP 100  40  19    32    51  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  24    36   60  P
       404183   MICROWAVE ENGINEERING        PP 100  40  18    31    49  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  14    31   45  P
       404184A  DIGITAL IMAGE PROCESSING     PP 100  40  25    44    69  P C   404192D  WIRELESS NETWORKS             PP 100  40  15    33   48  P
       404185C  PLCS AND AUTOMATION          PP 100  40  14    28    42  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              36  P
       404186   LAB PRACTICE I (CN & MWE)    TW  50  20              38  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              41  P
       404186   LAB PRACTICE I (CN & MWE)    OR  50  20              38  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              32  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              39  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              38  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              31  P C   404195   PROJECT PHASE II              TW 100  40              85  P
       404188   PROJECT PHASE I              OR  50  20              41  P C   404195   PROJECT PHASE II              PR  50  20              38  P

   GRAND TOTAL = 932/1500 , RESULT:FIRST CLASS
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       B120053285   ZAMBARE ANKUR GOVIND                  SUSHMA              ,71527714G   ,PICT        ,B120053285

                 SEM.:1                                                       SEM.:2
       404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  12    29    41  P C   404189   MOBILE COMMUNICATION          PP 100  40  19    51   70  P
       404182   COMPUTER NETWORKS            PP 100  40  23    38    61  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  16    35   51  P
       404183   MICROWAVE ENGINEERING        PP 100  40  17    38    55  P C   404191C  AUDIO VIDEO ENGINEERING       PP 100  40  15    33   48  P
       404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  16    51    67  P C   404192D  WIRELESS NETWORKS             PP 100  40  21    32   53  P
       404185C  PLCS AND AUTOMATION          PP 100  40  15    37    52  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              39  P
       404186   LAB PRACTICE I (CN & MWE)    TW  50  20              38  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              42  P
       404186   LAB PRACTICE I (CN & MWE)    OR  50  20              41  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              40  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              41  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              38  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              35  P C   404195   PROJECT PHASE II              TW 100  40              94  P
       404188   PROJECT PHASE I              OR  50  20              41  P C   404195   PROJECT PHASE II              PR  50  20              45  P

   GRAND TOTAL = 992/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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       B120053286   ZAWARE PRASAD ASHOK                   MADHURI             ,71633670H   ,PICT        ,B120053286

                 SEM.:1                                                       SEM.:2
       404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  12    28    40  P C   404189   MOBILE COMMUNICATION          PP 100  40  15    46   61  P
       404182   COMPUTER NETWORKS            PP 100  40  15    35    50  P C   404190   BROADBAND COMMUNICATION SYS.  PP 100  40  27    31   58  P
       404183   MICROWAVE ENGINEERING        PP 100  40  20    47    67  P C   404191D  SOFT COMPUTING                PP 100  40  22    36   58  P
       404184B  EMBEDDED SYSTEMS & RTOS      PP 100  40  12    50    62  P C   404192D  WIRELESS NETWORKS             PP 100  40  23    40   63  P
       404185D  ARTIFICIAL INTELLIGENCE      PP 100  40  12    36    48  P C   404193   LAB PRACTICE III(MC & BCS)    TW  50  20              46  P
       404186   LAB PRACTICE I (CN & MWE)    TW  50  20              45  P C   404193   LAB PRACTICE III(MC & BCS)    OR  50  20              43  P
       404186   LAB PRACTICE I (CN & MWE)    OR  50  20              39  P C   404194   LAB PRAC. IV(ELECTIVE III)    TW  50  20              45  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20              46  P C   404194   LAB PRAC. IV(ELECTIVE III)    PR  50  20              35  P
       404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20              37  P C   404195   PROJECT PHASE II              TW 100  40              92  P
       404188   PROJECT PHASE I              OR  50  20              43  P C   404195   PROJECT PHASE II              PR  50  20              43  P

   GRAND TOTAL = 1021/1500 , RESULT:FIRST CLASS WITH DISTINCTION
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  NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
  OTHER LINES: HEAD OF PASSING,  MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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    B120053287   ABHISHEK USHEKWAR                   PREETI              ,71400687E   ,PICT         ,B120053287

              SEM.:1                                                   SEM.:2
    404181   VLSI DESIGN & TECHNOLOGY     PP 100  40  12   32   44  P C   404189   MOBILE COMMUNICATION           PP 100  40  08   32$  40  P C
    404182   COMPUTER NETWORKS            PP 100  40  13   34   47  P C   404190   BROADBAND COMMUNICATION SYS.   PP 100  40  10   38   48  P C
    404183   MICROWAVE ENGINEERING        PP 100  40  06   17   --  F     404191C  AUDIO VIDEO ENGINEERING        PP 100  40  11   43   54  P C
    404184A  DIGITAL IMAGE PROCESSING     PP 100  40  12   46   58  P C   404192D  WIRELESS NETWORKS              PP 100  40  04   36   40  P C
    404185C  PLCS AND AUTOMATION          PP 100  40  13   34   47  P C   404193   LAB PRACTICE III(MC & BCS)     TW  50  20            31  P C
    404186   LAB PRACTICE I (CN & MWE)    TW  50  20            36  P C   404193   LAB PRACTICE III(MC & BCS)     OR  50  20            30  P C
    404186   LAB PRACTICE I (CN & MWE)    OR  50  20            24  P C   404194   LAB PRAC. IV(ELECTIVE III)     TW  50  20            24  P C
    404187   LAB PRAC. II(VLSI &ELEC. I)  TW  50  20            25  P C   404194   LAB PRAC. IV(ELECTIVE III)     PR  50  20            31  P C
    404187   LAB PRAC. II(VLSI &ELEC. I)  PR  50  20            32  P C   404195   PROJECT PHASE II               TW 100  40            88  P C
    404188   PROJECT PHASE I              OR  50  20            36  P C   404195   PROJECT PHASE II               PR  50  20            42  P C

GRAND TOTAL = --/1500 , RESULT:FAILS   [$ O.1]
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