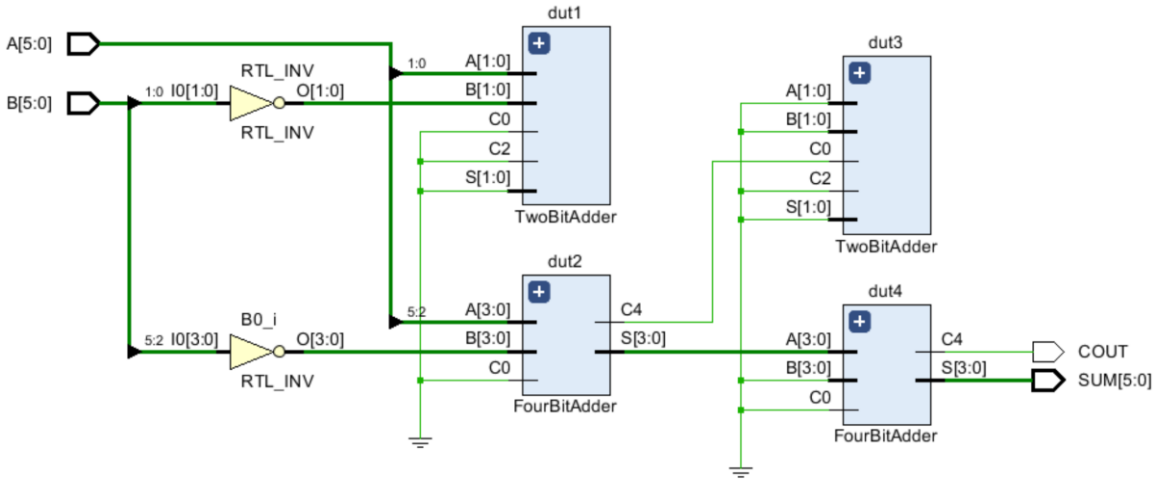
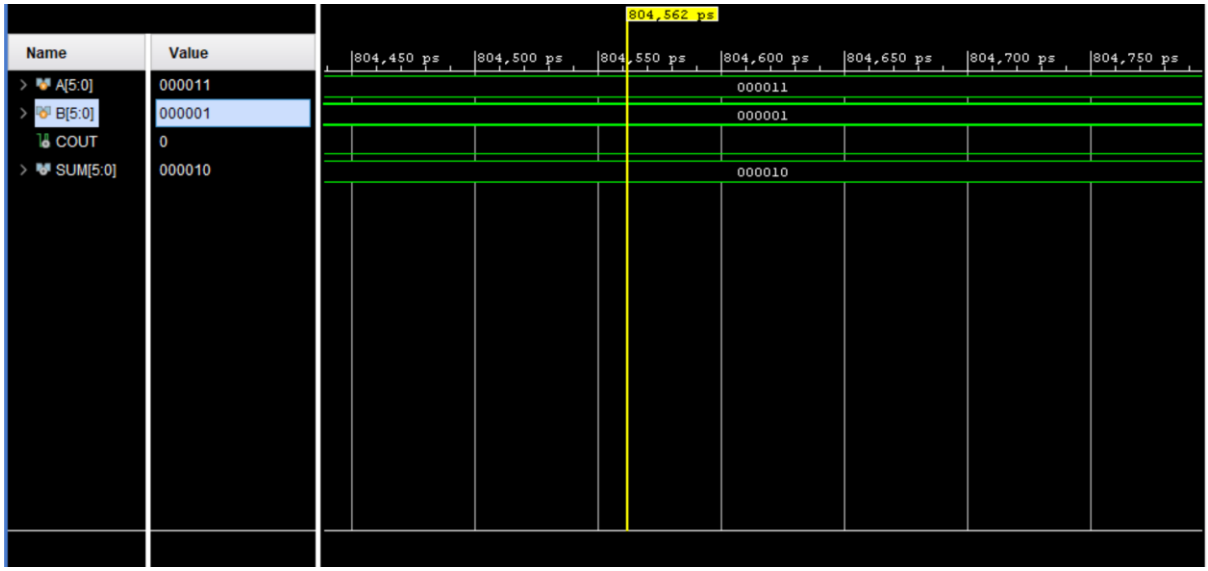
EE2026 - LAB ASSIGNMENT

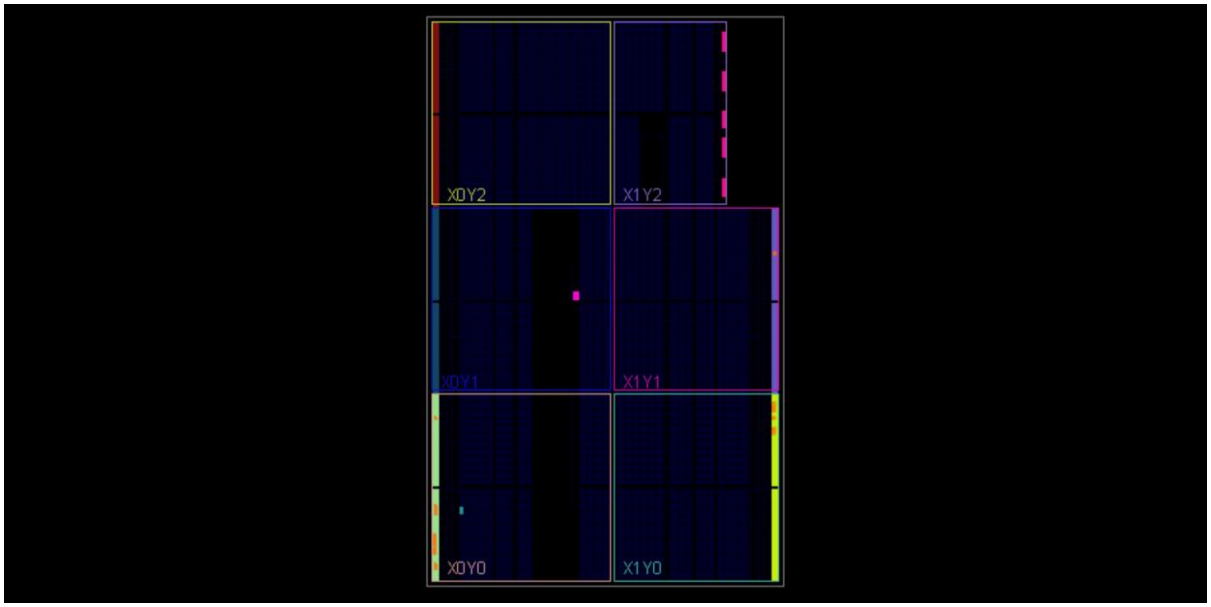
PART A) RTL SYNTHESIS



PART B) SIMULATION



PART C) BITSTREAM


X0Y2
X1Y2
X0Y1
X1Y1
X0Y0
X1Y0