Name: - Amustika Bulia Roll No1-2301010263 Subject 1- Operating System course 1- B. tech CSZ-D 7 signment - 1 Despite the evolution of honolwave , why do modern system still ruly heavily on moder system operating system? Even through handware has advanced, usus and applications can't disuitly interest with it. The OS provides obstruction, susowed management, process scheduling, successing and multitasking system, making usable and officient. Without on 0:5. 9 programming and survived application. most suitable. Such devices needs quick and
puedictable suspenses (e.g. detecting abnormal heartless instantly 1-RTOS ensures low lateray, reliability and time-bound execution, which are vitical for health monitoring Dys I would avoid a non monolithic kund. Although fact it lack modulavity how poor fault isolation, and disugging everaus is handler. A wash in one survius can bring is he down the we system which is not acceptable in performance to down the whole disign is mosu seeme and maintainable Page No.

William A.		1
	Date / /	17
duy	This claim is false . Os stewative affects	TE
sol	security, suliability, efficiency and scalibility.	Y
	A poolely spenctioned DS Chay even	
	byocissis but will face performance issue, dibugging difficulties, and weak security.	
	debugging difficulties, and weak security.	
	For ea in walnut improved modularity	
1	For eg - microkund, improves modulavity and fault isolation compand to monolithic	
	10 les testan sill raly les on	
aus_	Constity Edinida	
Sol	(i) PCB Analysis: - PCB storus sugisters, program	
(4)	country and process state. If engister 1stated	
16 16	switch can be traced how	
12	Santistum per printing solutions survives	
Delhouse	eii) Context switching: - Involves saving CPU state	
tion.	1 rugistus program counter, memory	
	mapping). of running process and loading the	
	State of next process.	
- 31	1 Sept 1 Kust - 1100 Squating System 18705	
	ciii) system call type-for 1/0 allocation mid-	9
	system call are perferred. They allow the	
	process to continue without waiting fee	
	2/0 completion, imperoving efficiency.	
- Our 6	Save = 2ms, load = 3ms, Schidulu = 1ms	
- 511	Total = 6 ms	
1.62	The state of the s	2
390	Impact -> truguert switching incuoses CPU	
	actual execution and man de made multitacking	4
	putormance. Page No.	
collection of the second	The construction of the second state of the second state of the second s	

					3
Date/	Thruad Ef	ficiency Chu	to Par By	dia .	
			- Harris of the	ime = 40/4	, = 105
$\begin{array}{c c} \longrightarrow & M \\ \hline & m \\ \hline & \mathcal{I} \mid 0 \end{array}$	Whithwardly waits, an	g allows s, suduces d incuases	parallel c. idle CPU through	xecution.) time a put.	on lwing
(a) Brows	P. P.	F ₂	P3 12.5	15 17.5 Conttina	4]
	14.0	Time C CFS Cyanti	r Chast	161-18-	F3
Prous P2	5.0	SJF Time	cms) chart	15 - 6 1 11 - 17 - 17 - 17 - 17 - 17 - 17 - 17 -	7-5 20
				i i	
36					
				Page	No.

	The second of the second				Date				
9-7	Thre	ead Ef	ficienc	y C1	neck		21	ţ	1
		1 9		· 1	11	: 211	(6)		
-0	Sin	gle - 1	unead	ed =	40 /	sic	41/1	,**	
gi.	wit	4 4 th	reads	->	Exec	ution t	ime 5	40/4	= 105
				(y 17 197	117 + 1	- Or -	12.5	
->	Muli's	threadin	9 al	lows	paral	lel ex	ecution	on r	nuetiple
(b)	2400	coses	. xed	uces	idle	CPO 1.	time du	فسنت	9/0
>		lena						0	,
9		7				0-7		=	,
9-8 (a)	2		1.2	(1 (10	. 5.)	, o = 1 - 3		
9 7	1	= 1 -	Mi do	16-	1-7-		3	,	
•	-1-14 B	1	Car A	Pa	ways 2	Pz	1	, i	
2	brown	٩,	E 2						14
h Fri	- Land	2.	5	5.0			2.5	5 . 17	٠٢ - ٢
		, 7,ag \$	2 100	hvar l	-710	ne cmaz	in a m	P 45-1	31
			F	CFS	har	tt ch	ast	+	
ji	*	e			Y (19)			· X	
3) /s = = d	e de v	ir vy.	· .	* v / x =	d Corner	1 2 3 5		
D	+5	•	1	, h[.], .	0 10		A+ 1.00	la s	1
Protesses	P2		P,	15		Py	41 Janes	ele Pei	P3
•	1	4 4	25.4	L _a 'a	Jun .	1 1111111			-
0	2	٠ς,	5.0	7.5	64.	0 000	12.5	15 13	5 20
	- 7	a.		Tim	e Cm	٥)		41.5	
			55 F	han	ut c	hart			
9				7,		7			7.5
Y - 7.01		4.	1 5			t verie		1	
beorning.	۴,	· Pz	83	1 18	Pu	P	P ₃	Py	٤
	usa with		4118	14.01.	Press		. p. b		
0	(a. 12 a.d	والأرب كال	10	tim	e (m) 1.	5	- A	20 :	1
	3 7 5	1. ILR	ound			=4)	Gantt	Char	1
	g'17 J.	and the	wedged to		i sh	101	1 1	may	
125			T.				- 5	Spiral	
								speral	

(b) FCPS PI -> P2 -> P3 -> P4 SJF (Non-premptive) : Parpy > Py > Pz ATAT = 12 ms AWT = 6.5 mg Round Robin (9=4) m order: P1 -> P2 -> P3 -> Pu -> P3 -> Pu -> P3 -> Pu -> P3 AWT = 9.25 ms. (c) Best Algorithm: SJF, Since it gives lowest av. erage waiting and Turnaround time. 9-9 (i) Cloud Migration. choose microkernel -> provides better security Scability. It help by isolating one efficiently managing resources, allowing os to run on same hardware, improving tolerance. Smart Home System. (a) os we priority scheduling so high task are ecuted immediately while gpc ensure device communicate efficiently. Low priority task our in backy round) Suitable Algorithm's Priority Scheduling for urgent tasks, combined with Round Robin for fair CPU Sh efficiency. This ensure responsiveness