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<u>PES University, Bangalore</u> (Established under Karnataka Act No. 16 of 2013)

UE14CS302

NOV-DEC 2016: END SEMESTER ASSESSMENT (ESA)

B.TECH. V SEMESTER CSE

UE14CS302 - INTRODUCTION TO OPERATING SYSTEMS

Time	e: 03 Hrs Answer All Questions Max Marks: 10	0			
1a.	Describe a mechanism for enforcing memory protection in order to prevent a program from modifying the memory associated with other programs.				
1b.	Why is the separation of mechanism and policy desirable in Operating Systems?	5M			
1c.	Discuss, with examples, how the problem of maintaining coherence of cached data manifests itself in the following processing environments: i. Single-processor systems ii. Multiprocessor systems iii. Distributed systems	5M			
1d.	What is the difference between an Independent Process(s) and Cooperating Process(s)? List the advantages of Process Cooperation.	5M			
2a.	Explain the concept of single threaded Process and Multi threaded Process with the help of a	5M			
2b.	Maintaining data consistency requires mechanisms to ensure the orderly execution of cooperating processes. What kinds of problems occur if this is not carefully handled?				
2c.	Given the following snapshot, check whether the system is currently in safe state. If so find the safe sequence. Allocation Max A B C D A B C D P0 2 1 2 1 7 6 5 4 P1 4 4 3 4 6 7 6 6 P2 3 2 7 1 6 5 9 3 P3 3 1 3 1 4 4 3 3 P4 1 6 1 9 8 7 3 9 Rmax=<15,18,18,20> Can a new request by P3<1,0,1,0> be granted immediately.	10M			
3a.	Explain the concept of internal and external fragmentation using appropriate diagrams.	5M			
3b.	Consider the following page reference string: 1,2,1,2,1,7,6,1,2,7,6,7,8,9,1,2,3,4,5,6,7 Assuming demand paging with Two frames, how many page faults would occur if we apply Optimal Page replacement Policy?	5M			

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	Consider the follo	wing set of proces	ses, with the leng	th of the CPU burst t	ime given in	
	milliseconds:					
	Process	Burst Time	Arrival Time	Priority		
	P1	012	0	1		
	P2	022	0	2		
	P3	112	0	2		10M
3c.	P4	033	0	2		
	P5	018	0	1		
	applying i. Non-Pre-	e Response Time, a emptive Shortest Joive Priority Schedu	ob First Schedulin	rime, Average Turna g	round Time by	
4a.	The open-file table is used to maintain information about files that are currently open. Should the operating system maintain a separate table for each user or just maintain one table that contains references to files that are being accessed by all users at the current time? If the same file is being accessed by two different programs or users, should there be separate					6M
4b.	entries in the open file table? Discuss how performance optimizations for file systems might result in difficulties in maintaining the consistency of the systems in the event of computer crashes.					6M
4c.	Discuss the advantages and disadvantages of supporting links to files that cross mount points (that is, the file link refers to a file that is stored in a different volume).					6M
4d.	Consider a file system that uses inodes to represent files. Disk blocks are 8-KB in size and a pointer to á disk block requires 4 bytes. This file system has 12 direct disk blocks, plus single, double, and triple indirect disk blocks. What is the maximum size of a file that can be stored					2M
	in this file system	scheduling discipli	nes, except FCFS	, is truly fair (starvat	on may occur).	
5a.	None of the disk-scheduling disciplines, except FCFS, is truly fair (starvation may occur). i. Explain why this assertion is true. ii. Describe a way to modify algorithms such as SCAN to ensure fairness. iii. Explain why fairness is an important goal in a time-sharing system. iv. Give three or more examples of circumstances in which it is important that the operating system be unfair in serving I/O requests.					8M
	Suppose that a disk drive has 1,000 cylinders, numbered 0 to 999. The drive is currently serving a request at cylinder 150, and the previous request was at cylinder 805. The queue of pending requests, in FIFO order, is:					
5b.	200 040 206 280 544 618 356 153 465 361					
	arm moves to sa	atisfy all the pendin	g requests for by	applying 0-00/111 u		
5c.	algorithm. What are the advantages and disadvantages of supporting memory-mapped I/O to device-control registers?					