


ANVESH PANDEY

RF Engineer

 Anvesh Pandey

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 Greater Noida, India

SUMMARY

Self-driven Engineer skilled in complex problem-solving and fabrication of circuit. Research intern @Ampere Lab, INSA Lyon. B.Tech Electronics and communication student @Galgotias College. Initiated of 'Digital Dreamweaver'.

SKILLS

Languages: Matlab, Python

Technologies: CST Microwave Studio, Keysight ADS, Solidworks, Basic knowledge of Linux (Ubuntu and Debian) and Windows(7+).

PROJECTS

Technology	Highly Efficient 2.45 GHz Rectifier Circuit for RF Energy Harvesting Applications IEEE
	The improved bridge rectifier in this study was built utilising four Schottky diodes. To precisely forecast the rectifier performances, the Microwave rectifier has been simulated on Agilent ADS 2019 at 2.45 GHz with associated circuit techniques.

EDUCATION

2020 - Present	Galgotias College of Engineering and Technology, Greater Noida (AKTU) B.Tech
	CGPA: 7.66 <i>Relevant Coursework:</i> Antenna and wave propagation, Signal and System, VLSI, Data communication and Networks, Digital Communication
2019 - 2020	Gopal Public School, Sultanpur (CBSE) 12th
	Percentage: 80.6
2017 - 2018	Stella Maris Convent School, Sultanpur (CBSE) 10th
	Percentage: 85.33

EXPERIENCE

9/2023 - 11/2023	Institut National des Sciences Appliquées de Lyon, France Research Intern
	<ul style="list-style-type: none">Worked under the supervision of Prof. Dr. Bruno Allard for guidance and mentorship. Conducted in-depth research on rectifier mathematical analysis and simulationCharacterized non-standardised materials to obtain their RF properties.Fabricated RF Rectenna components including Antenna, Rectifier on different substrates.<i>Skills:</i> CST, Python, ADS, MATLAB, LaTeX
6/2023 - 7/2023	Noida Metro Railway Corporation (NMRC) Summer Intern
	<ul style="list-style-type: none">Engaged in workshops and training sessions to develop a comprehensive understanding of the telecommunications and signalling infrastructure in the context of metro rail operations.Collaborated with senior engineers to learn about designing and operating signalling systems used for train control and traffic management.<i>Skills:</i> Communication infrastructure, Infrastructure planning, Maintenance procedures, Operational efficiency.

EXTRACURRICULAR

Digital Dreamweaver

- During my undergraduate studies, I initiated the establishment of 'Digital Dreamweaver,' a community at my college dedicated to fostering knowledge sharing and resource accessibility for fellow B.Tech students.
- The community, which currently boasts a membership of over 70 individuals, maintains comprehensive records of resources, achievements, and setbacks to aid future B.Tech aspirants.

LANGUAGES

English - Intermediate, **Hindi** - native