

Anhang - A

Portadreßbelegung des SBC-86

Portadr.	Baustein	Bedeutung	Zugriff
Portadr.	Baustein	Daten	In
80H	Keyboard		
90H	Display (rechts)	Daten	Out
92H	"	"	"
:	"	"	"
9EH	Display (links)	"	"
A0H	PIT - Zähler 0	Daten	In/Out
A2H	Zähler 1	"	"
A4H	Zähler 2	"	"
A6H	Modus	Steuerung	Out
B0H	PPI - Port A	Daten	In/Out
B2H	Port B	"	"
B4H	Port C	"	"
B6H	Ctrl	Steuerung	Out
C0H	PIC - ICW1/OCW2,3	Daten	In/Out
C2H	ICW2,3/OCW1	"	"
D0H	SIC1 - E/A-Kanal	Daten	In/Out
D2H	Modus	Steuerung	"
E0H	SIC2 - E/A-Kanal	Daten	In/Out
E2H	Modus	Steuerung	"

Anhang - B

Schnittstellen - Belegung und Konfiguration

- Parallelport X1:

PIN	Belegung	PIN	Belegung
1	5V	18	PB5
2	5V	19	PB6
3	5V	20	PB7
4	5V	21	PC0
5	PA0	22	PC1
6	PA1	23	PC2
7	PA2	24	PC3
8	PA3	25	PC4
9	PA4	26	PC5
10	PA5	27	PC6
11	PA6	28	PC7
12	PA7	29	GND
13	PB0	30	GND
14	PB1	31	GND
15	PB2	32	GND
16	PB3	33	GND
17	PB4	34	GND

- Serielle Ports X2/X4:

PIN	Belegung
2	TxD
3	RxD
4	RTS
5	CTS
7	GND

- Experimentalschnittstelle X501:

Datenbus	PIN-Eingang	PIN-Ausgang
D0	1	2
D1	3	4
D2	5	6
D3	7	8
D4	9	10
D5	11	12
D6	13	14
D7	15	16

Die PIN-Zählweise entspricht der bei IC's üblichen Zählweise.

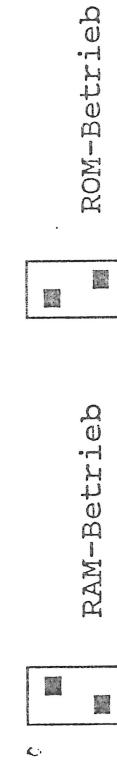
- Adressschalter SW509:

Schalter auf ON bedeutet Low-Potential

Schalter	Adreßbit
8	A0
7	A1
6	A2
5	A3
4	A4
3	A5
2	A6
1	A7

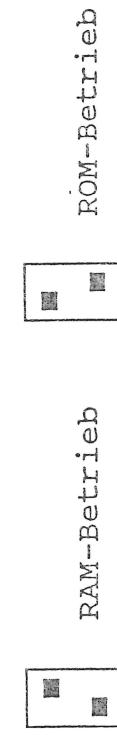
- Schalter S2:

Umschalten RAM/ROM IC11, IC15 (2. Bank)



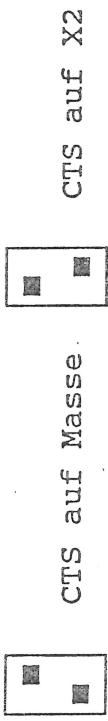
- Schalter S3:

Umschalten RAM/ROM IC12, IC16 (3. Bank)



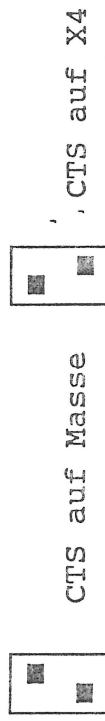
- Schalter S28:

Umschalten CTS der 1. seriellen Schnittstelle



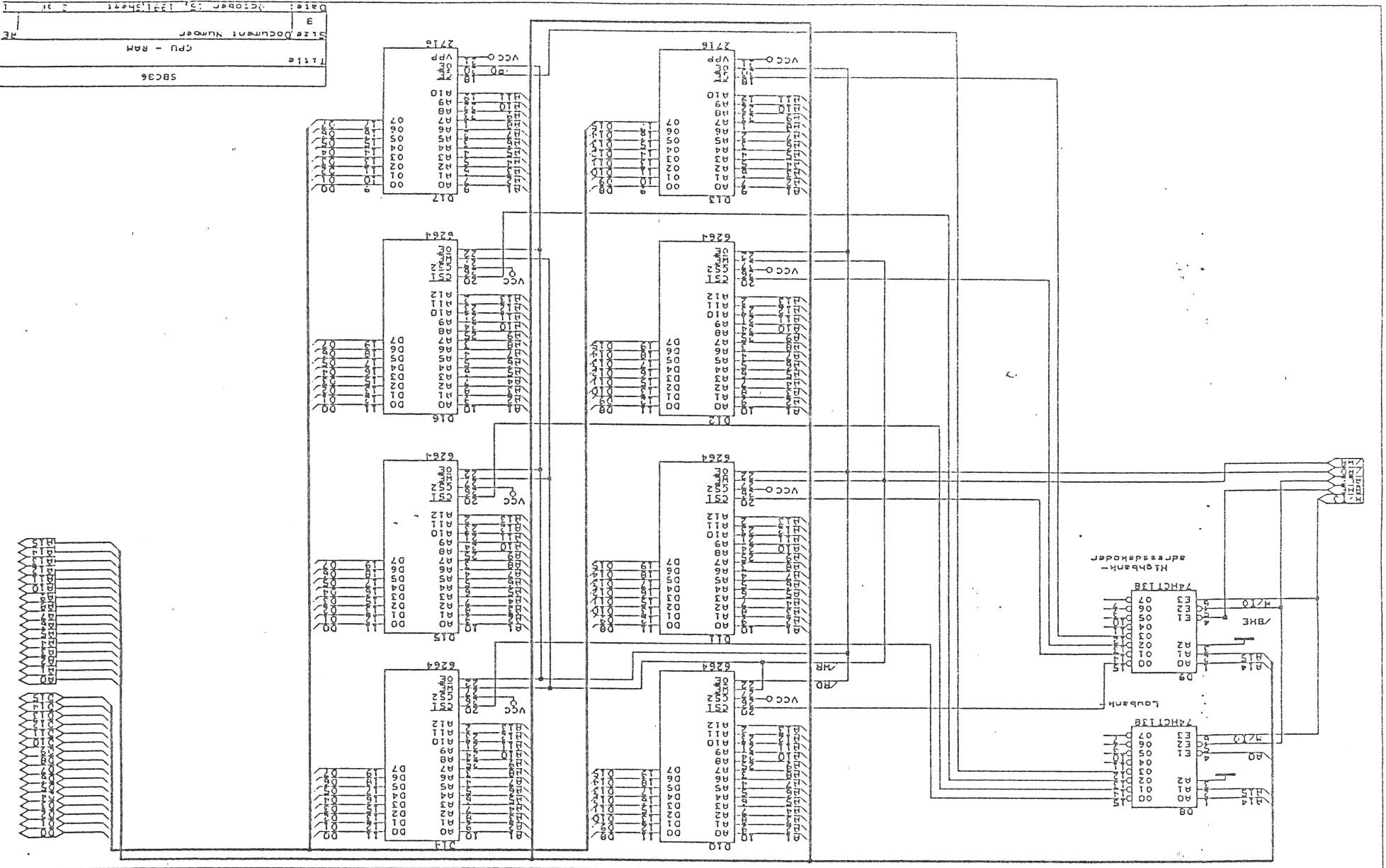
- Schalter S29:

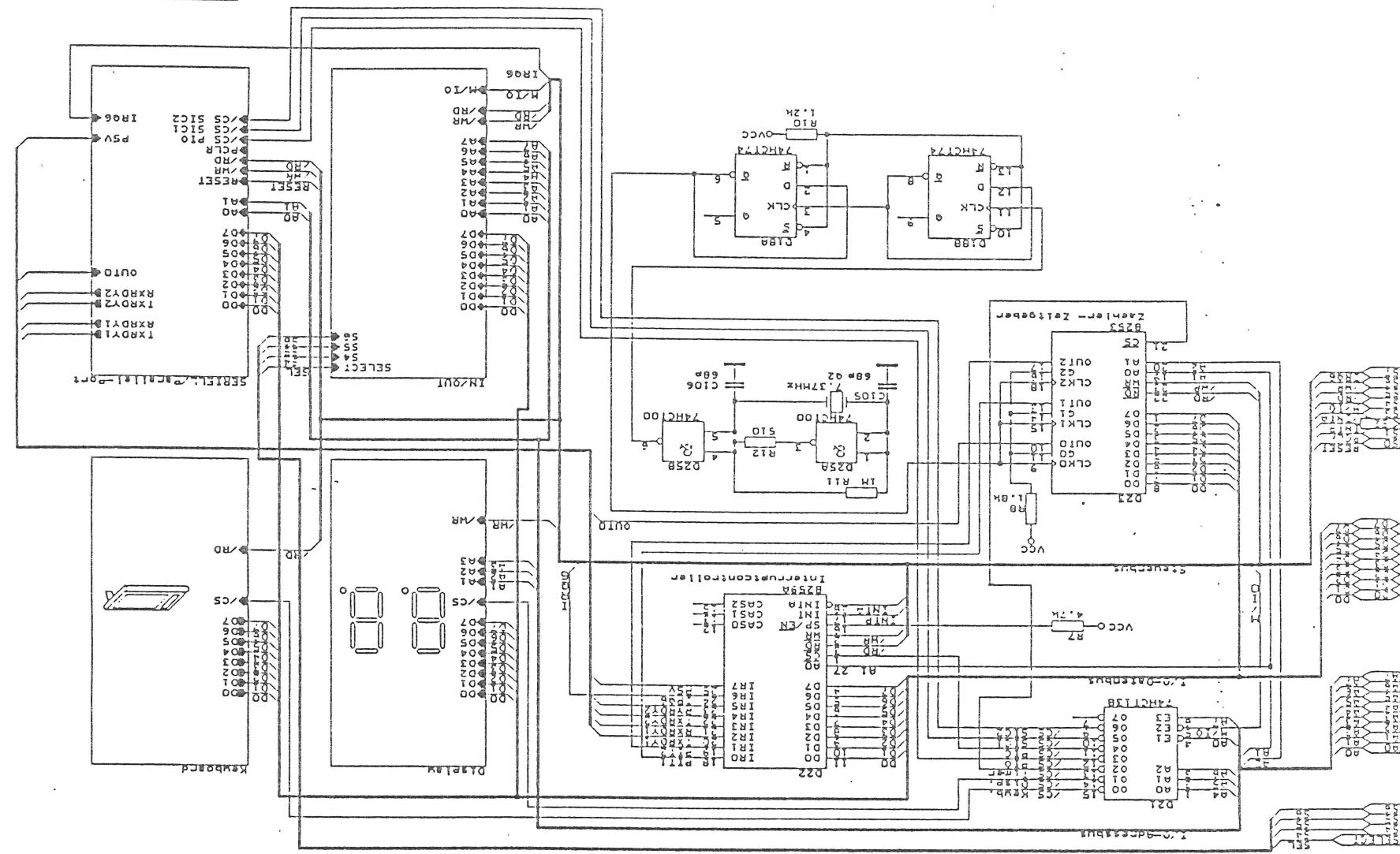
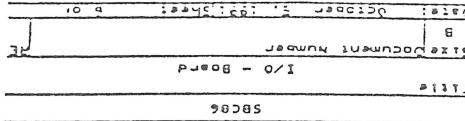
Umschalten CTS der 2. seriellen Schnittstelle



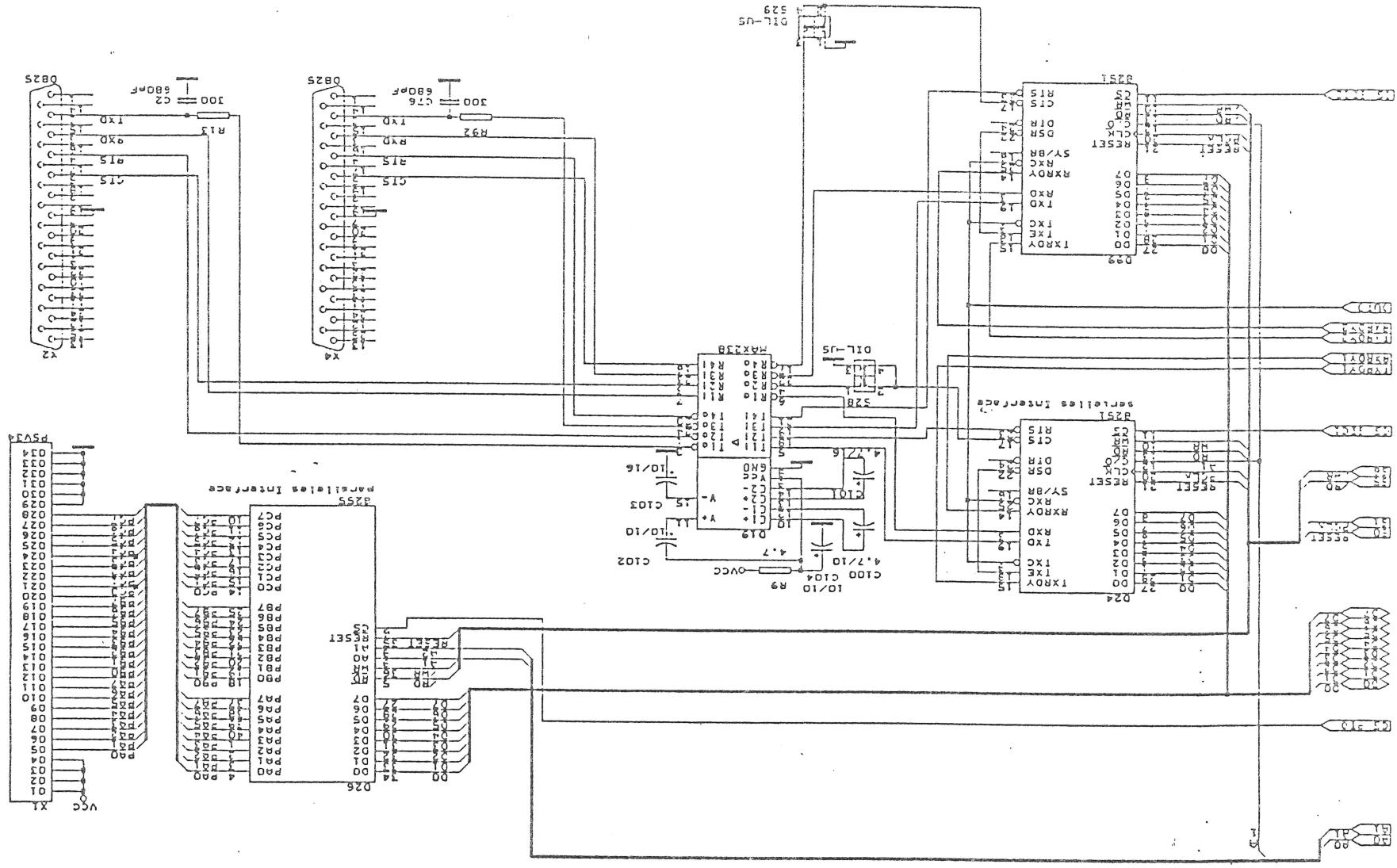
Anhang - C

Schaltpläne des SBC-86





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