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- Pin connection diagram for U1 (M2_A+E) showing connections to a 3.3V supply and GND. The diagram lists various pins and their functions, with some pins marked as reserved or disabled.
- | Pin | Signal | Notes |
|-----|-----------------------------|----------------|
| 52 | PERST0 | RESERVED |
| 53 | CLKREQ0 | |
| 55 | PEWAKE0 | |
| 35 | PET_0+ | VENDOR_DEFINED |
| 37 | PET_0- | VENDOR_DEFINED |
| 41 | PER_0+ | VENDOR_DEFINED |
| 43 | PER_0- | |
| 59 | PERn0 | I2C_DATA |
| 61 | RESERVED/PETp1 | I2C_CLK |
| 63 | RESERVED/PETn1 | ALERT |
| 65 | RESERVED/PERp1 | |
| 67 | RESERVED/PERn1 | |
| X3 | USB_D+ | |
| X5 | USB_D- | |
| X66 | UIM_SWP/PERST1 | |
| X68 | UIM_POWER_SNK/CLKREQ1 | |
| X70 | UIM_POWER_SRC/GPIO1/PEWAKE1 | |
| 20 | UART_WAKE | |
| 22 | UART_RXD | |
| 32 | UART_TXD | |
| 34 | UART_CTS | |
| 36 | UART_RTS | |
| 17 | SDIO_DATA2 | |
| 19 | SDIO_DATA3 | |
| 21 | SDIO_WAKE | |
| 23 | SDIO_RESET | |
| 50 | SUSCLK | |
| 47 | REFCLKp0 | COEX1 |
| 49 | REFCLKn0 | COEX2 |
| 71 | RESERVED/REFCLKp1 | COEX3 |
| 73 | RESERVED/REFCLKn1 | |
- Connections to 3.3V and GND are indicated at the top and bottom of the diagram.