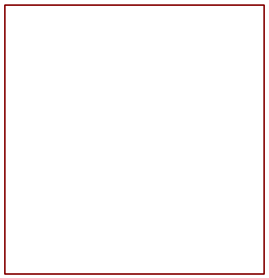
[1] POWER

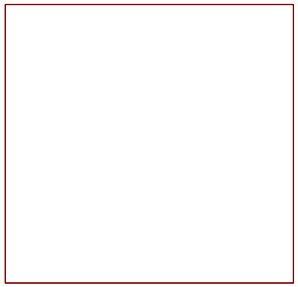
POWER



File: Power_adacus_sch.kicad_sch

[2] MCU

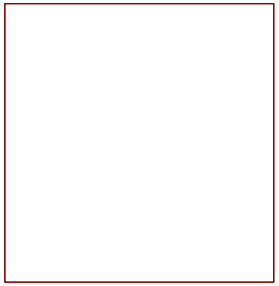
MCU



File: MCU_ADACUS_sch.kicad_sch

[3] ADC

ADC



File: ADC_ADACUS_sch.kicad_sch

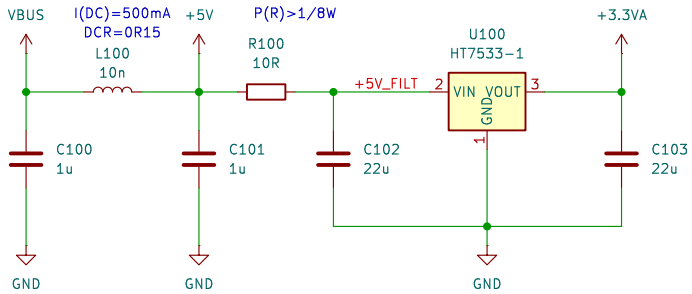
[4] DAC

DAC

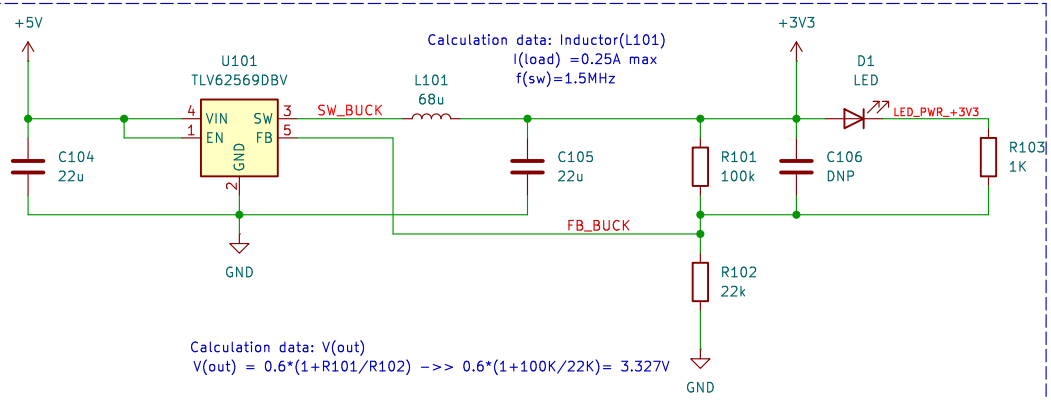
File: DAC_ADACUS_sch.kicad_sch

Sheet: / File: ADACUS.kicad_sch		
Title:		
Size: A4	Date:	**Rev:**
KiCad E.D.A. 8.0.3		Id: 1/5

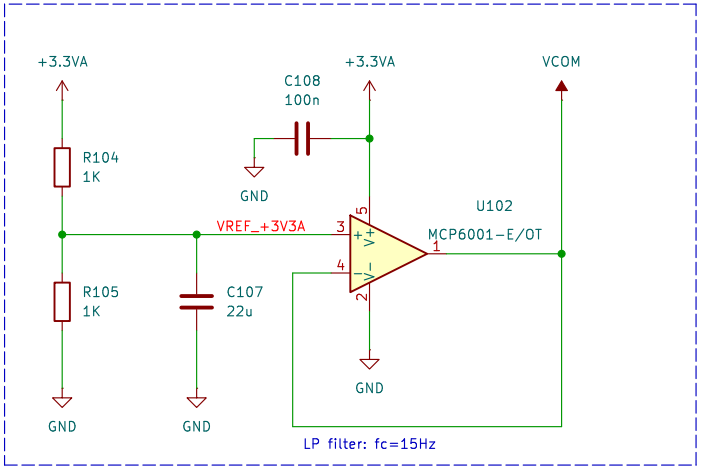
ANALOG LDO, +3V3A



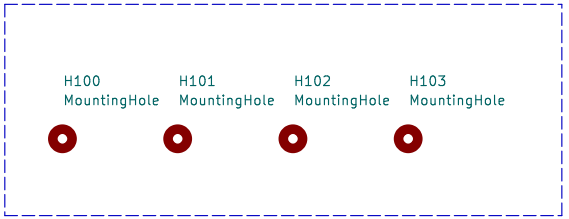
DIGITAL BUCK CONVERTER +5V <-> +3V3



BIAS GENERATOR, +3V3A <-> VCOM

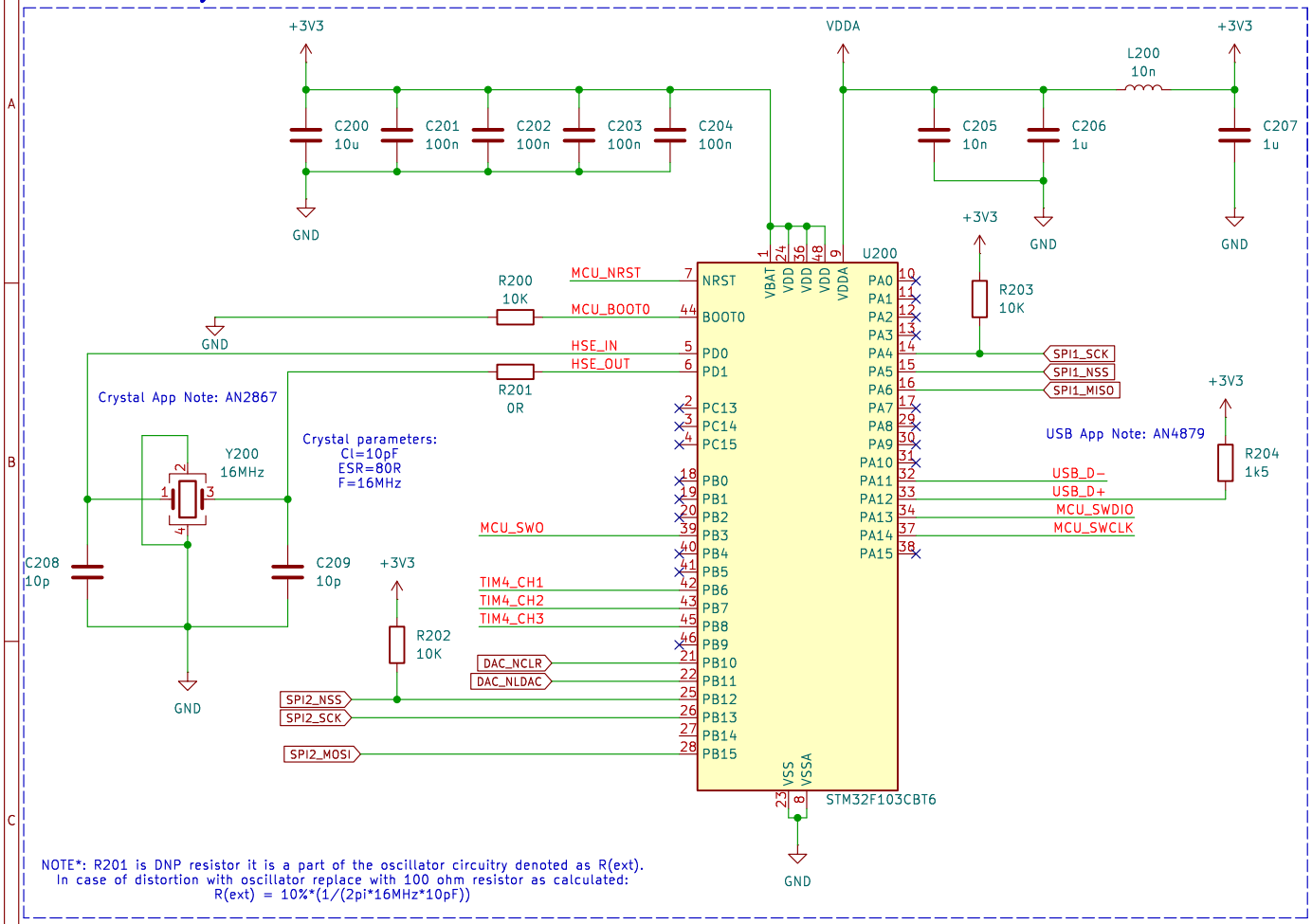


MECHANICAL



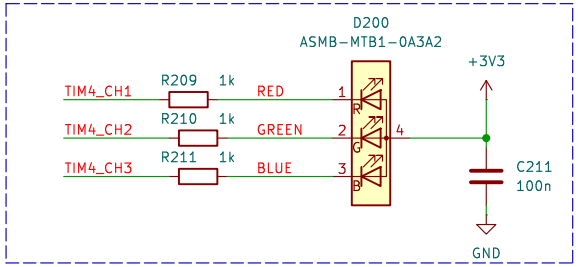
Head-PIN (TS)		
Sheet: /POWER/		
File: Power_adacus_sch.kicad_sch		
Title: Power Schematic		
Size: A4	Date: 7/24/2024	**Rev:**
KiCad E.D.A. 8.0.3		Id: 2/5

MCU +3V3,+3V3A

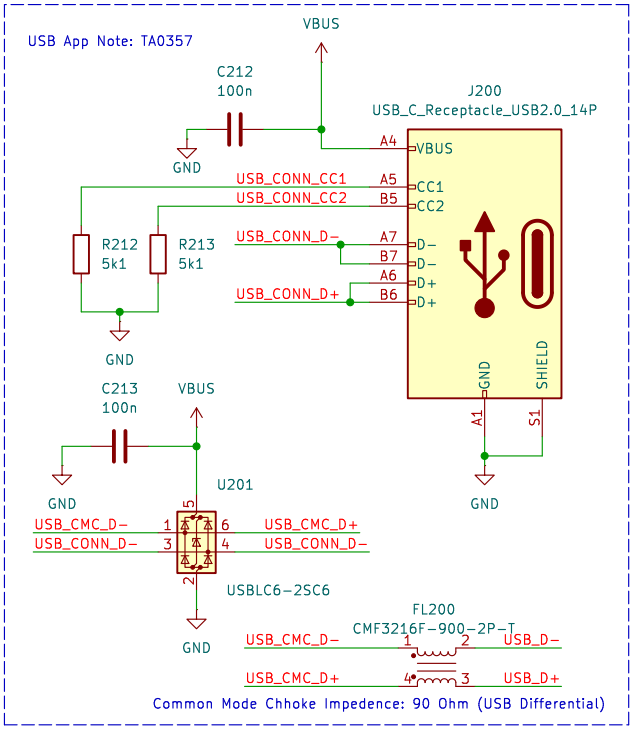


NOTE*: R201 is DNP resistor it is a part of the oscillator circuitry denoted as R(ext).
In case of distortion with oscillator replace with 100 ohm resistor as calculated:
R(ext) = 10%*(1/(2pi*16MHz*10pF))

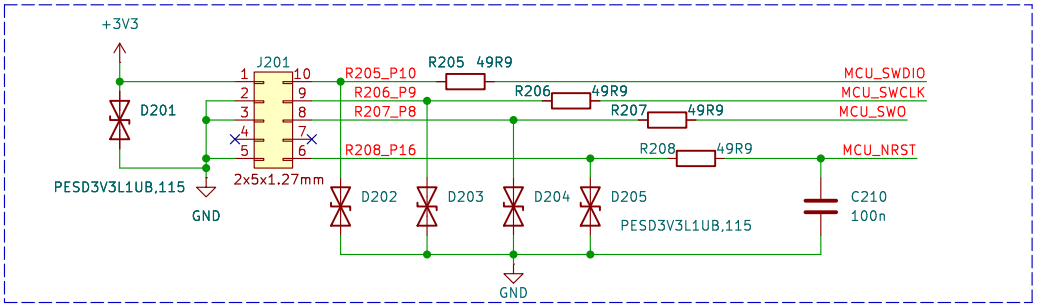
TIMER +3V3



USB TYPE C, ESD PROTECTION +VBUS



SWD CONNECTOR +3V3



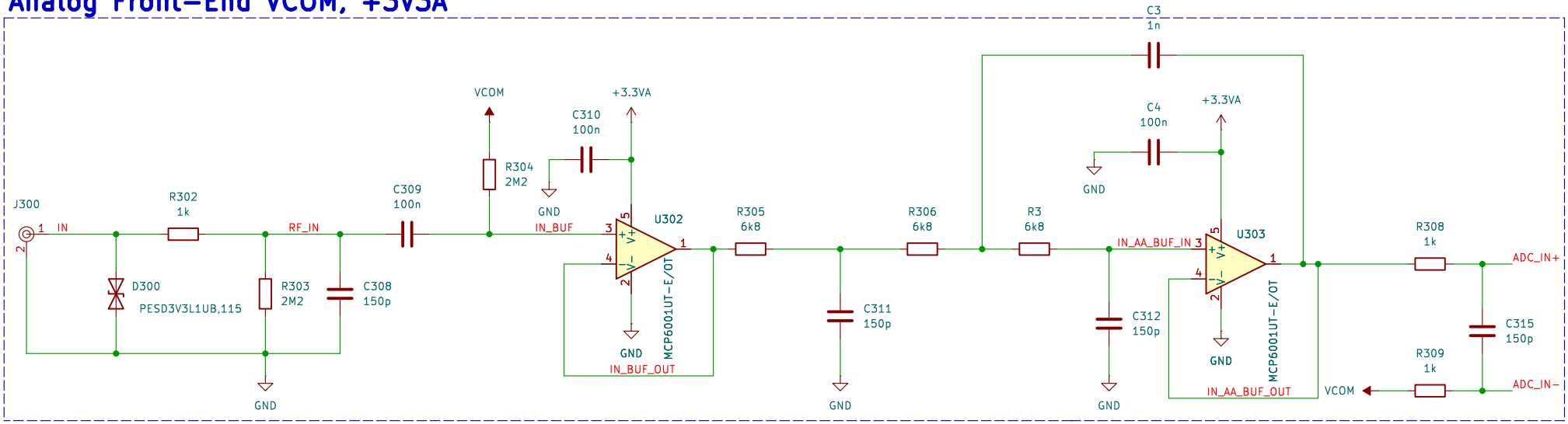
Head-Pin (TS)

Sheet: /MCU/
File: MCU_ADACUS_sch.kicad_sch

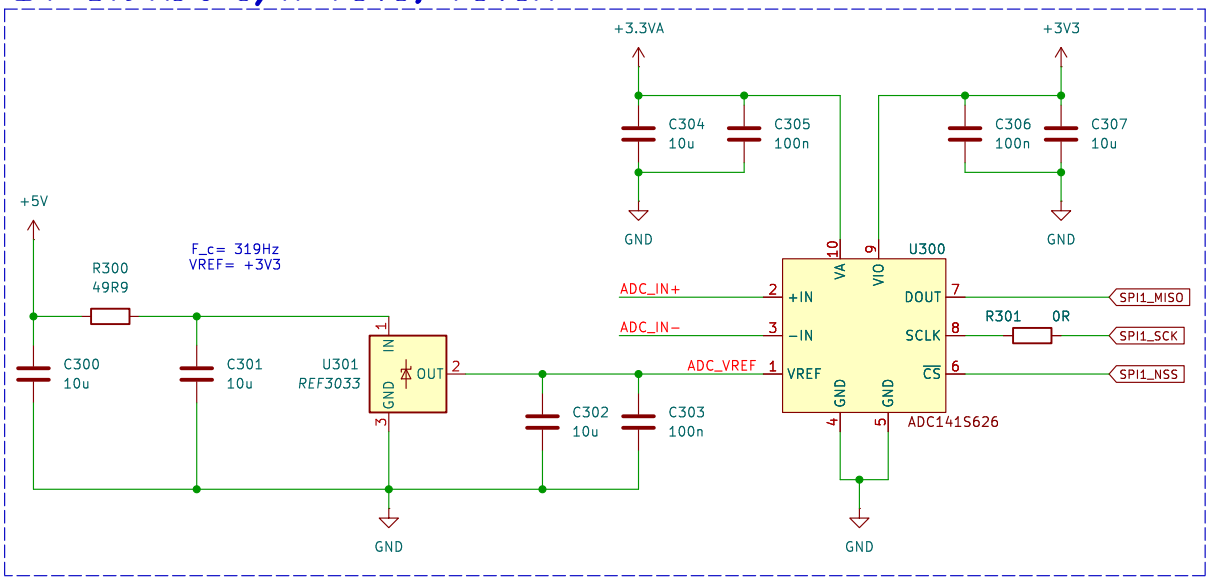
Title: MCU Circuitry

Size: A4	Date: 2024-07-24	**Rev:**
KiCad E.D.A. 8.0.3		Id: 3/5

Analog Front-End VCOM, +3V3A



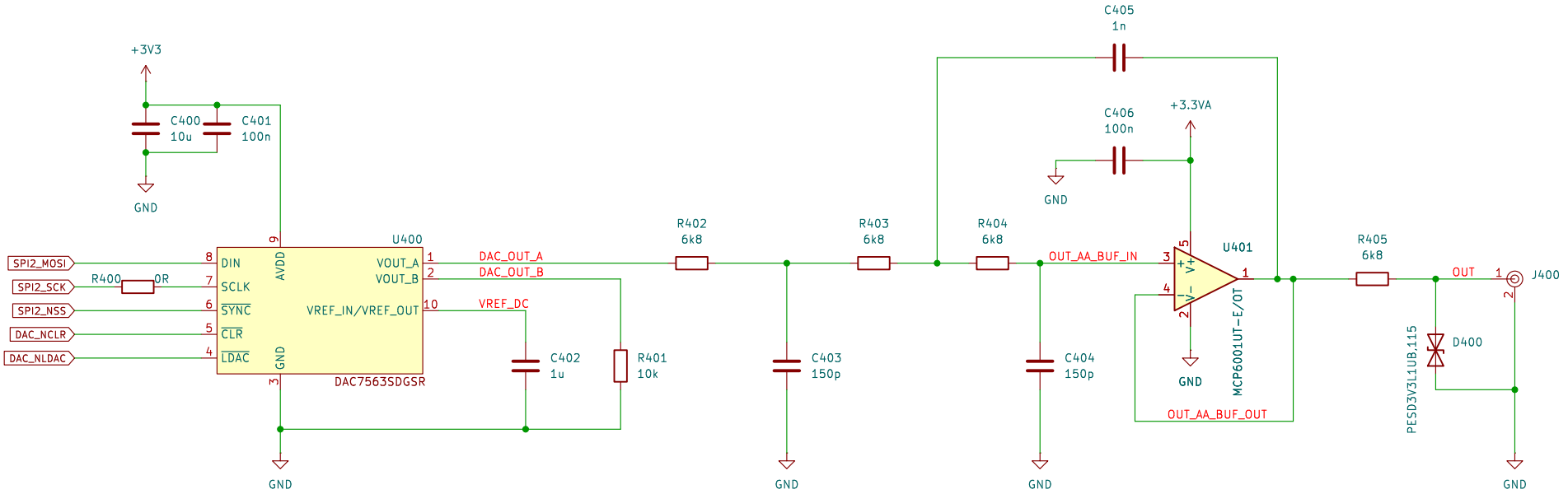
14-Bit ADC S/H +3V3, +3V3A



Head-Pin (TS)		
Sheet: /ADC/		
File: ADC_ADACUS_sch.kicad_sch		
Title: ADC Schematic		
Size: A4	Date: 2024-08-06	**Rev:**
KiCad E.D.A. 8.0.3		Id: 4/5

DAC, +3V3A

Anti-Aliasing filter and Output Connector BNC, +3V3A



Head-Pin (TS)		
Sheet: /DAC/		
File: DAC_ADACUS_sch.kicad_sch		
Title: DAC Schematic		
Size: A4	Date: 2024-08-06	**Rev:**
KiCad E.D.A. 8.0.3		Id: 5/5