

刘万涛 Ivan

EE UNDERGRADUATE

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📍 Zhejiang China



EDUCATION

- **College of modern science and technology, China JiLiang University**
BEng in Electrical Information Engineering
2019.09—2023.06
Digital electronics/Analog electronics/Digital system design (Verilog)/Algorithm and Data Structure/5g wireless technology and deployment/Digital signal processing/Digital image processing/Object oriented programming (java&c++)

SELECTED AWARDS

- **第九届大唐杯全国大学生新一代信息通信技术大赛**
Fisrt Prize in Zhejiang Contest District
2022.01—2022.04
As a member of a two person team, I am mainly responsible for the relevant contents of the Internet of vehicles
- **第十三届蓝桥杯大赛(EDA设计组别)**
Second Prize in Zhejiang Contest District
2021.11—2022.04
Design schematic diagram and PCB layout based on LiChuang EDA & basic theory of electronic technology
- **第十三届挑战杯创新创业计划竞赛**
Second Prize in Zhejiang Contest District
2021.11—2022.05
As the team leader, responsible for the overall planning and promotion of the project and task allocation
- **NECCS 2022**
Third Prize in Zhejiang Contest District
2022.02—2022.05
Individual competition, mainly testing English reading comprehension, dictation ability and knowledge breadth

SELECTED PROJECTS

- **Personal Blog** 
Owner
2021.01—now
Based on Hexo, NodeJS's Module
- **EE-Wiki** 
Founder
2021.01—now
Based on Mkdocs and Git.
Slogan:"Co-building wiki, making knowledge open source!"
- **Traffic-Light-System**
Designer
2022.04
Based on Artix-7/Vivado Platform、Using VerilogHDL language、Tips: FSM、Decoding display...

CAPACITY

 **Dev-Lang:**

Verilog/C++/Java/HTML+CSS+JS



Dev-Tools:

Git/Linux/VIVADO/Verilator/
Matlab/Multisim/Altium Designer



Language:

CET4: 574

CET6: 606

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(Click or scan for the newest version of resume)

