

GPS Orion is reference design to demonstrate the capability of the Zarlink Semiconductor GP2000 Global Positioning chip set. It is a fully functional GPS receiver design which provides full 12 channel parallel tracking hardware and software for GPS system integrators and designers of GPS receiver subsystems. This approach offers a complete GPS receiver 'engine' design with the design information being freely available, providing the ability to integrate GPS with dedicated IC components. This allows wider design flexibility and lower cost solutions than using third-party module-based receivers. It enables the GP2000 chipset to be evaluated in real applications.

Features

- Small board size - 95 mm x 50 mm
- 12 channel GPS Receiver architecture
- Highly integrated miniature silicon Front-end IC
- Low cost 12 Channel Correlator IC
- 32 bit RISC processor with spare processing
- Easy to embed into host systems
- Non-volatile data retention
- Supported by Development System (GPS Architect) with access to GPS software source code at minimal cost

The GPS Orion comprises two boards : a GPS Receiver board, and an Interface board. The Receiver board is based on the GP2000 chip set plus complementary components which make a complete GPS Receiver. The Interface board provides power and data conditioning interfaces to peripheral equipment, and long term memory backup.

GPS Receiver Board

The GPS Orion receiver architecture uses integrated circuits manufactured by Zarlink Semiconductor. The GP2000 chipset comprises the GP2010 or GP2015 RF downconverter, the Dynex Semiconductor DW9255 35.42MHz (2MHz BW) IF SAW filter, the GP2021 correlator IC, and the 32-bit ARM60-B RISC processor.

The use of a GP2015 RF downconverter offers good immunity from out-of-band noise, especially when used with the DW9255 surface mount SAW filter as a 2nd IF filter. The design offers superior RF performance with a high degree of noise rejection from digital sources. The 2-bit sampled IF, in conjunction with triple-conversion architecture provides best-in-class RF jamming resistance.

The GP2021 12 channel correlator can be used to track C/A code for the Standard Positioning Service. Any channels not used can be deselected to save power. The digital interface is compatible with most 16 or 32-bit processors and the IC includes interface logic to allow it to be easily coupled to the ARM60-B 32 bit RISC processor.

The ARM60-B 32-bit RISC microprocessor offers high

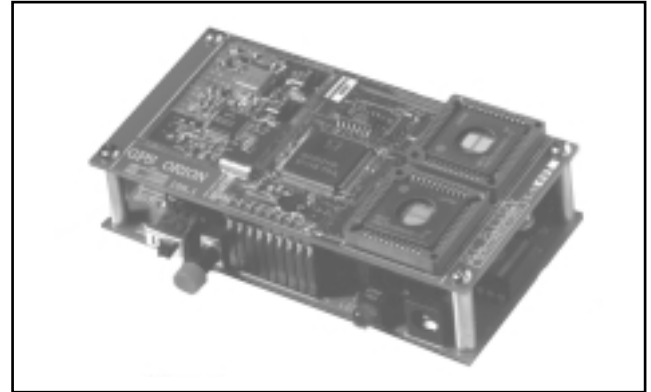


Figure 1 - GPS Orion Receiver Board with Interface Board

instruction throughput and fast real-time interrupt response. The ARM60-B offers superior performance for 12 channel processing, which is important for applications such as vehicle tracking and automotive applications.

Interface Board

The Interface board provides those functions which would normally be included elsewhere in a GPS system. These functions include : voltage regulation, battery back-up (1000 hours memory retention), and serial interfaces to output the information and to input commands and DGPS correction data.

Software

The software for GPS Orion is stored in EPROM and is self-booting. The software provides many facilities including:

- NMEA 0183, or proprietary WINMON data interfaces
- Navigation position filtering
- Satellite Almanac and Ephemeris save and upload
- Reliable satellite tracking
- RTCM SC-104 Differential Correction input

Detailed Design Data and Software

All support material for the GPS Orion Reference design can be downloaded from the Zarlink website (www.zarlink.com) by visiting the Product Profile page of the GP2015 or GP2021. This information is supplied as a single ZIP file (size 4381KB).

Data Retention

The Receiver board includes a supercap (0.22 F) which gives approximately 4 hours retention of volatile data (position, satellite ephemerides, etc.) in isolation from the Interface board. The Interface board includes a rechargeable battery giving approximately 1,000 hours data retention.

Related Products

Part No	Description	Data Sheet Reference
DW9255	35.42 MHz SAW Filter (Dynex Semiconductor)	DS3861
GP2015	GPS RF Front End in TQFP package	DS4374
GP2021	12 Channel Correlator	DS4077
P60ARM-B	ARM60-B 32 Bit RISC Processor	DS4004
GPS Architect	12 Channel GPS Development System	DS4605

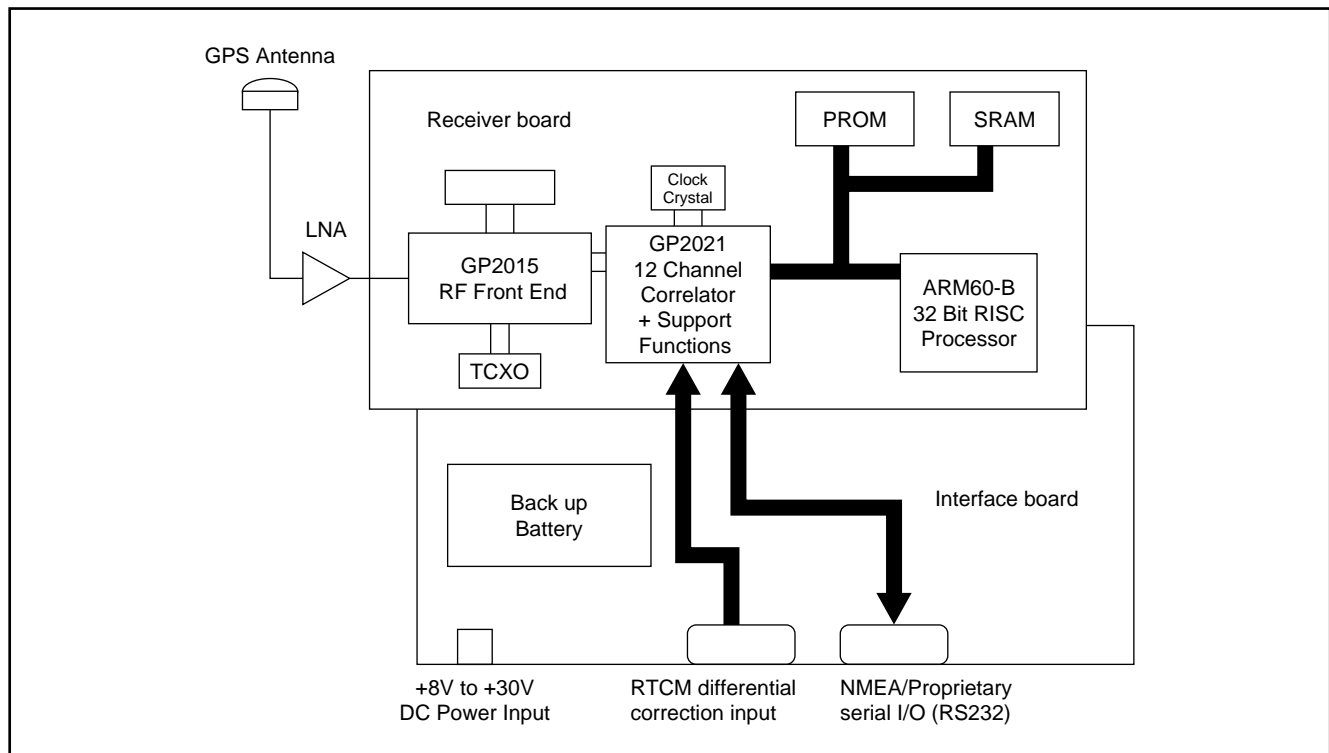


Figure 2 - GPS Orion Architecture

Outline Specification

Receiver Characteristics

GENERAL:	L1 frequency (1575.42MHz), C/A code direct-sequence spread-spectrum, 12 parallel channel continuous-tracking receiver, using 2-bit digital quantization.	
DATUM:	WGS-84 only	
POSITION ACCURACY: (standard)	25 metres Spherical Error Probability (SEP); 100 metres (2D Root Mean Square) with S/A active.	
POSITION ACCURACY: (DGPS)	3 metres SEP, with S/A active.	
TIME-TO-FIRST-FIX (TTFF - Ref Notes)	Typical	90%
● Cold start:	105 seconds	<165 seconds
● Warm start:		<60 seconds
● Hot start:	11 seconds	<15 seconds
RE-ACQUISITION AFTER SIGNAL LOSS:	< 2 seconds (90%)	
ANTI-JAMMING RESISTANCE:	-20dBm CW to within ± 7.5 MHz of L1 (for <3dB degradation in GPS SNR).	

DATA I/O

WINMON MODE: (J2 socket)	Proprietary Zarlink Semiconductor ASCII interface protocol. Full input/output interface, controlling all user-definable options. 19200 baud, 8-bits, No Parity, 1 stop bit.
NMEA MODE: (J2 socket)	NMEA 0183 V2.01 with GGA, RMC, GSV & GSA (output only - not configurable). 4800 baud, 8-bits, No parity, 1 stop bit. Receiver configuration is still available in NMEA mode, by using WINMON command protocol at 4800 baud, 8-bits, No parity, 1 stop bit.
UPDATE RATE:	1 per second (both modes)
RTCM DIFFERENTIAL CORRECTIONS: (J1 socket)	RTCM SC-104 (input only) - message types 1, 2 & 9-3 9600 baud, 8-bits, No Parity, 1 stop bit

PHYSICAL CHARACTERISTICS - GPS ORION RECEIVER BOARD

PRIME POWER: Vdd into receiver board	+5 volts DC, +/- 10%
EXTERNAL BACKUP POWER: Vdr	>+2.2 volts DC, <+5.5volts DC
ACTIVE-ANTENNA POWER:	+5 volts DC available
POWER CONSUMPTION: Vdd	GPS Receiver Board only: 370mA 1.85W With Antenna: 395mA 1.98W
MEMORY/RTC BACKUP POWER CONSUMPTION: Vdr	60µA @ Vdr > +2.2volts DC
INSTALLED SUPERCAP BACKUP LIFE:	4 hours approx.
DATA I/O LEVELS:	CMOS TTL levels (0v & +5v)
SIZE: (GPS Orion Receiver Board only)	95mm x 50mm x 20mm (with RF connector) 95mm x 50mm x 12mm (no RF connector)
SIZE: (with GPS Orion Interface Board attached)	95mm x 50mm x 30mm
CONNECTORS:	RF: SMA socket I/O: 9-pin (1 x 9), 0.1" pitch plug

PHYSICAL CHARACTERISTICS - GPS ORION INTERFACE BOARD

INPUT POWER SUPPLY:	+8 volts to +30volts DC
MEMORY / RTC BACKUP BATTERY:	+3.6 v NiCad, 110mAh
BATTERY CHARGE - ● RATE: ● TOTAL RECHARGE TIME:	7.5mA typical 18 hours approx.
BATTERY BACKUP LIFE (when supplying Vdr pin on GPS Orion receiver board):	8 weeks approx.
DATA I/O LEVELS:	RS232 levels (±10v)
OUTPUT DATA-MODE CHANGE:	Slide switch
SOFT RESET:	Push button
CONNECTORS:	I/O to Orion: 9-pin (1 x 9), 0.1" pitch socket Power: 2.5mm power socket (centre positive) RS232: 2 x 9 pin D-type plug

ENVIRONMENTAL CHARACTERISTICS (RECEIVER BOARD ONLY)

OPERATING TEMP:	-40°C to +85°C
STORAGE TEMP:	-50°C to +110°C

NOTES:

- 1) COLD START:- A cold start results when there is no valid Almanac or Ephemeris information available for the satellite constellation in SRAM, or when the time and/or position information is NOT known (i.e starts at 0 in both cases).

Also a cold start will be initiated if an Almanac is valid, but a fix cannot be achieved within 10 minutes of power-up. This could occur if the receiver position has moved significantly since it was last powered-up, but the position change and time are NOT initialised by the user
- 2) WARM START:- A warm start results when there is a valid Almanac, and the initial time and position are known in SRAM, but the ephemeris is NOT valid (i.e more than 4 - 6 hours old).
- 3) HOT START:- A hot start results when there is a valid Almanac, valid ephemeris (i.e. less than 4 - 6 hours old), and when accurate time and position information are also known in SRAM (position error less than 100km, time error less than 5 minutes). This can be simulated by a momentary Soft-Reset (i.e. press the push-button on the GPS Orion interface Board).



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