





12C De	evices		
ADDR	Part	Type	Location
0×10	U203	BQ77PL900	B/PM
0×54	U501	EEPROM	BBB

 0x10
 U203
 BQ77PL900
 B/PM

 0x54
 U501
 EEPROM
 BBB

 0x90
 U402
 LTC2991
 DC-DC

 0x98
 U203
 LTC2990
 Power In

 0x9A
 U301
 LTC2990
 B/PM

NOTES:

- * Do NOT change ROCKET_IGNITE, pin default reset state is High—Z w/ pulldown resistor. Other pins can be configured in EEPROM at boot time.
- * All I2C devices on LTC3 are slaves. The BBB is the only master so the LTC will not need arbitration.

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Sheet: /BeagleBone Black Cape/ File: beaglebone_cape.sch

Title: LTC3 BeagleBone Black Cape Interface

Size: B	Date: 2016-09-04	Rev: A
KiCad E.D.A.	kicad 4.0.2+dfsg1-stable	ld: 3/6





