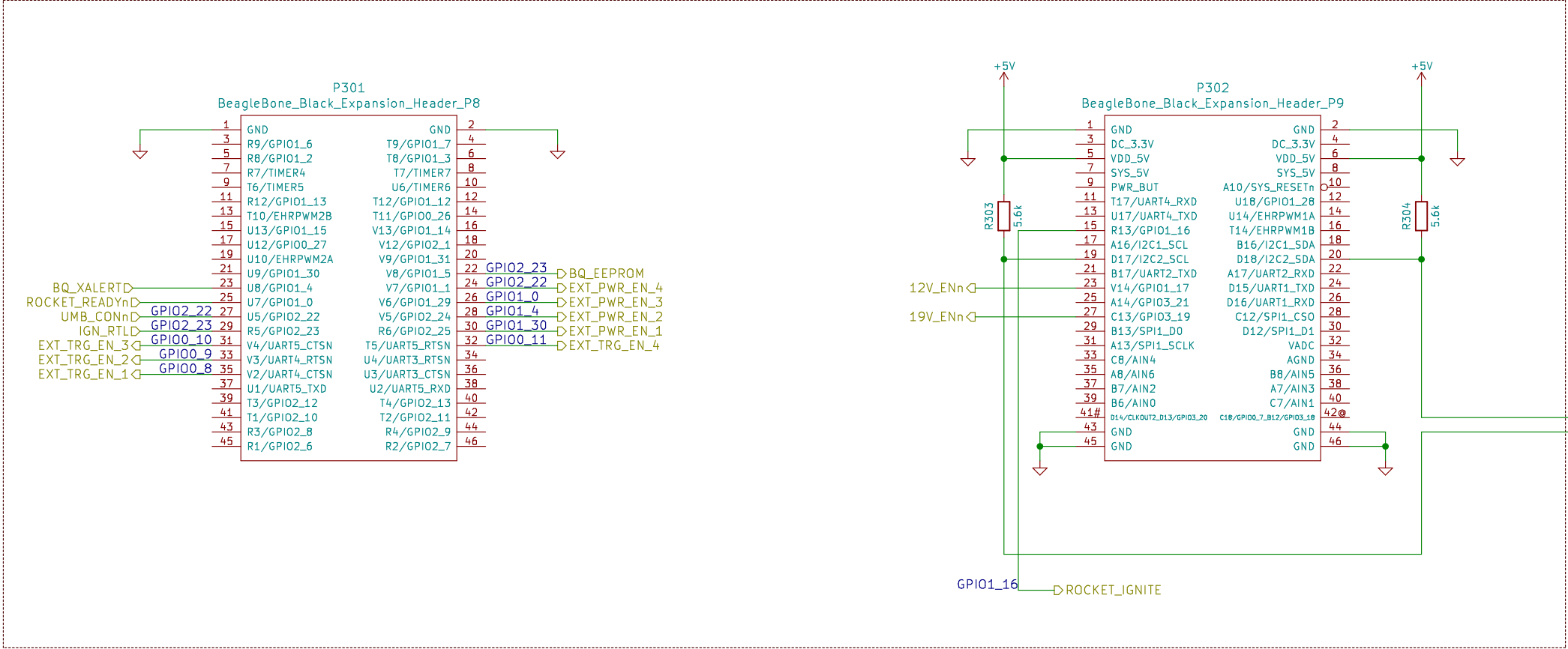
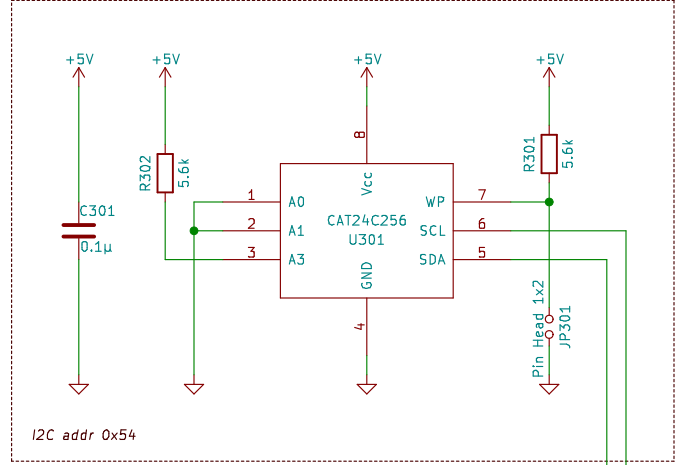


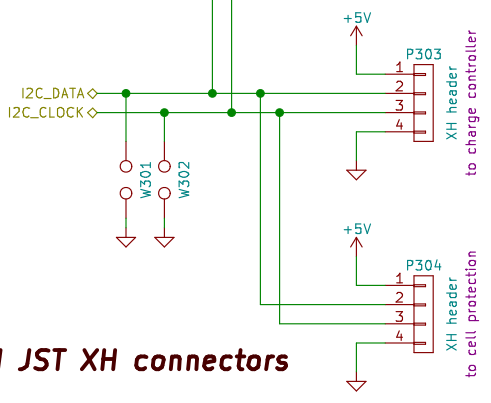
- NOTES
1. V\_sense should connect as close as possible to the largest load on the given power rail.
  2. Place Rset resistors as close to package pins as possible.
  3. Ceramic (Cin) capacitors should be located within 0.5 in of the input pins.
  4. We may need heat sinks on the converters. The datasheet indicates a range of 2W to 5W of power dissipation given our specs.
  5. Pay attention to the datasheet's recommendations regarding capacitor selection.



BeagleBone Expansion Headers



Cape EEPROM

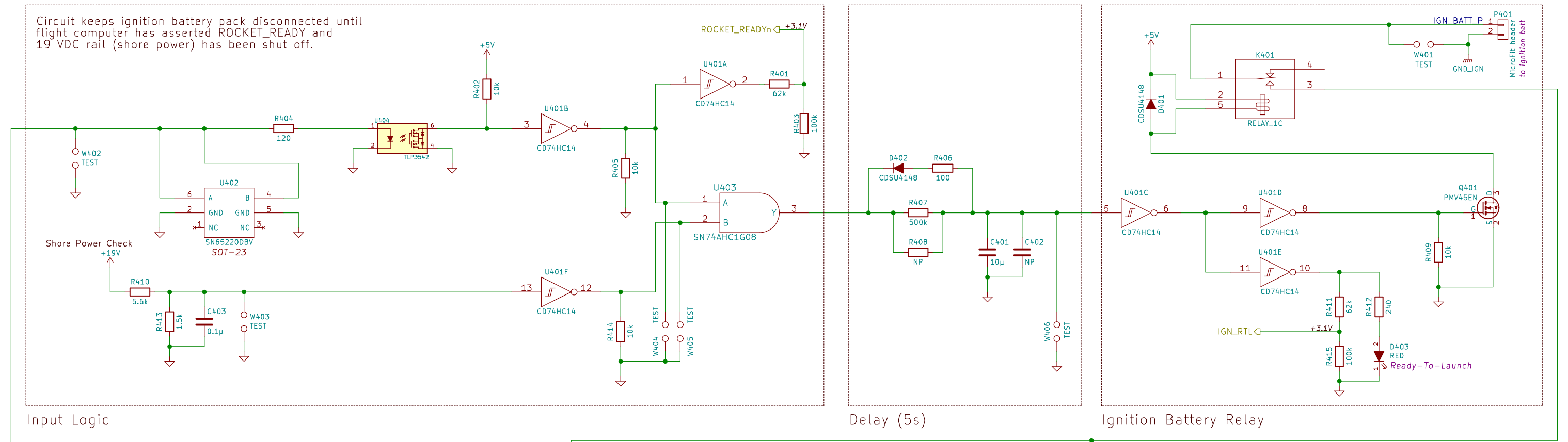


TODO: add off-board JST XH connectors

I2C Devices			
ADDR	Part	Type	Location
0x10	U203	BQ77PL900	B/PM
0x54	U501	EEPROM	BBB
0x90	U402	LTC2991	DC-DC
0x98	U203	LTC2990	Power In
0x9A	U301	LTC2990	B/PM

NOTES:  
\* Do NOT change ROCKET\_IGNITE, pin default reset state is High-Z w/ pulldown resistor. Other pins can be configured in EEPROM at boot time.  
\* All I2C devices on LTC3 are slaves. The BBB is the only master so the LTC will not need arbitration.

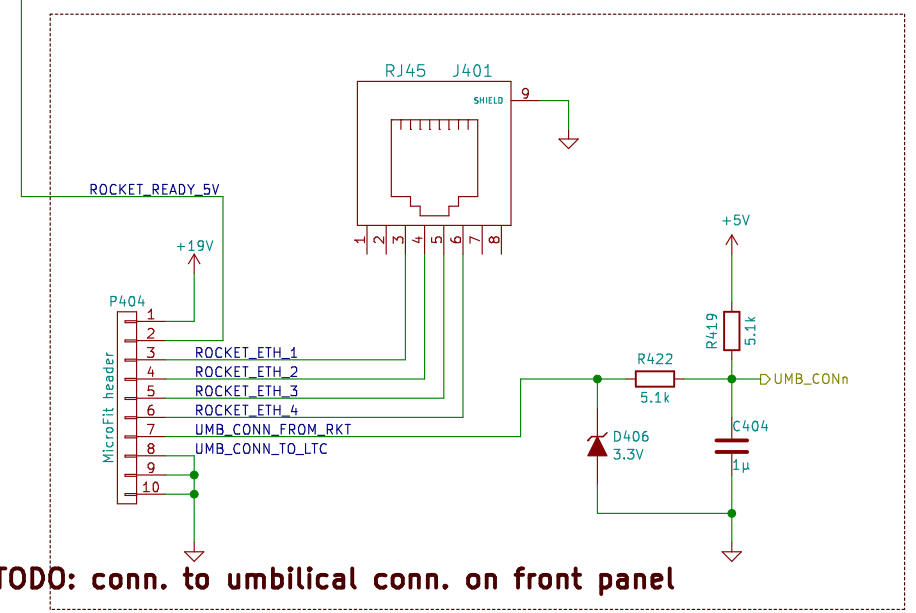
Circuit keeps ignition battery pack disconnected until flight computer has asserted ROCKET\_READY and 19 VDC rail (shore power) has been shut off.



Input Logic

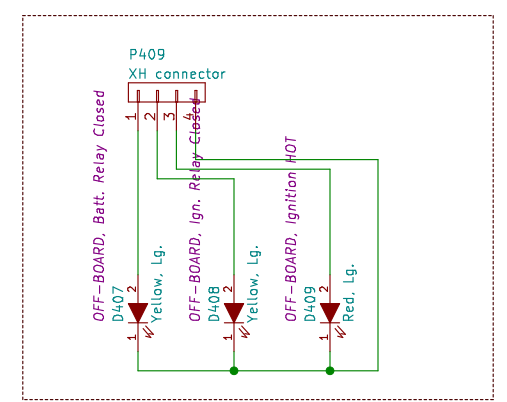
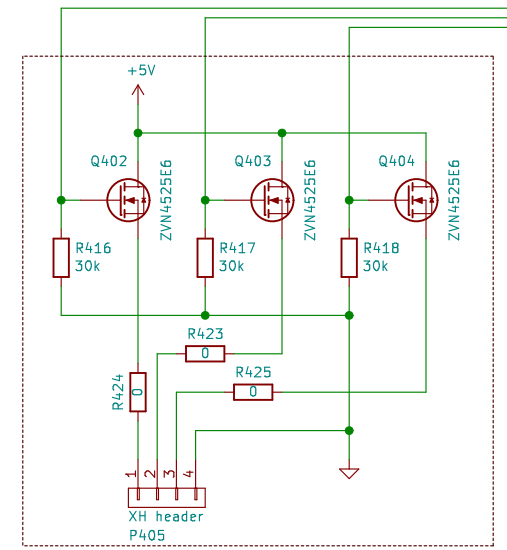
Delay (5s)

Ignition Battery Relay

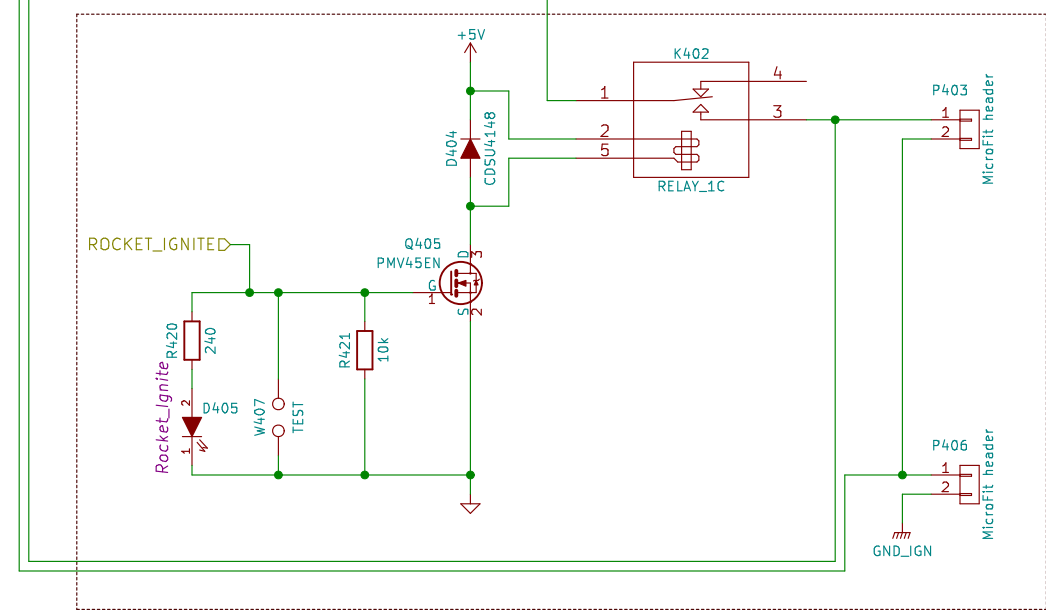


TODO: conn. to umbilical conn. on front panel

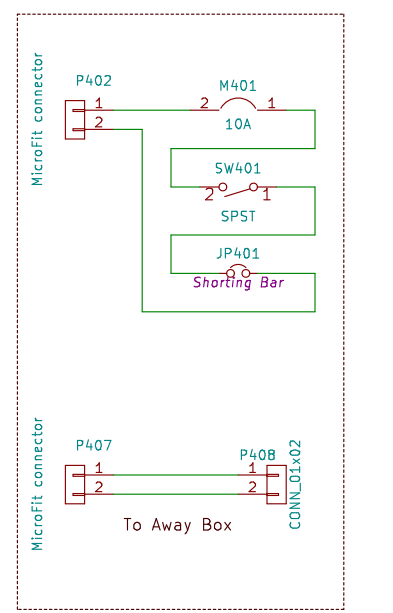
Rocket Umbilical  
Rocket-to-BeagleBone Ethernet



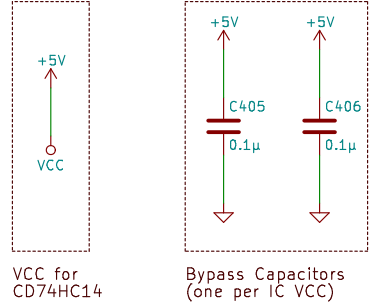
BAKERCON Hazard Gauge  
(super-bright LEDs, exterior panel)



Rocket Ignition Relay

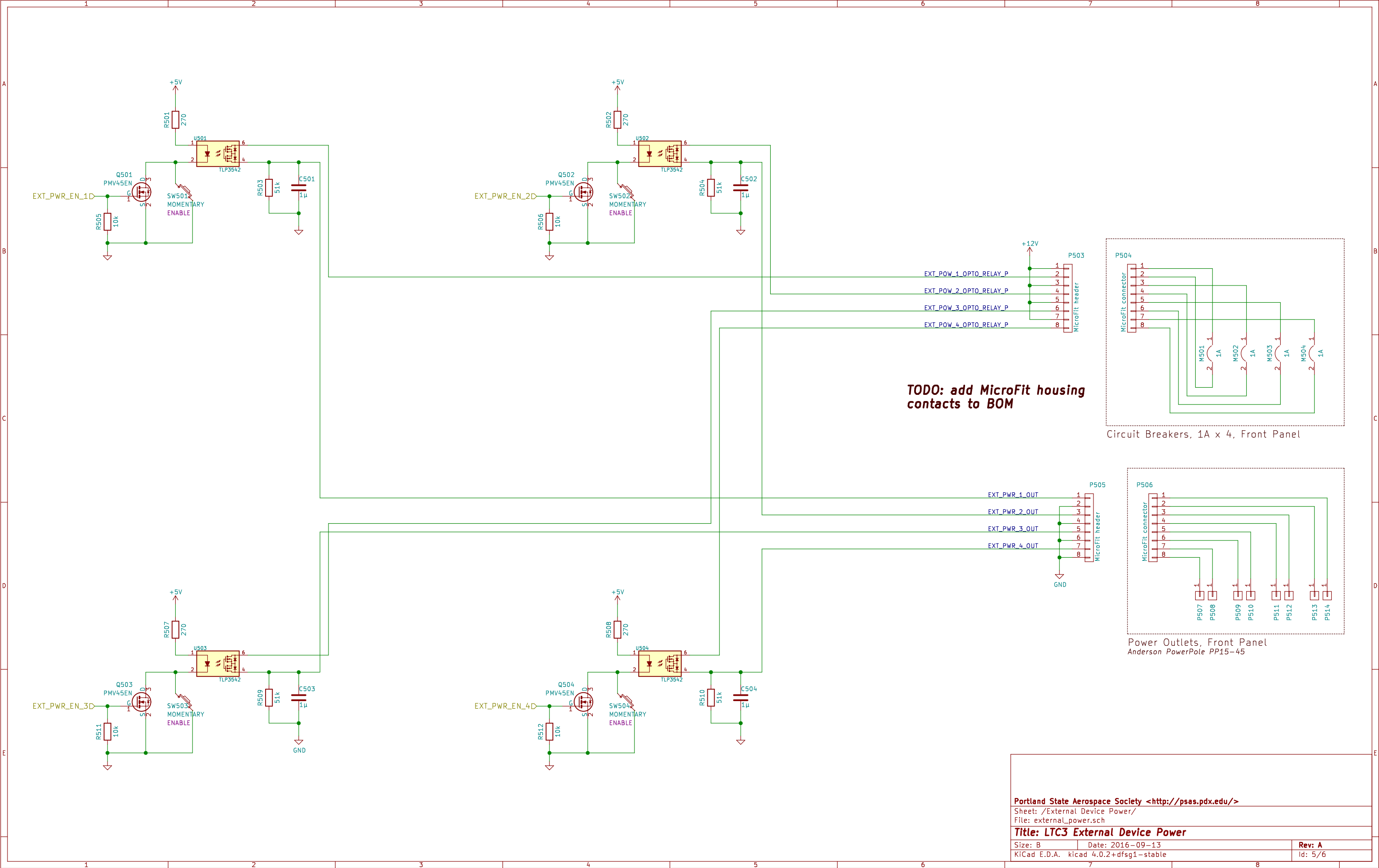


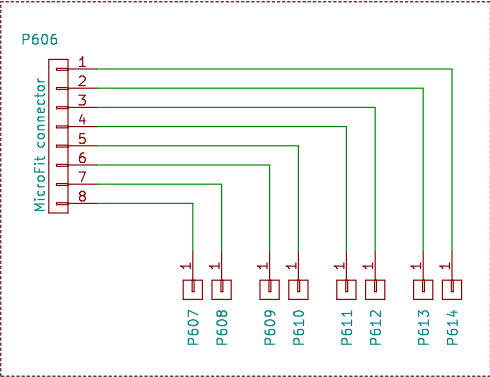
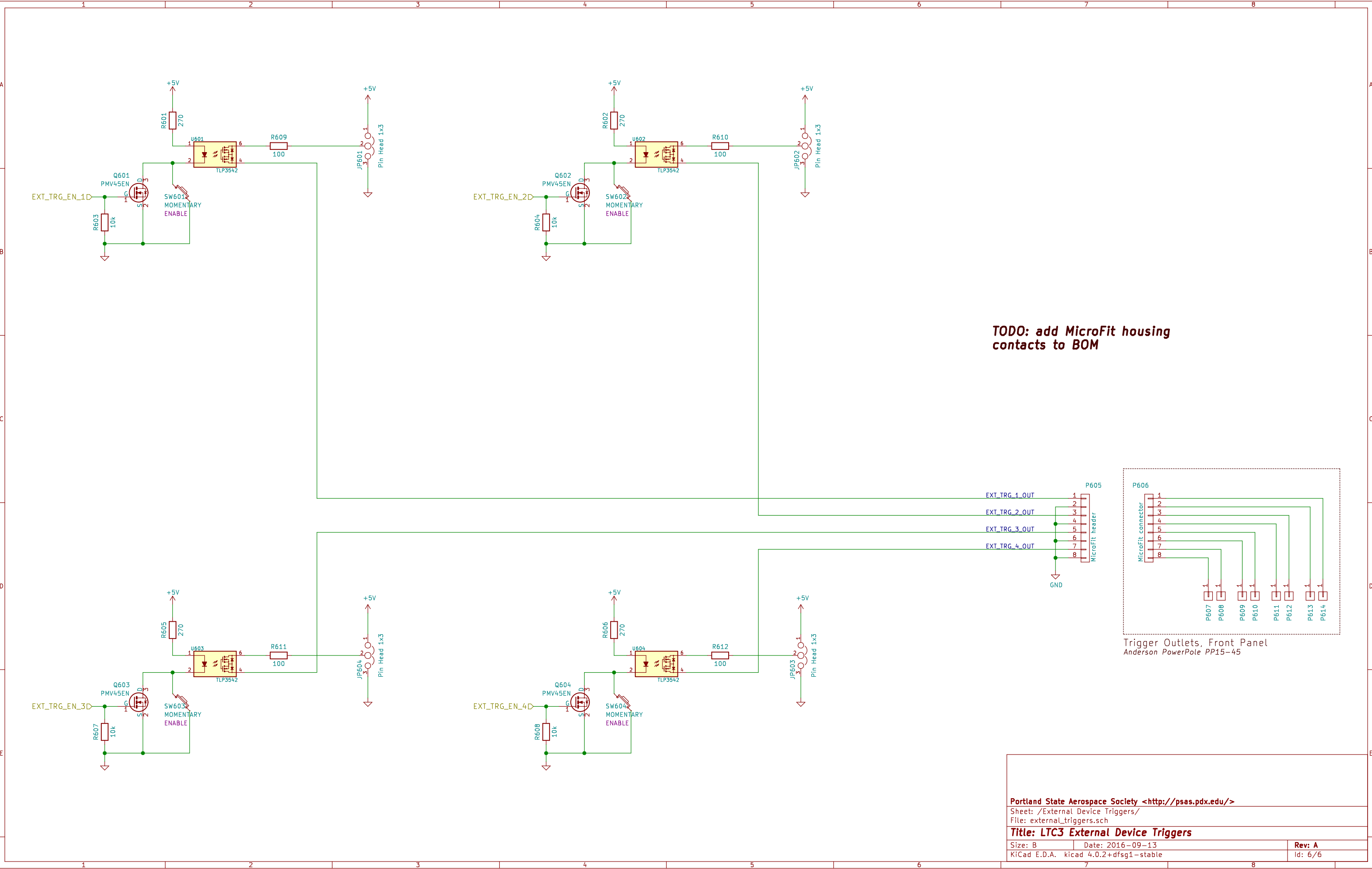
Breaker, Arm Switch,  
Shorting Bar, &  
Ignition Connector  
(front panel)



VCC for CD74HC14  
Bypass Capacitors  
(one per IC VCC)

TODO: add MicroFit housing  
contacts to BOM





Trigger Outlets, Front Panel  
Anderson PowerPole PP15-45