





12C Devices

U203

U501

U402 U203

U301 LTC2990

0×10

0×54

0×90 0×98 0×9A

- * Do NOT change ROCKET_IGNITE, pin default reset state is High—Z w/ pulldown resistor. Other pins can be configured in EEPROM at boot time.
- * All I2C devices on LTC3 are slaves. The BBB is the only master so the LTC will not need arbitration.

: Type 3 BQ77PL90	Location O B/PM								
1 EEPROM 2 LTC2991 3 LTC2990	BBB DC-DC Power In						Portland State Aerospace Society http://psas.pdx.edu/ Sheet: /BeagleBone Black Cape/ File: beaglebone_cape.sch	'>	
1 LTC2990	B/PM	Title: LTC3 BeagleBone Black Cape Interfa				· · · · · · · · · · · · · · · · · · ·			
							Size: B Date: 2016-09-04 KiCad E.D.A. kicad 4.0.2+dfsg1-stable	Rev: A	
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