











BeagleBone Expansion Headers

12C Devices			
ADDR	Part	Type	Location
0×10	U203	BQ77PL900	B/PM
0×54	U501	EEPROM	BBB
0×90	U402	LTC2991	DC-DC
0×98	U203	LTC2990	Power In
0×9A	U301	LTC2990	B/PM

NOTES

- * Do NOT change ROCKET_IGNITE, pin default reset state is High—Z w/ pulldown resistor. Other pins can be configured in EEPROM at boot time.
- * All I2C devices on LTC3 are slaves. The BBB is the only master so the LTC will not need arbitration.

Portland State Aerospace Society http://psas.pdx.edu/>

Sheet: /BeagleBone Black Cape/

File: beaglebone_cape.sch

Title: LTC3 BeagleBone Black Cape Interface
Size: B Date: 2016-05-07

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 Rev: A

 KiCad E.D.A. kicad 4.1.0-alpha+201605071002+677644ubuntu16.04.1
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