



NOTES:  
\* Page references are to the bq datasheet.  
\* Do not assign footprints to off-board components.  
\* bq77PL900 I2C addr is 0x10 (p.38).  
\* D301 and D304 are used to peak detect transients, and may not be necessary. Include footprints on PCB, but do not place parts.

UNCONNECTED PINS  
\* VOUT and IOUT are internally connected to ground when disabled (pp. 30, 33).  
\* XIRST is an open-drain output with an internal 3k pull-up to VLOG (p. 35).

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Sheet: /  
File: LTC3\_CellProtection.sch

**Title: Cell Protection**

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