



# CSE 4205

## Digital Logic Design

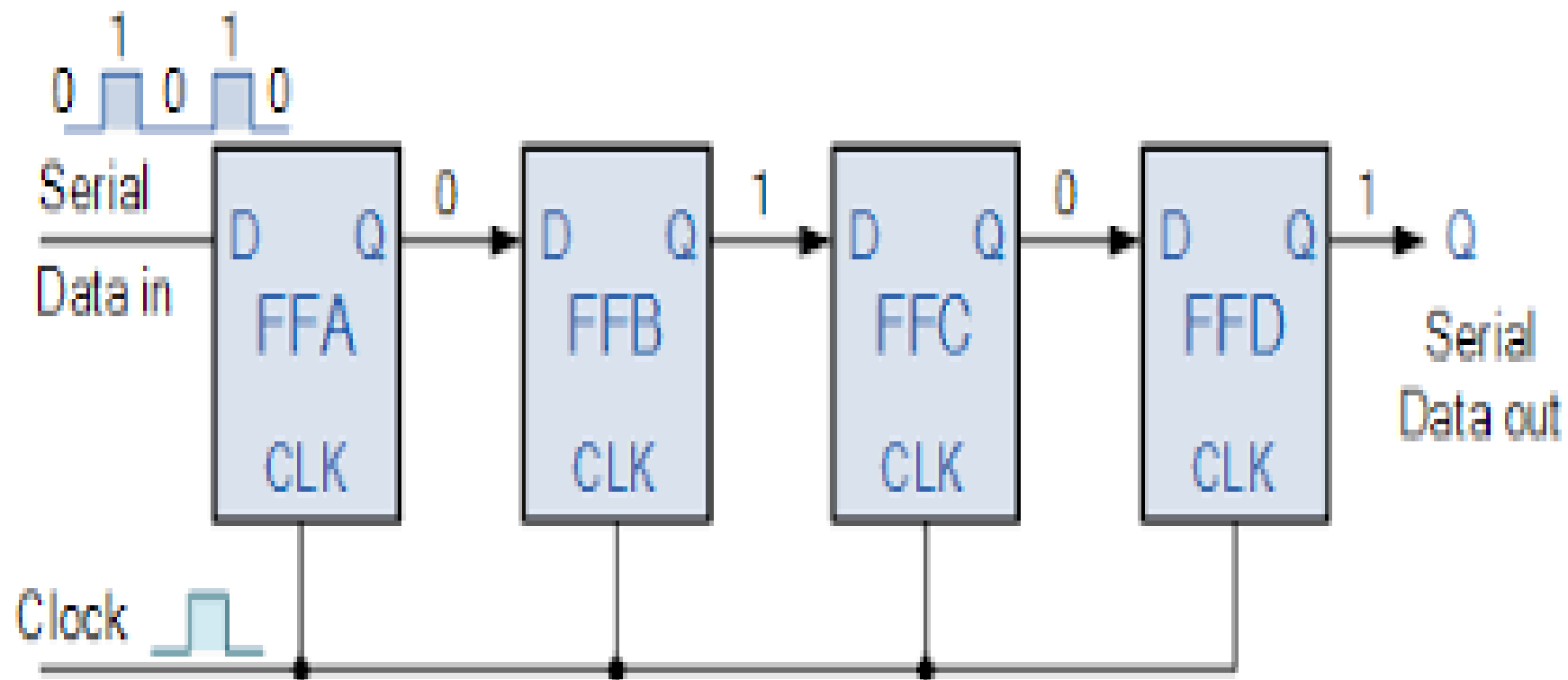
# Register

**Course Teacher: Md. Hamjajul Ashmafee**

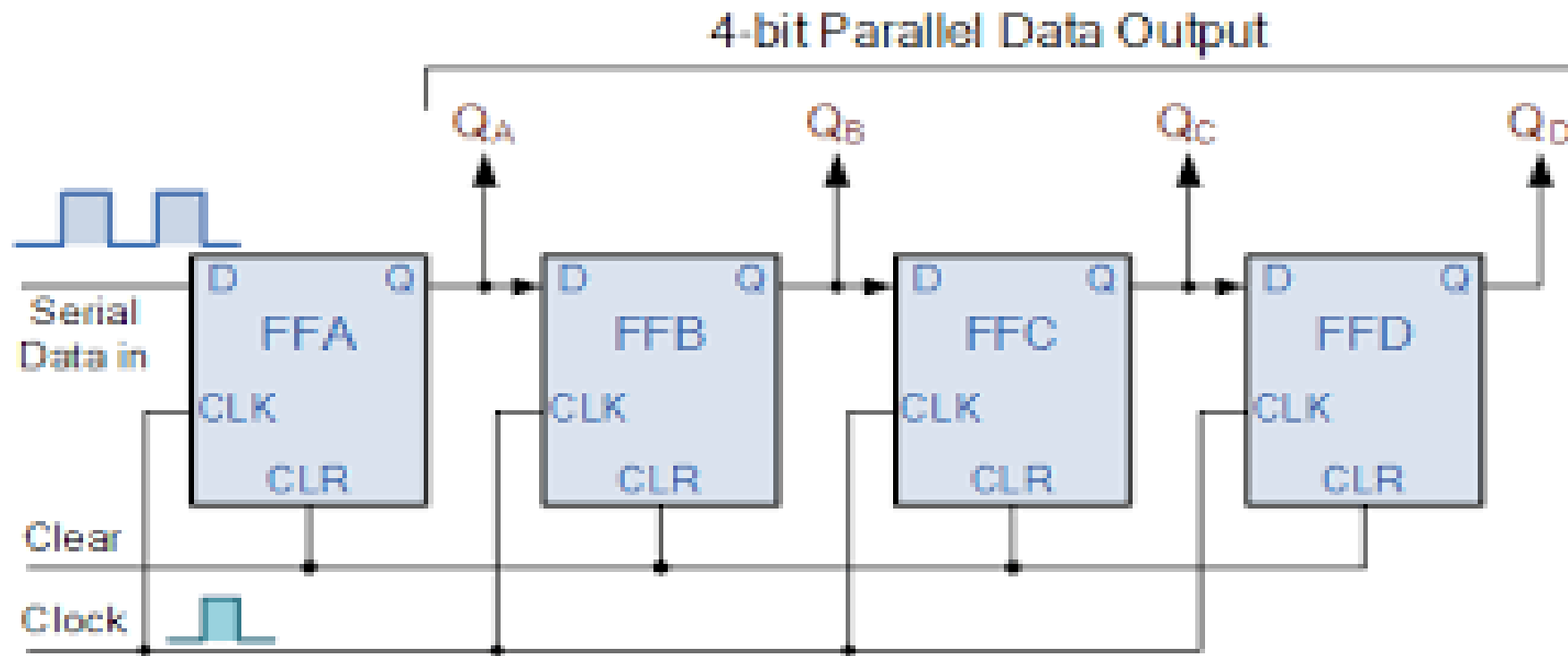
**Lecturer, CSE, IUT**

**Email: [ashmafee@iut-dhaka.edu](mailto:ashmafee@iut-dhaka.edu)**

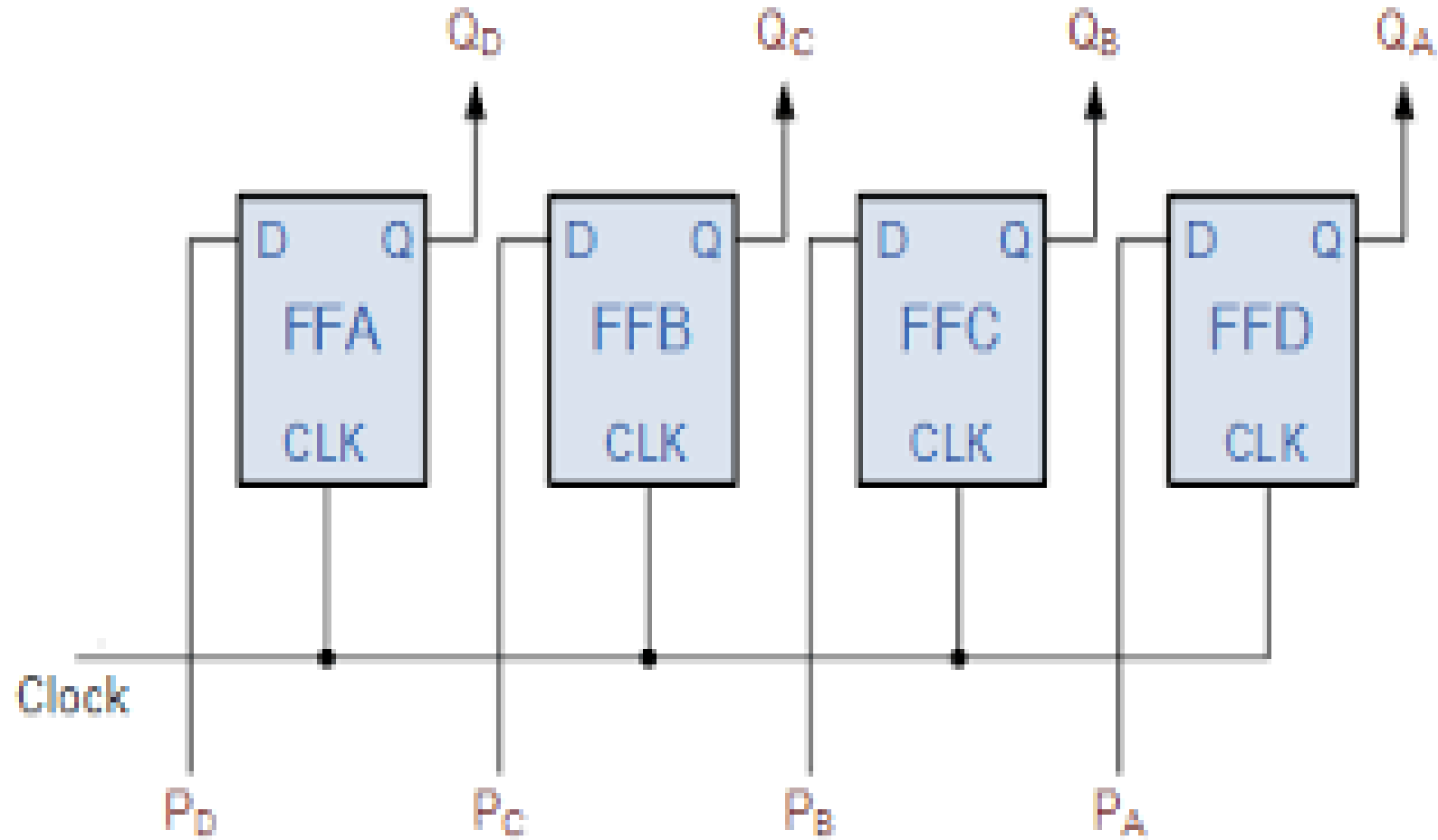
# *SISO*



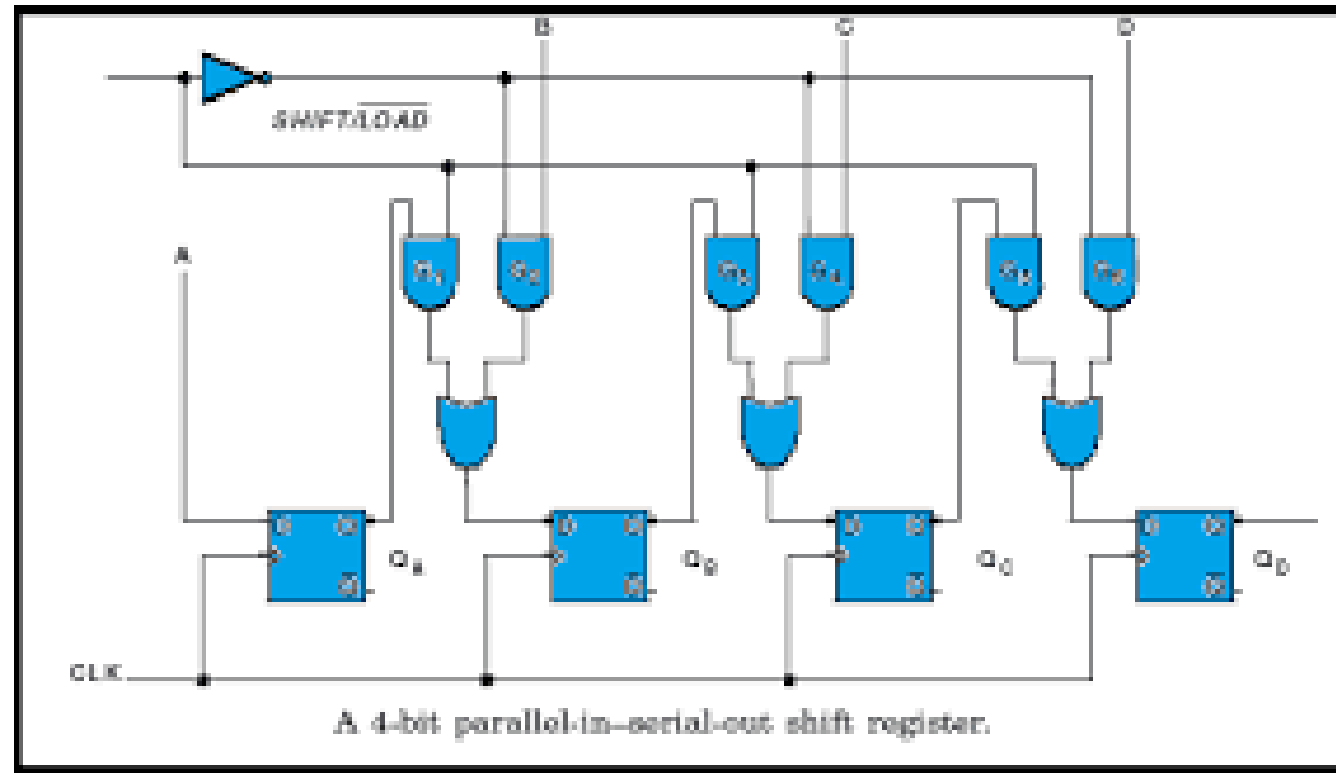
# SIPO



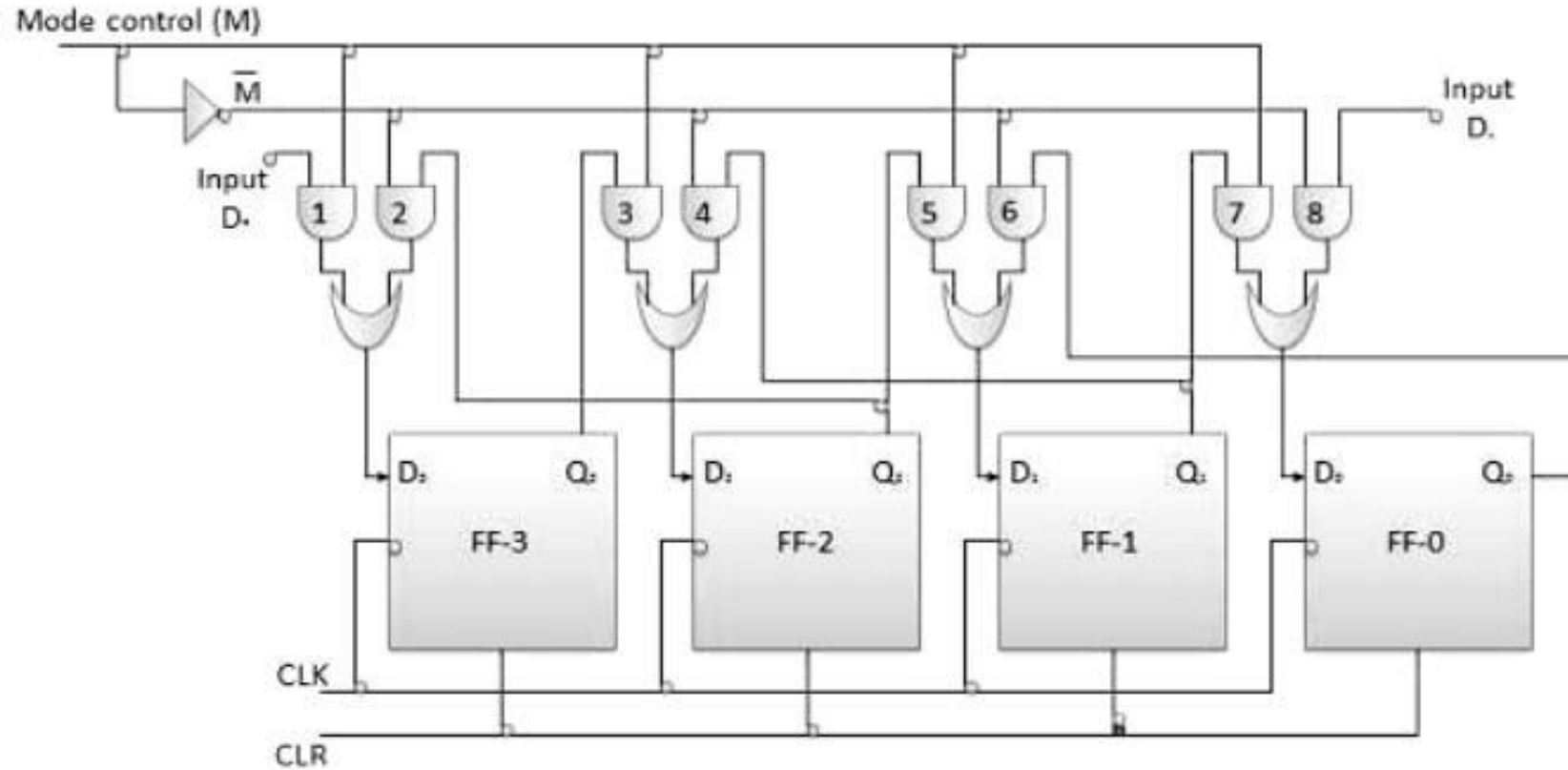
# PIPO



# PISO



# Bidirectional Shift Register



# Universal Shift Register

