

**Quiz # 03**

**Course Title:** Microprocessors and Assembly Language  
**Course ID:** CSE - 4503

**Total Marks:** 15

**Time:** 25 Mins.

**Student Id:** .....

**Obtained Marks:** .....

**Q1.** Define the following terms with appropriate example:

**5**

- i. Clock cycle
- ii. Bus cycle
- iii. Instruction cycle
- iv. Machine cycle
- v. 'T' duration (consider a microprocessor is having a clock speed of 40 MHz)

**Q2.** Draw the details of WRITE Bus timing diagram showing all the necessary/required signals of 8086. You should consider that the write operation will be made in the memory address of 01111H and there is no WAIT state.

**10**