



Apoorva Narode

An independent and self-motivated student, pursuing a Career in Electronics which helped me to explore many ailed areas and gain a rich skillset and develop an interdisciplinary problem solving ability. Every experience no matter how trivial, provides the opportunity to learn something worth. Proficiency in English, Hindi and Kannada.

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📍 Hubli, India

EDUCATION

Bachelor of Engineering

KLE Technological University, Hubli

08/2019 - 04/2023

CGPA-7.96(till 6th sem)

Electronics and Communication

- Digital Circuits
- Computer Networking
- Verification Methodologies
- Communication System
- Digital Design with Verilog

Pre-university Course

KLE Prerana PU College, Hubli

06/2017 - 04/2019

percentage-82%

Schooling

K.J. Somaiya English Medium School,
Sameerwadi

06/2004 - 04/2017

CGPA-9.8

PERSONAL PROJECTS

(1) Design and develop a system to detect the rotten food items based on VOC index and notify the user.
(08/2021 - 11/2021)

- MINI Project

(3) 2 bit multiplier implementation using CMOS
(09/2021 - 11/2021)

- implementation of 2 bit multiplier in CMOS technology using Cadence Genus tool

(4) Parallel Digital VLSI architecture for combined support vector machine (SVM) training and classification
(02/2022 - 04/2022)

- MINOR Project
- classification of diabetic and adiabatic patients using Support vector machine algorithm

(5) Verification of AHB2APB Bridge Protocol using UVM
(07/2022 - 12/2022)

- ABMA (Course Project)
- I have presented this work in **International Conference on VLSI, Signal Processing, Power Electronics, IoT, Communication, and Embedded Systems (VSPICE-2022)** was held on 22nd and 23rd December 2022 at N. M. A. M. Institute of Technology, Nitte, India.
- This work is done under the guidance of Dr. Savroja V. Siddamal and Dr. Suneeta V. Budihal
- I won Best Paper award for my work

(6) Resource allocation in 5G Network
(08/2022 - Present)

- This the research project done under Dr. Suneeta V.B.
- DDPG algorithm is used for resource allocation (networking+ reinforcement learning)

SKILLS

VLSI

DDPG

Digital Desgin

Basic C

Operating System

8051

Leadership

Communication

Networking

Machine learning

reinforcement learning

verilog

UVM

Verification

RTL

ACHIEVEMENTS

Finalist in Hult prize KLE TECH 2021 at regional level.

Proposed a product based solution for people who are suffering from malnutrition

Best Paper Award

for the work "Verification of AHB2APB Bridge Protocol using UVM"

VOLUNTEERING EXPERIENCE

Society for Space Education, Research and Development (SSERD) (07/2021 - 10/2021)

Head of Marketing and Promotion

KLETECH Higher Studies Club (07/2021 - 05/2022)

Project and Resource Management

Electronics Association (04/2022 - Present)

Cultural Secretary

KLETECH Higher Studies Club (01/2007 - Present)

Advisory Member

Suvidha Foundations (09/2022 - 10/2022)

Human Resource Intern

Hult Prize KLETech (11/2022 - Present)

Team Co-Ordinator

CERTIFICATES

Embedded Systems by Teachnook and Wissenaire IIT Bhubaneshwar (09/2022 - 12/2010)

The basic concepts of Embedded systems and their applications. 1) Digital thermometer 2) Design and develop a system to detect rotten food items based on the VOC index and notify the user.

Internship and Job preparation (12/2022)

Top Scorer

35th International Conference on VLSI Design by VLSI 2022 (02/2022 - 03/2022)