Virtual Machine 2.0 Specification

Version 1.0.6

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1 Definitions

Value A value shall be a 32bit number.

Stack The stack is a data structure, from which one may only add a value or may read, and than always remove, the lastly added value.

Pushing pushing refers to the act of adding a new value onto the stack.

Poping poping refers to the act of reading the lastly pushed value from the stack and removing it.

Register A register stores one value at a time. One can push the stored value onto the stack or pop a value from the stack and store the poped value in the register.

Address An address shall be a 32bit number which describes the location of stored data.

Linear Memory linear memory refers to a data structure where one can write arbitrary many values to an arbitrary address²³. One can also push any value stored in the linear memory on to the stack.

State The state of the vm refers to the data which is stored in the $linear\ memory$, registers, $instruction\ pointer$ and stack.

Instruction An instruction shall be callable, and modify the state of the vm, when called.

ByteCode The bytecode is an array of 8bit numbers which refer to instructions or static $values^4$.

Opcode An opcode is a 8bit number⁵, which uniquely defines an instruction.

Instruction Pointer The instruction pointer is a pointer which points into the bytecode.

Float A float refers to single-precision float as defined by the IEEE STD 754-2008 - IEEE STANDARD FOR FLOATING-POINT ARITHMETIC 7

 $^{^{1}}$ We may refer to the value which was added lastly as the $top\ value$.

²Address are numbered starting from 0.

³The linear memorys address's are in no way related to the address's of other data structure.

 $^{^44}$ 8bit values are read and sticked together to build one 32bit number.

 $^{^5\}mathrm{Usually}$ represented in hex.

⁶We say the poped *value* is the *argument* of the instruction poping it.

⁷http://standards.ieee.org/findstds/standard/754-2008.html

2 High Level Function

The vm keeps track of the Instruction Pointer (IP). The IP points into the bytecode starting at the first byte. The vm functions in a loop, executing the instruction which is defined by the opcode, to which the IP is currently pointing. Every instruction may or may not modify the state of the vm, but must always modify the ip. The vm stops executing when the ip points to the instruction 0x11.

3 Technical notes

Every instruction is defined to be one byte in size. The signage and type of a value is interpreted by the instruction to which it is being passed. The VM only exposes the values stored on the stack to the instructions, but internally the vm stores, besides the value, what instruction pushed that value onto the stack. (This is required so the return instruction knows what value was pushed onto the stack by the call instruction.)

4 Input Parameters

Any command line arguments added to the VM executable after the file to execute will be pushed onto the stack in order of appearance; following that the amount of these numbers is pushed onto the stack.

5 Instruction Set

We use "..." to specify a range of numbers. The first and last values are both included in the range.

Opcode	Name	Description
0xa00xa9	readRegister0readRegister9	Pushes the value stored in a register onto the
		stack.
0xb00xb9	setRegister0setRegister9	Writes one argument to the given register.
0xc0	setSize	Takes one argument which sets by how many
		addresses the linear memory gets expanded
		when calling $0xc3$ (alloc) 10 . You can call this
		INSTRUCTION EXACTLY ONES.
0xc1	move	Writes the first argument into linear memory at
		the address given by the second argument.
0xc2	read	Pushes the value, specified by the address given
		as first argument, from linear memory onto the
		stack.
0xc3	alloc	Expands the writeable address range in the
		linear memory by the size that was set with
		the instruction 0xc0 a.k.a. setSize. 11
0x d 0	push	Pushes a value, specified by the next 4 bytes in
		the bytecode, onto the stack.
0xd1	remove	Removes a value from the stack.

⁸E.g. some instructions might interpret a value as n float or as a signed/unsigned integer

⁹e.g. given "vm ./path/to/my/file 1024" 1024 would be considered.

¹⁰ Addresses are 32bit in size, therefore if you were to set this value to 32 you would, every time you call alloc, increase the linear memory by 1024bit (1 Kibibit).

¹¹If you want to write to linear memory you have to call this instruction at least ones; there would be no address to which you cloud write to otherwise.

0xe0	uadd	Adds two arguments and pushes the result onto the stack. (All used values are interpreted to
		be unsigned)
0xe1	sadd	Adds two arguments and pushes the result onto
OVEI	Sadd	the stack. (All used values are interpreted to
		be signed)
0xe2	fadd	Adds two arguments and pushes the result onto
UAGZ	ladd	the stack. (All used values are interpreted to
		be floats)
0xe3	usub	Subtracts two arguments and pushes the
0xe3	usus	result onto the stack. (All used values are
		interpreted to be unsigned)
0xe4	ssub	Subtracts two arguments and pushes the
	SSUD	result onto the stack. (All used values are
0 o F	fsub	interpreted to be signed) Subtracts two arguments and pushes the
0xe5	ISUD	result onto the stack. (All used values are
		•
0		interpreted to be floats)
0xe6	umult	Multiples two arguments and pushes the
		result onto the stack. (All used values are
O 7		interpreted to be unsigned)
0xe7	smult	Multiples two arguments and pushes the
		result onto the stack. (All used values are
2 2		interpreted to be signed)
0xe8	fmult	Multiples two arguments and pushes the
		result onto the stack. (All used values are
		interpreted to be floats)
0xe9	udiv	Divides two arguments and pushes the result ont
		the stack. (All used values are interpreted to
_		be unsigned)
0x ea	sdiv	Divides two arguments and pushes the result ont
		the stack. (All used values are interpreted to
		be signed)
0xeb	fdiv	Divides two arguments and pushes the result ont
		the stack. (All used values are interpreted to
		be floats)
0xec	utof	One argument is transformed to be stored in
		float representation. 12 (Value is interpreted
		be unsigned)
Oxed	stof	One argument is transformed to be stored in
		float representation. 12 (Value is interpreted
		be signed)
0xee	abs	One argument is turned into a positive number.
Oxef	ucmp	Compares two argument; Than pushes a value onto
		the stack, which represents if arg1 <arg2, is<="" or="" td=""></arg2,>
		arg1>arg2, or if arg1 = arg2. 13 (All values ar
		interpreted to be unsigned)
OxfO	scmp	Compares two argument; Than pushes a value onto
		the stack, which represents if arg1 <arg2, is<="" or="" td=""></arg2,>
		arg1>arg2, or if arg1 = arg2. 13 (All values ar
	1	interpreted to be signed)

¹² The instruction will store the number represented in its first argument with as much precision as a float allows. If precision is lost, no further actions are taken.

13 0 represents arg1 = arg2, 1 represents arg1<arg2, 2 represents arg1>arg2.

0xf1	fcmp	Compares two argument; Than pushes a value onto
		the stack, which represents if arg1 <arg2, if<="" or="" td=""></arg2,>
		arg1>arg2, or if arg1 = arg2. 13 (All values are
		interpreted to be floats)
0x01	jmp	Sets the ip to the address given by the first
		argument of this instruction.
0x 02	jless	Functions like the <pre>jmp</pre> instruction if, and only
		if, the last cmp determinant that arg1 <arg2< td=""></arg2<>
		(Arguments of cmp).
0x 03	jgreater	Functions like the <pre>jmp</pre> instruction if, and only
		if, the last cmp determinant that arg1>arg2
		(Arguments of cmp).
0x04	jequal	Functions like the <pre>jmp</pre> instruction if, and only
		if, the last cmp determinant that arg1=arg2
		(Arguments of cmp).
0x 05	jNequal	Functions like the <pre>jmp</pre> instruction if, and only
		if, the last \textit{cmp} determinant that $\texttt{arg1} \neq \texttt{arg2}$
		(Arguments of cmp).
0x 06	call	Sets the ip to the address specified in its
		first argument and pushes the address of this
		instruction in the bytecode onto the stack.
0x07	return	Looks through the stack, top to bottom, and
		jumps to the address which was put there by a
		previous call instruction.
0x10	int	Triggers the interrupt specified by its first
		argument. 14 15
0x11	halt	Stops the vm.
0x12	noop	Iterates the IP by one with no other changes to
		the state of the vm.
0x13	out	Prints one argument to standard out (The Value
		is written in decibel form.).
-	Table 1:	Instruction Sot

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Interrupts 6

 $^{^{14}\}mathrm{See}$ Section 6 $^{15}\mathrm{It}$ is not sure which, if any, interrupts will exist.