2024 Digital IC Design Homework 1

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| NAME | 陳映臻 | | |
| Student ID | P76111783 | | |
| **Functional Simulation Result** | | | |
| **Stage 1 Pass** | | **Stage 2 Pass** | **Stage 3 Pass** |
| **Stage 1** | | | |
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| **Stage 2** | | | |
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| **Stage 3** | | | |
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| **Description of your design** | | | |
| 因為這次作業為組合邏輯電路，因此將三個Stage分成三個區塊撰寫，2個ALU使用Behavioral description，Q comparator使用Data Flow Description的寫法。 | | | |