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Subject: Design

Standard: 6th Tech CSE Division: Section C Roll No: 17032400082

N.D.E.X.

Sr. No.	Name of the Experiment	OBSERVATIONS Date	INFERENCES Page No
1.	To study and verify the truth table of basic gates	18-7-25	1-3
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Experiment 1

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Obj: To study and verify the truth tables of basic gates.

Apparatus Required: Connecting wires, power supply, digital trainer kit, integrated circuit.

AND gate

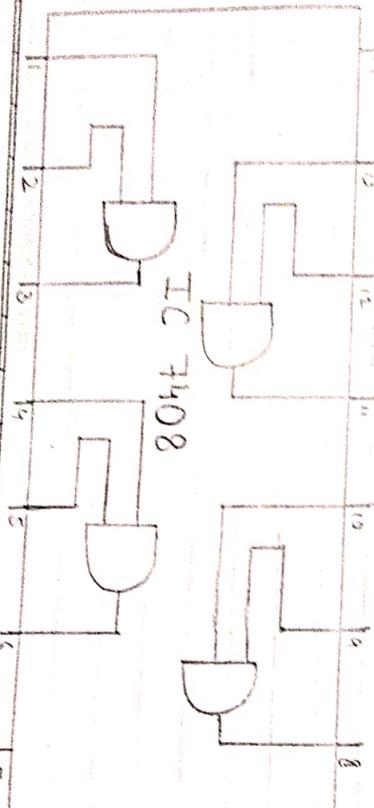
Boolean Expression

$$X = A \cdot B$$

Logic Symbol



Truth Table	
Inputs	Output
0 0	0
0 1	0
1 0	0
1 1	1



Theory: Basic gates - AND, OR and NOT: are electronic circuits that perform logical operations on binary inputs (0 or 1) to produce a binary output. Inputs and outputs exist only at two levels: High(1) and Low(0). A truth table lists all input combinations and their corresponding outputs, showing how the gate responds to each case.

AND Gate (7408)

An AND gate has two or more inputs, but only one output. The output assumes the logic 1 state only when each one of its inputs is at logic 1 state. The output assumes logic 0 state even if one of its input is at logic 0 state. AND gate is also called an 'and' and nothing gate.

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OR gate

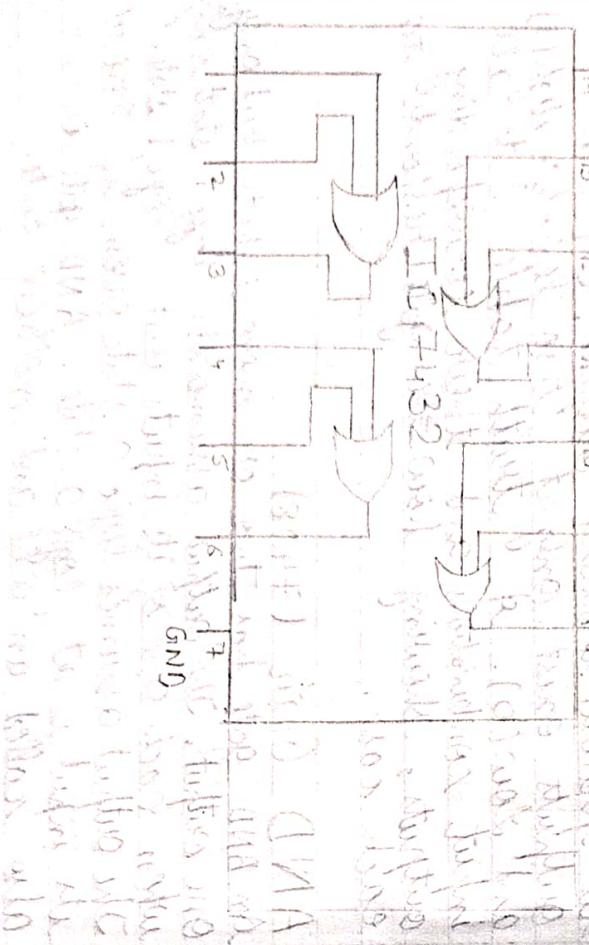
Boolean Expression $X = A + B$

Logic Symbol and Function Truth Table

INPUT	OUTPUT
0 0	0
0 1	1
1 0	1
1 1	1

Inputs

Output



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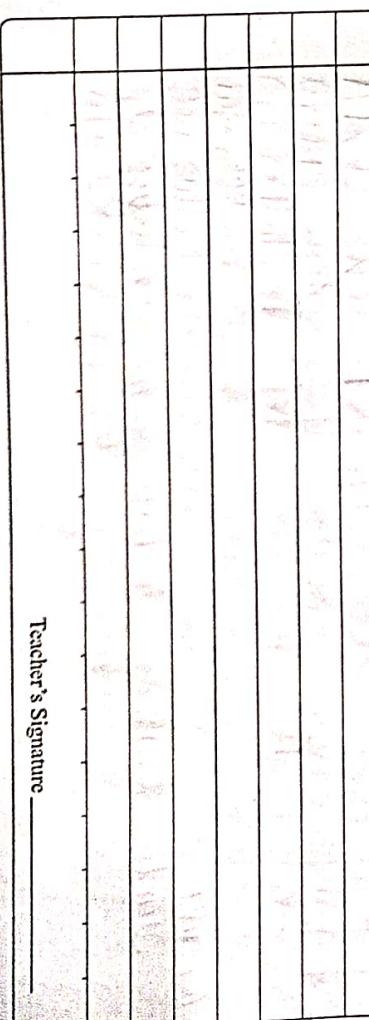
The symbol for AND operator is '•',

OR Gate (7432)

An OR gate may have two or more inputs but only one output. The output assumes the logic 1 state even if one of its input is in logic 0 state. Its output assumes logic 0 state only when each one of its inputs is in logic 0 state. Also known as 'Any or all' gate. The symbol for OR operation is '+'.

NOT Gate (7404)

A NOT gate is also known as inverter, has one input and only one output. It is a device where output is always complement of its input. That is the output of not gate assumes the logic 1 state when input is in logic 0 and vice versa. The symbol for NOT operation is '---' (bar).



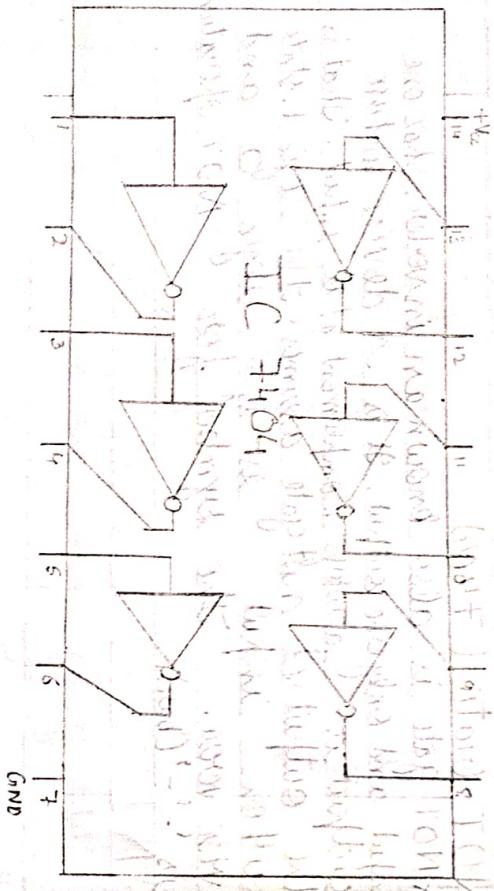
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NOT gate

Boolean Expression $X = A$

$$X = A$$

Logic Symbol & Truth Table



Result:
Truth table of all basic gates are verified.

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Procedure:

1. Set up the breadboard.
2. Test all required ICs.
3. Give various combinations of inputs and check the output.
4. Repeat the procedure for each IC.

Precautions:

1. All the connections must be tight.
2. The connections in the breadboard must be made carefully to prevent bending of IC pins.
3. Connecting wires in the trainer kit must be changed when power supply is off.

Result:

Truth Table of all basic gates are verified.

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Experiment 2

Aim : To Study and verify the Truth tables of universal gates NAND (7400) and NOR (7402).

Apparatus Required : Connecting wires, Power supply, Digital trainer kit.

IC IC 7400 - 10

IC 7402 - 10

NAND Gate
Boolean expression would be $X = \overline{A \cdot B}$ or $\overline{A} + \overline{B}$

Logic Symbol

Truth table

Inputs Out

0 0 1

0 1 0

1 0 0

1 1 0

Inputs Out

0 0 1

0 1 0

1 0 0

1 1 0

Inputs Out

0 0 1

0 1 0

1 0 0

1 1 0

Inputs Out

0 0 1

0 1 0

1 0 0

1 1 0

Inputs Out

0 0 1

0 1 0

1 0 0

1 1 0

Inputs Out

0 0 1

0 1 0

1 0 0

1 1 0

Inputs Out

0 0 1

0 1 0

1 0 0

1 1 0

NAND Gate (7400)

NAND gate is universal gate. It can perform all the basic logic function. NAND means NOR AND that is, one output is NOTed, so NAND gate is the combination of AND gate and a NOT gate. The output is logic 0 levels, when each of its inputs assumes a logic 1 level. For any other

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Result: By comparing the output waveforms with the given truth tables, it is found that the truth tables of all universal gates are verified.

Conclusion: All universal gates can be formed by using the basic gates like AND, OR, NOT and EXOR.

Precautions: 1. All connections must be tight, otherwise there may be short circuit or open circuit. 2. While using digital trainer kit, make sure that connecting wires are attached tightly and properly.

3. The pins of breadboard should be handled carefully without damaging the pins. 4. Connections should be made when the circuit is OFF.

Result: The truth tables of all universal gates are verified.

Conclusion: All universal gates can be formed by using the basic gates like AND, OR, NOT and EXOR.

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Experiment 3

Aim : To Study the formation of basic universal gates.

Apparatus Required: Connecting wires, Power supply, Digital Trainer kit, ICs.

Work on Universal gates with old ICs.

Theory :

Universal gates :-
The Nand and Nor gates are called as universal gates, because it is possible to implement any Boolean expression with the help of only Nand or only Nor gates.

Hence a user can build any combinational circuit with the help of only Nand gates

or only Nor gates.

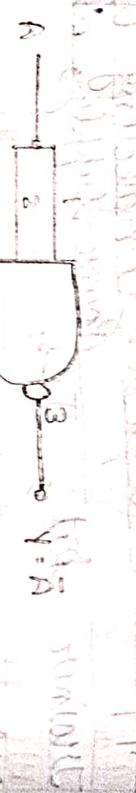
The Nand or Nor gates are universal gates. Because it is possible to implement any Boolean expression with the help of only Nand or only Nor gate.

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NOT using NAND gate

$$Y = \overline{A \cdot B}$$

Given $Y = \overline{A \cdot B} \therefore Y = \overline{A} + \overline{B}$
 But $\overline{A} \cdot \overline{B} = A + B$ (By De Morgan's Law)



NOT using NOR gate

The Boolean expression for NOR gate is
 $X = \overline{A + B}$

This is a great advantage because a user will have to make a stack of only Nand or Nor gates.

All gates using Nand Gates :-

- Not using Nand :-
 The Boolean expression for NOT gate is $A = \overline{A}$
 Figure shows the realization of a NOT gate using a two input Nand gate. As both inputs are connected together, we can write input $A = B = A$
 So output is given as

$$Y = \overline{A \cdot A} \therefore A = B$$

But $A \cdot A = A \therefore$ By KMO Law

$$Y = \overline{A}$$

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AND using NAND gate

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$$G \oplus R = A$$

As AND gate requires double inversion



This equation can be realized using only NAND gate as shown.

$$Y = A \cdot B$$

2. AND using NAND :-
The Boolean expression for an AND gate is $Y = A \cdot B$. Taking double inversion $Y = \overline{\overline{A} \cdot \overline{B}}$. But $\overline{\overline{A}} = A$

OR using NAND gate

OR gate is $\overline{A + B}$. Double inversion gives $A + B$.

Double inversion is De Morgan's theorem from shop class teacher Mr. A+B = $\overline{\overline{A} \cdot \overline{B}}$

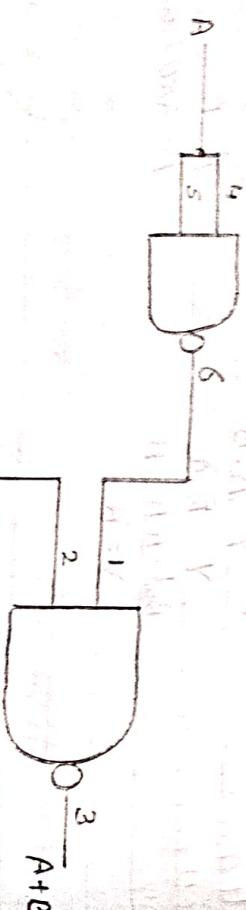
$$Y = \overline{\overline{A} \cdot \overline{B}}$$

But by De-Morgan's theorem

$$\overline{A + B} = \overline{\overline{A} \cdot \overline{B}}$$

3. OR using NAND :-
The Boolean expression for an OR gate is $Y = A + B$. Taking double inversion $Y = \overline{\overline{A} + \overline{B}}$

$$Y = \overline{\overline{A} + \overline{B}}$$



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NOT using NOR gate

$$S.I.A. Y = \overline{A+B}$$

$\therefore A = B$ \therefore By OR law

$$\text{Cud } A+A = A \text{ & in } Y = \overline{A}$$

$\therefore A = \overline{A}$



This expression can be realized using Only ~~NOR~~ NOR gate as shown.

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All gates using NOR gate:-

1. NOT using NOR:-

The Boolean expression for NOT gate is $A = \overline{A}$.
Figure shows Realization of a NOT gate using a Two input NOR gate. As both inputs are connected together we can write input $A = B = A$.
So output is given as

$$Y = \overline{A+B}$$

$$Y = \overline{A+\overline{A}}$$

$$Y = \overline{\overline{A}}$$

$$\therefore A = B$$

$$\therefore \overline{\overline{A}} = \overline{A}$$

$$\therefore A = \overline{A}$$

OR Using NOR gate

2. OR using NOR:-

The Boolean expression for OR gate is $Y = A+B$.
Taking double inversion

$$Y = \overline{\overline{A+B}}$$

$$\text{But } \overline{\overline{A}} = A$$

$$\text{And } \overline{B} = \overline{B}$$

$$\therefore Y = A + \overline{B}$$

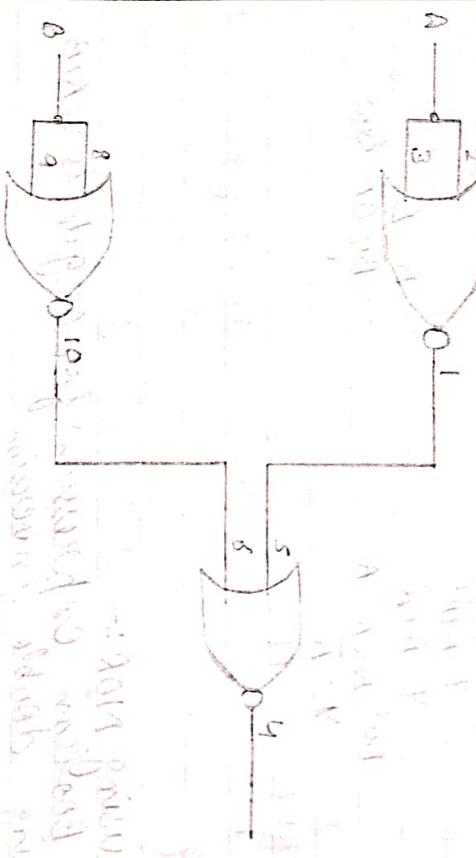


AND using NOR gate

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But by De Morgan's theorem $\neg p \vee q$ is equivalent to $p \rightarrow q$.

THE MEGABR A +



Result
some goals have been successfully realized
from my University's part. It was, indeed, an

classic goals have been successfully realized
from the University's gates. No more fitting

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3. AND Using NOR :-
The Boolean expression for an AND gate is $Y = A \cdot B$

Solving double inversion

Procedure:

1. Place the IC on breadboard.
2. Wire inputs A and B from DIP switches to chosen gate inputs.
3. Connect each gate output Y to an LED.
4. For each gate, apply input combinations and observe outputs.

Repeat for both NAND-Only and NOR-Only

1. Keep wiring short and meat to avoid
loose contacts.
2. Power OFF while inserting / removing ICs.
3. Use current - limiting resistors with LEDs

Result : Basic gates have been successfully realized from universal gates.