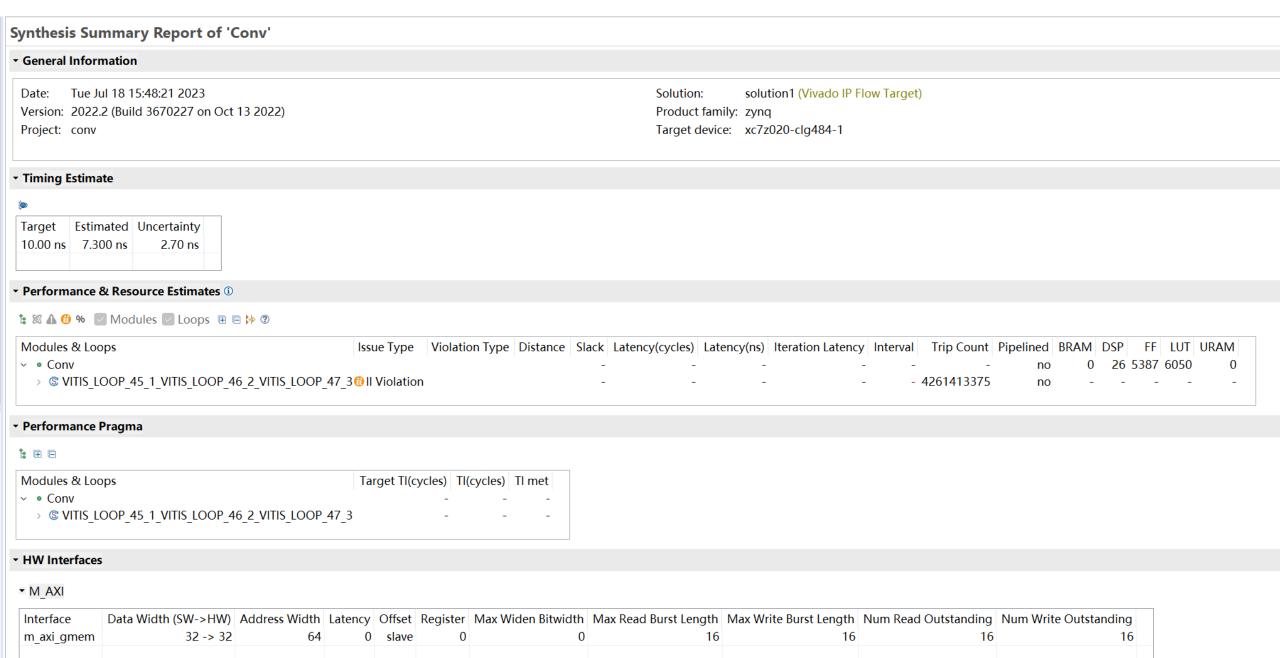
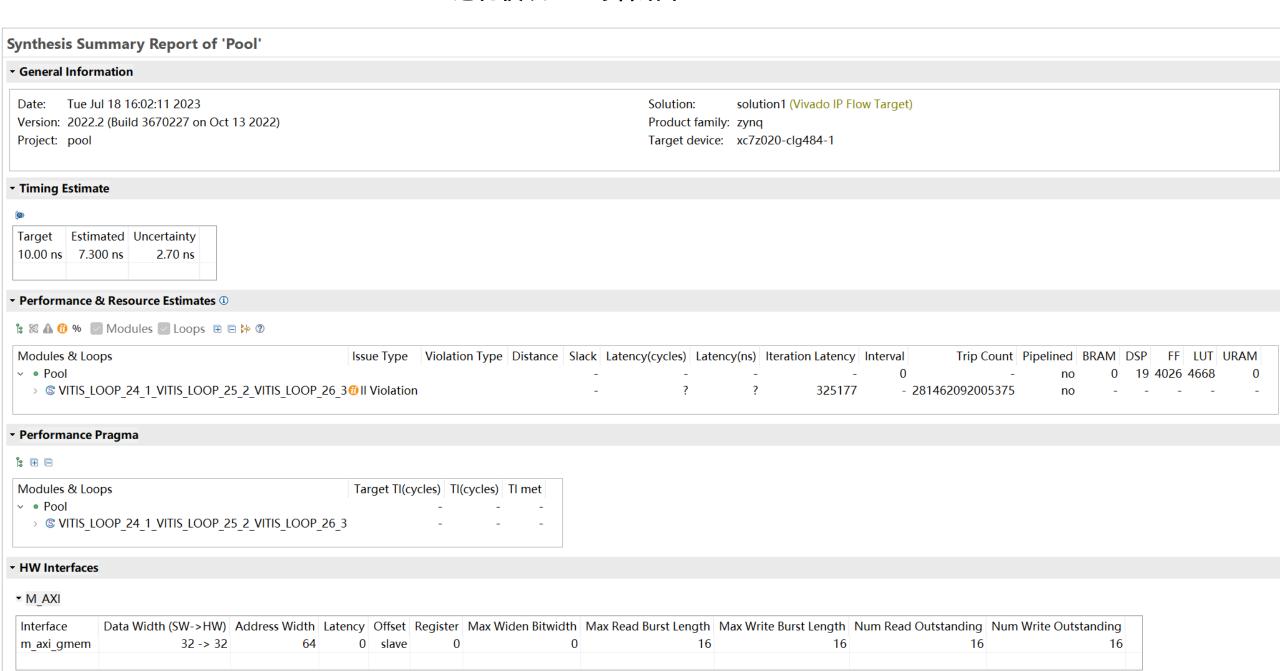
卷积模块HLS综合结果



池化模块HLS综合结果



Vivado Block Design

