EMBEDDED SOFT & HARD DEV **ELECTRONICS** QA & SDET

More details her https://aquarust.github.io

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ABOUT ME

44 years old.

Geek and passionate about technologies since my childhood. My credo is to develop and upgrade my skills every day.

"A day without learning and have fun is a day lost".

I am confident in my ability to adapt to any new situation.

Team player Open minded Autonomous Communicative,

LANGUAGES

- French Native
- English Fluent

HOBBIES

- Piano, Guitar, Ocarina, Harmonica
- Fencing, Lighsaber
- Astronomy
- Discover new techs

EDUCATION

Master - Université Paris 11

Electronics & Signal processing

Bachelor

Embedded Systems & Radio comunications

SKILLS

- Specialist in real time / critical embedded software
- Specialist in SDET & QA (CI/CD Automation Tests)
- Languages (C/C++ Asm Bash Python & RUST in progress)
- MCU (TI ST Microchip Atmel Arduino DSP)

WORK EXPERIENCE

SDET/QA Luos -

SINCE 2020

Responsible of the Luos QA process and of the end to end testing strategy.

- Automate CI/CD pipeline with Github Actions : compilation, test covering, unit tests, automatic tests on a hardware benchmark, logs saving.
- Verify the process is respected, upload the release to the registry & update the lib changelog. Assist the developpers for test quality & unit testing.
- Specify, develop and build a testing benchmark with several MCUs (Arduinos, STM32, ...). Script to auto flash the MCUs with various scenarii binaries, auto generate the network. Develop a QA PC docker with nginx to and Rest API to easily access to the hardware from anywhere.

Thales -Software & SDET/QA

2010 - 2020

- Build the CI process for a whole Navy secured communication system: test of the REST API with Selenium, Robot Framework bash & python. BDD process. Auto generation of test documentation and results.
- QA for satellite communication systems: functionnal, performance, endurance tests. Automatic tools development. Test with Python and groovy with **Jenkins** environment. Test & Integration of the hardware in the Rafale airplane with the system team.
- OFDM communication protocol. Software development C/C++ on a RTOS (ucOS II). Validation in real environment.
- Drivers devloppement C/C++ & **Assembly** and unit tests on a **DSP C6x** for a satellite system. Responsible of the communication protocol between a DSP / FPGA & GPP.
- Team leader of the vocoder software embedded team for a HF product, and test and development (C/C++/Pyhon) on a DSP C54.
- Software development for a communication satellite in C with a DSP C54. Test the system and integration with the FPGA.

Alyotech - Software & Electronics

2006 - 2010

- Analogic & Digital electronic board conception.
- Automatic open/close doors system for Paris Metro: high critical security Firmware on a TI MCU in C and design of the analogic system.
- FPGA conception & VHDL for multibus boards (PCI, serials, USB, CAN, Ethernet ...). Design of the boards with Orcad.
- Surgical assistance: RT software (C & assembly) for ATmega & MSP-430.