**Test name:** access\_nocache\_noshare

**Address increment: 0x8**

PASS

**Test name:** dummy

Nothing to do

**Test name:** read\_cache\_noshare

**Test will always fail if the length of data array is not equal to the number of cores in the system.**

**Address increment: 0x10**

**Test name:** read\_cache\_share

**Test will always fail if the length of data array is not equal to the number of cores in the system.**

**Address increment: 0x10**

AC\_SNOOP values: 0111

**Test name:** read\_cache\_share\_overlap

AC\_SNOOP values: 0111

PASS

**Test name:** read\_modify\_cache

AC\_SNOOP values: 0111

PASS

**Test name:** read\_modify\_cache\_share

AC\_SNOOP values: 0111

PASS

**Test name:** read\_modify\_cache\_share\_intense

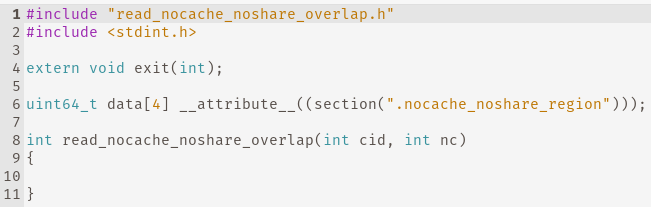
AC\_SNOOP values: 0111

**Only data[0] and data[1] are handled differently, that is ... for all the subsequent addresses only ... is being executed.**

**Test name:** read\_nocache\_noshare

**Test will always fail if the length of data array is not equal to the number of cores in the system.**

**Test name:** read\_nocache\_noshare\_overlap



???

**Test name:** writeback\_evict

AC\_SNOOP values: 0111

**There is still a problem, higher addresses are not being written back, only read**

**Only data[0] is handled differently, that is ... for all the subsequent addresses only ... is being executed.**

**Test name:** writeback\_readshared\_flush

AC\_SNOOP values: 0111

PASS

**Test name:** writeback\_readunique\_flush

AC\_SNOOP values: 0111

PASS

**Test name:** writeback\_readunique\_modify

AC\_SNOOP values: 0111

**There is still a problem, higher addresses are not being written back, only read**

**Only data[0] is handled differently, that is ... for all the subsequent addresses only ... is being executed.**

**Test name:** write\_cache\_noshare

PASS

**Test name:** write\_cache\_share

AC\_SNOOP values: 0111

PASS

**Test name:** write\_cache\_share\_overlap

AC\_SNOOP values: 0111

PASS