

ch.innovazion.arionide.lang.avr.branch.
BranchIfInterruptsDisabled.isConditionMet



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graph LR; A[ch.innovazion.arionide.lang.avr.branch.BranchIfInterruptsDisabled.isConditionMet] --> B[ch.innovazion.arionide.lang.peripherals.SRAM.get]
```

ch.innovazion.arionide.lang.peripherals.
SRAM.get