



FACULTY OF INFORMATION TECHNOLOGY AND ELECTRICAL ENGINEERING
DEGREE PROGRAMME IN ELECTRONICS (MASTER'S)

Course Name: Radio Engineering 1

Homework #4

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Theory.

As, $S_{12}=0$, this mean the transistor is unilateral, which also mean that there is no reverse transmission from output to input.

For unilateral device, maximum gain occurs when the transistor is matched at both ports. The required source & load reflection co-efficients are:

$$F_1 = S_{11}^* = 0.40 \angle 103^\circ$$

$$F_2 = S_{22}^* = 0.65 \angle 72^\circ$$

Now, we need to use lossless components and only inductors and capacitors to transform the 50Ω source into an impedance that has F_2 (reflection co-efficient) and 50Ω load into an impedance that has F_1 as seen by the transistor.

If we follow this, we will get maximum power transfer ~~and~~ which will lead to maximum transducer gain.

From Pozar (12.42), the maximum transducer gain for an unilateral device is,

$$G_{T\max} = \frac{1S_{21}|^2}{(1-S_{11})^2(1-S_{22})^2}$$

$$= \frac{0.4^2}{(1-0.4)^2(1-0.65)^2}$$

$$= \frac{0.16}{0.76 \times 0.58} = 26.3$$

Into dB value = 14.2 dB

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Maximum transducer gain is 14.2 dB

Now, LC matching network design:-

$$Z_S = Z_0 \frac{1 + \Gamma_S}{1 - \Gamma_S} \quad \& \quad Z_L = Z_0 \frac{1 + \Gamma_L}{1 - \Gamma_L}$$

$$\text{Here, } \Gamma_s = 0.40(\cos 103 + j \sin 103) \quad \left| \begin{array}{l} \Gamma_s = 0.65 (\cos 72 + j \sin 72) \\ \qquad \qquad \qquad = 0.2 + 0.62j \end{array} \right.$$

$$\begin{aligned} \therefore Z_S &= 50 \left(\frac{1 - 0.11 + j 0.477}{1 + 0.11 - j 0.477} \right) \\ &= 50 \times \frac{0.80 + j 0.477}{1.11 - j 0.477} \quad | \quad Z_L = Z_0 \frac{1 + T_L}{1 - T_L} \\ &= 50 \cdot \frac{1 + 0.2 + j 0.625}{1 - 0.2 - j 0.625} \\ &\approx 28.03 + j 60.5 \end{aligned}$$

$$= 26 + j32.7 \Omega$$

Given, $f = 2.8 \text{ GHz}$

As, the real part transistor impedance $\angle 50^\circ$, we use L-matching topology.

so, we have same L-transform and to transform $\tau_0 = 26.0 +$

$$Z_0 = 50 + j0 \Omega \text{ need to transform CA } Z = 1+j0 \text{ and } Z_A = 0.52 + j0.55$$

Normalize value $z = 1 + j0$, what I locate the point

Now, in the Smith chart I locate the point

$$Z_A \text{ on the Smith chart and } A = (0.76 - j0.94)$$

admittance of TA - 1. Beside, for shunt capacitor adding, I move downward until I find admittance circle.

Beside, for shunt ω until I find following the admittance circle the unit circle $y_B = (0.70 + j0.44)$.

the unit circle $\vec{OB} = (0.707, 0.707)$

Z_b at this point is $(0.08 - j0.5)$. To cancel the imaginary part I add an inductor in series.

$$x_L = 0.57 \times 50 = 28.3 \Omega$$

Subsequence change,
 $\Delta b = 0.44 - (-0.04)$
= 1.38.

$$X_C = -\frac{1}{\Delta b} = -\frac{1}{1.38} = -0.724$$
$$X_C = 50 \times (-0.724) = -36.23$$

$$C = \frac{1}{w|X_C|} = \frac{1}{2\pi \times 2.8 \times 10^6 \times 36.23}$$
$$= 1.56 \text{ pF}$$

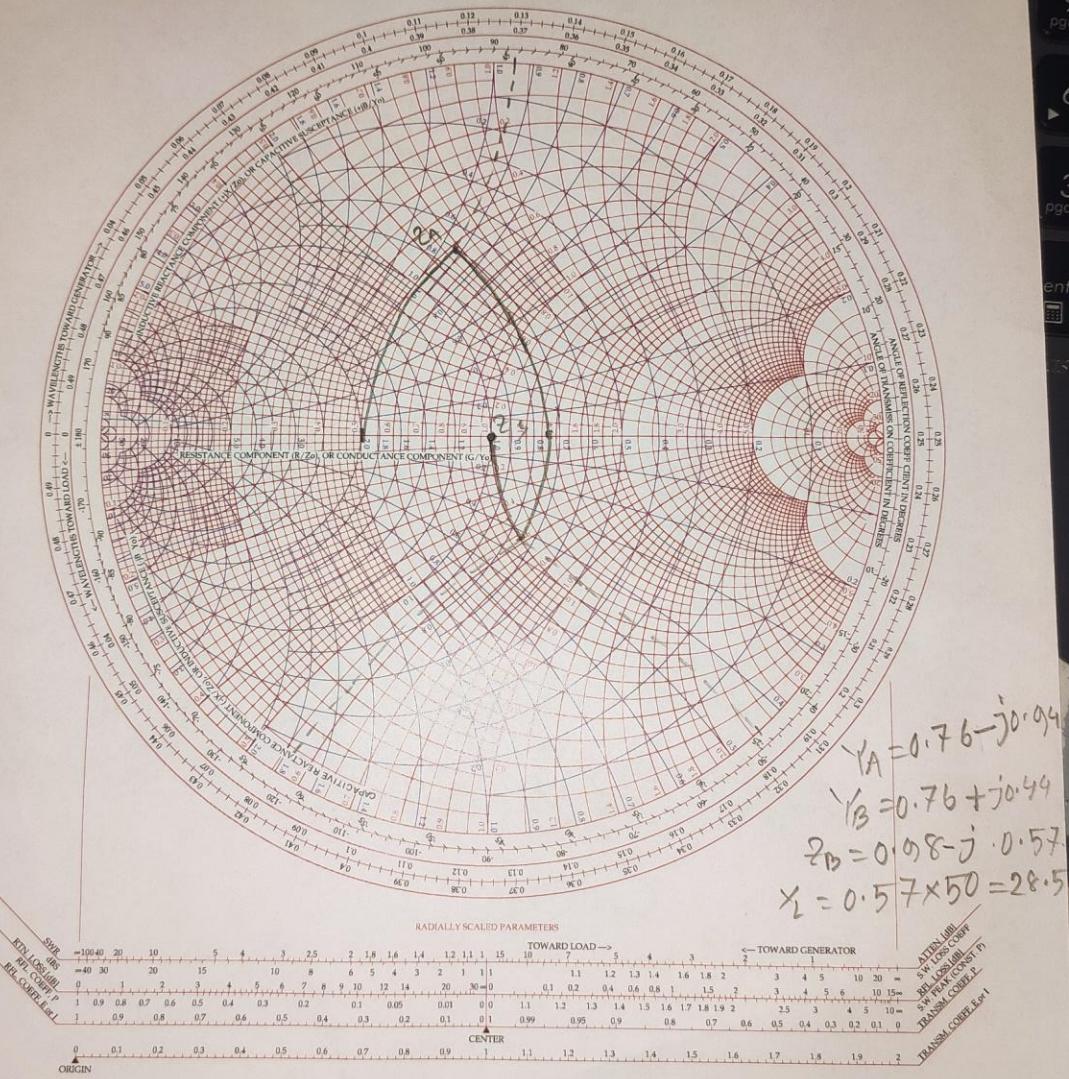
$$L = \frac{X_L}{w} = \frac{28.3}{2\pi f} = 1.6 \text{ mH}$$



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NAME	Anafat Miah	TITLE	Input	DWG. NO.	
SMITH CHART FORM ZY-01-N			Microwave Circuit Design - EE523 - Fall 1999		
DATE					

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On the load side, $Z_L = 28.03 + j60.5$

So, the Normalized value is, $Z_L = 0.58 + j1.21$.

Now, located point Z_L on the Smith chart, then move with the admittance circle to add the shunt capacitor.

$$Y_L = 0.3 - j0.64$$

$$Y_1 = 0.3 + j0.44$$

$$\Delta b = \frac{0.44 - (-0.64)}{1.08}$$

$$\therefore Z_1 = 1.05 - j1.55$$

So, value for $X_L = +1.55$ to move the Z_1 to $Z_L = 1 + j0.5$.

$$= 1.55 \times 50 = 77.5 \Omega$$

$$X_C = -\frac{1}{4b} = \frac{-1}{1.08} = -0.93$$

$$= 46.5 \Omega$$

$$X_C = 50 \times (-0.93) = -46.5 \Omega$$

$$\therefore L = \frac{77.5}{2\pi f} = 4.4 \text{ mH}$$

$$C = \frac{1}{\omega |X_C|} = \frac{1}{2\pi f \times 46.5} = 1.22 \mu F$$

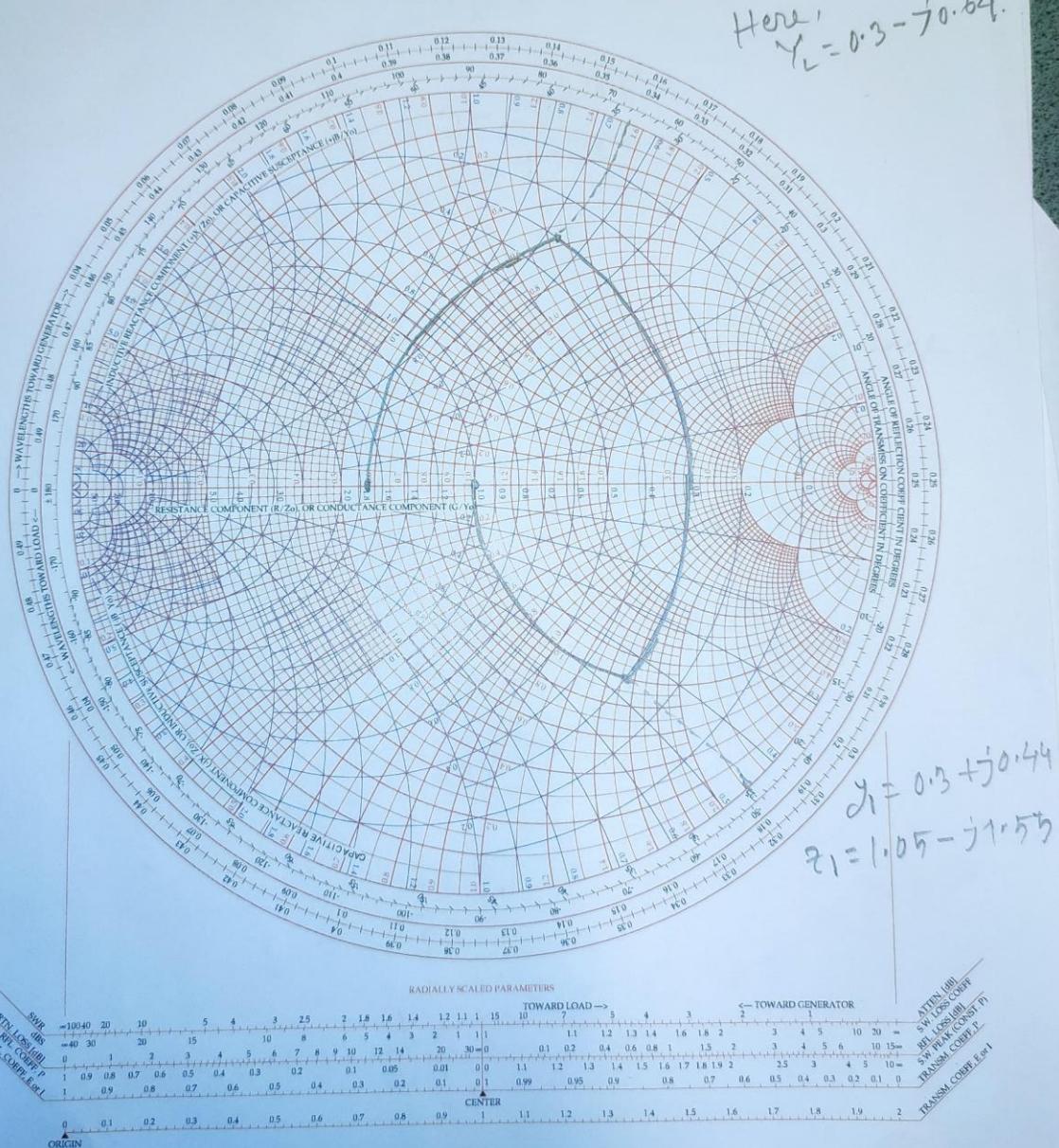


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NAME	Azzalata Mu'ah	TITLE	output
SMITH CHART FORM ZY-01-N		DWG. NO.	
		DATE	

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NORMALIZED IMPEDANCE AND ADMITTANCE COORDINATES



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