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B

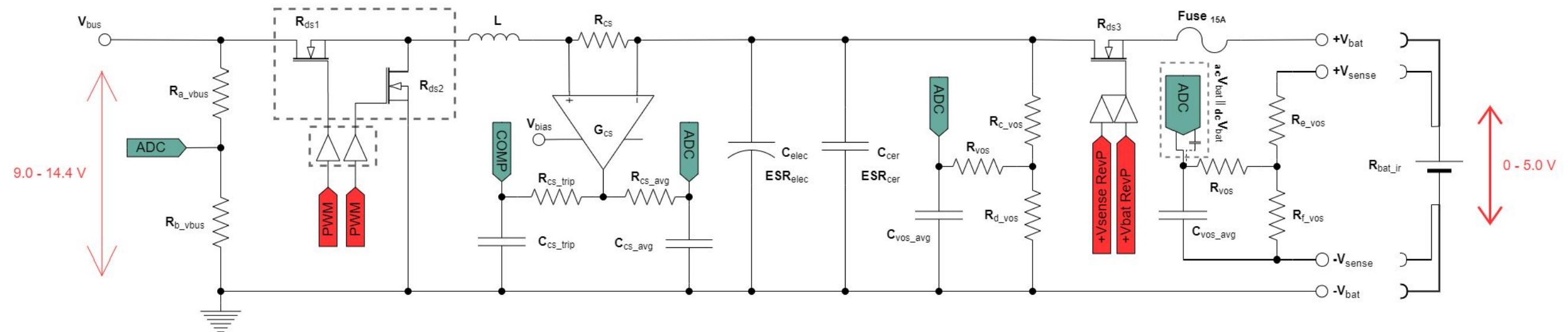
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D

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Orderable: TID #:	Designed for: Public Release Project Title: 10A Digital battery test system	Mod. Date: 4/01/2021
Number: XTIDA-010086 SVN Rev:	Sheet Title: Block diagram Assembly Variant: 001	Sheet: 1 of 8
Drawn By: Jeff Xie Engineer: Jeff Xie / Shauru Anand / U	File: XTIDA-010086_CoverSheet.SchDoc	Size: B
	jeff_xie@atmel.ti.com www.ti.com/support	



TEXAS INSTRUMENTS

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A

A

B

B

MAIN SHEET
XTIDA-010086_MAIN SHEET.SchDoc



CoverSheet
XTIDA-010086_CoverSheet.SchDoc



Hardware
XTIDA-010086_Hardware.SchDoc



C

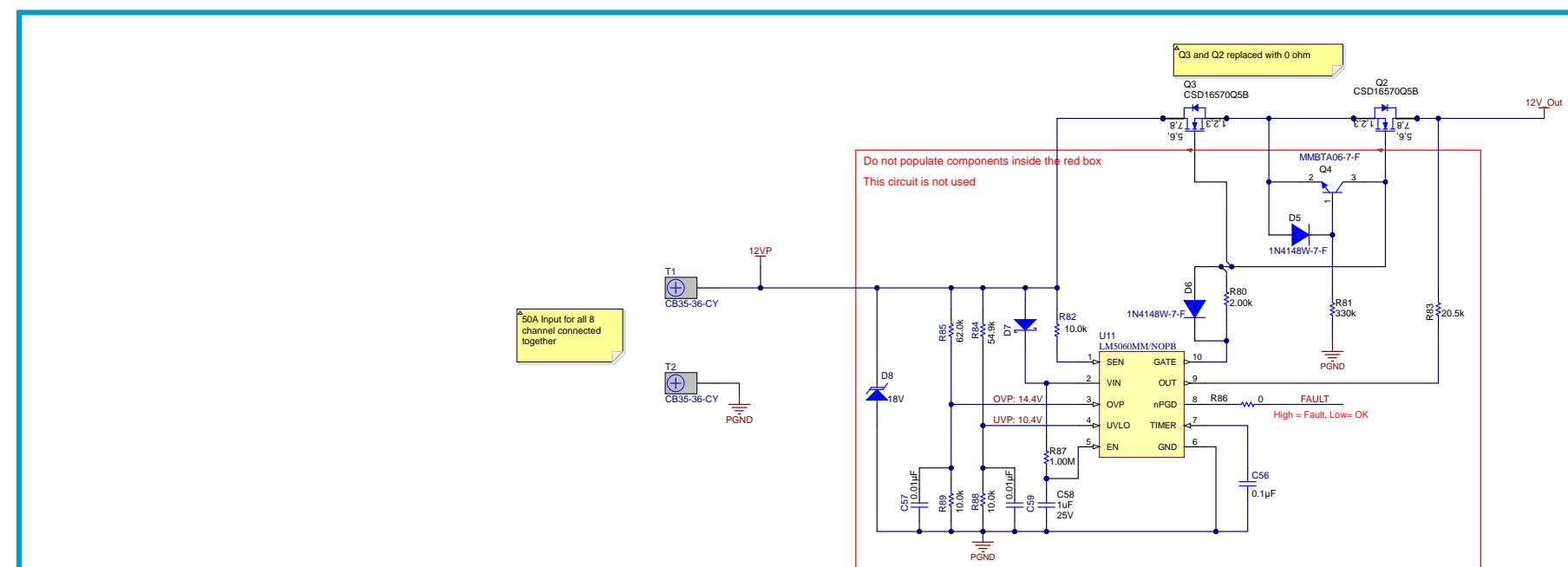
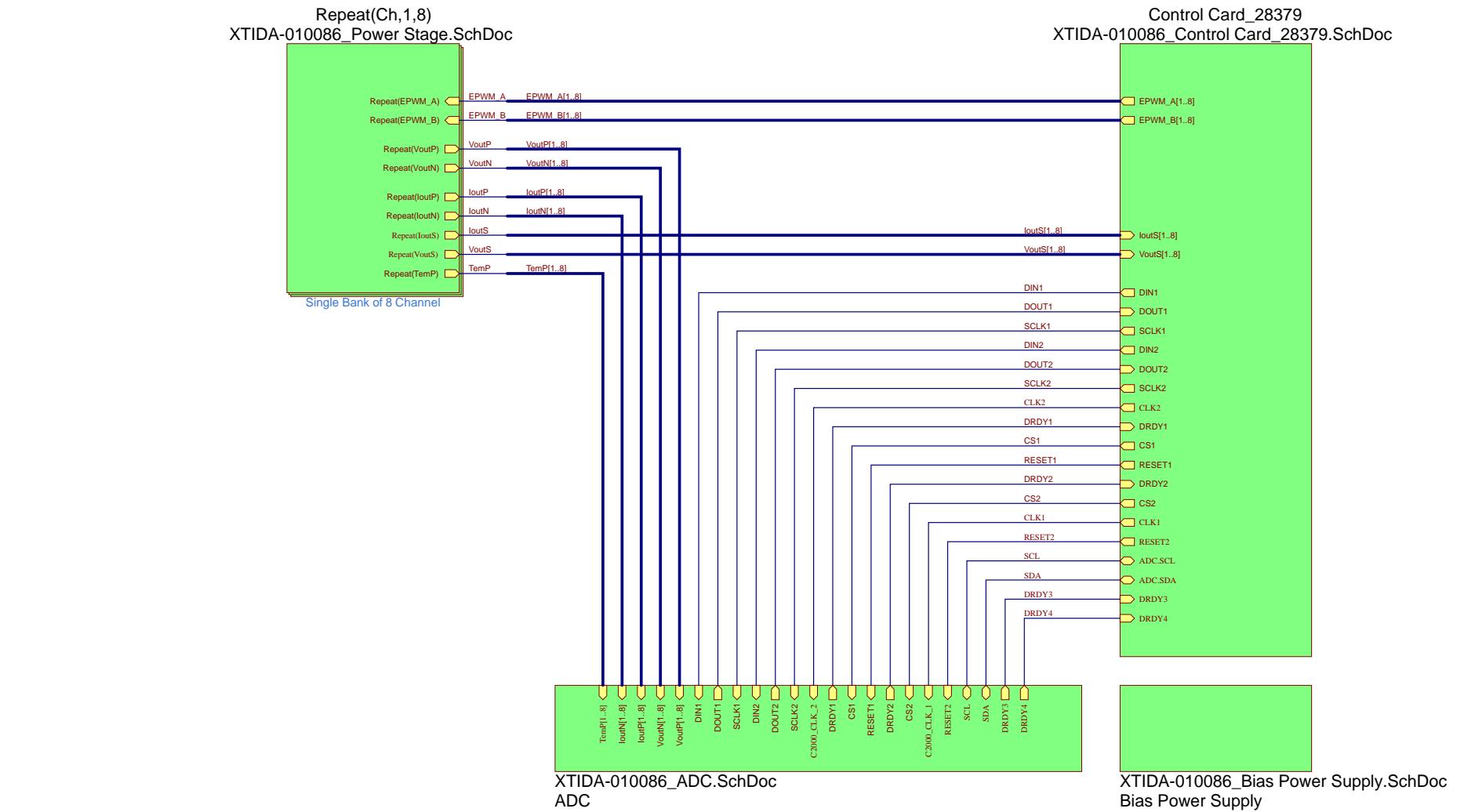
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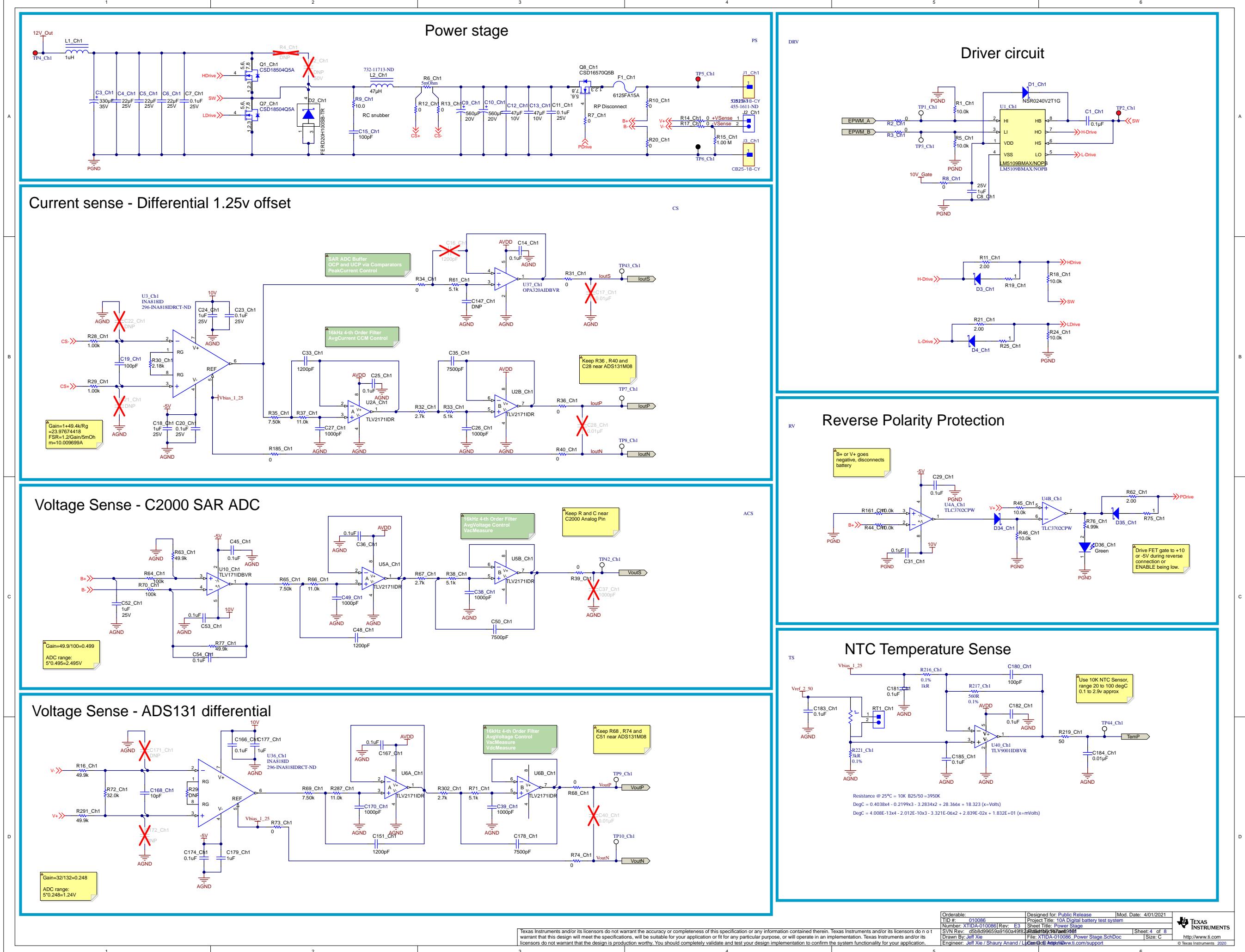
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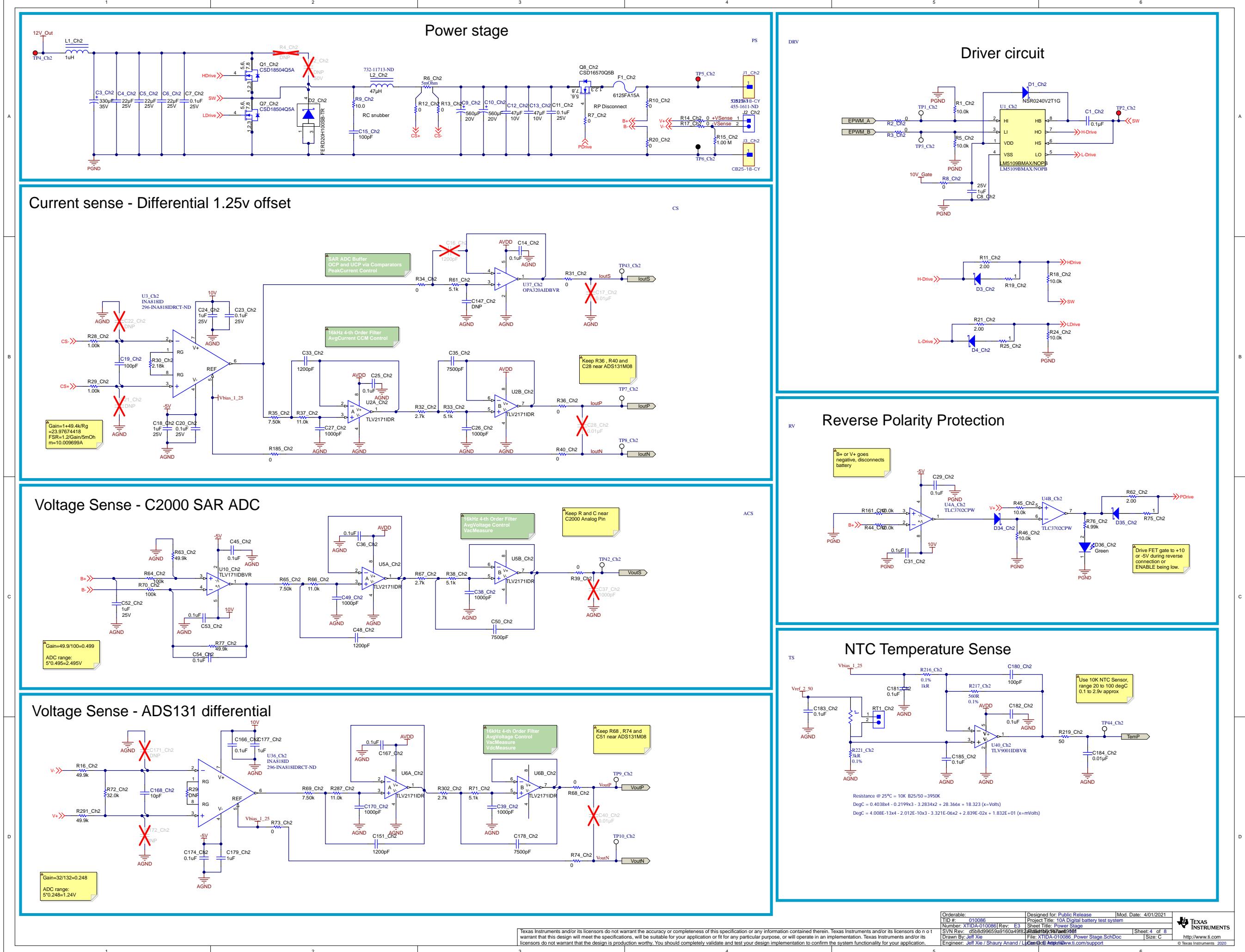
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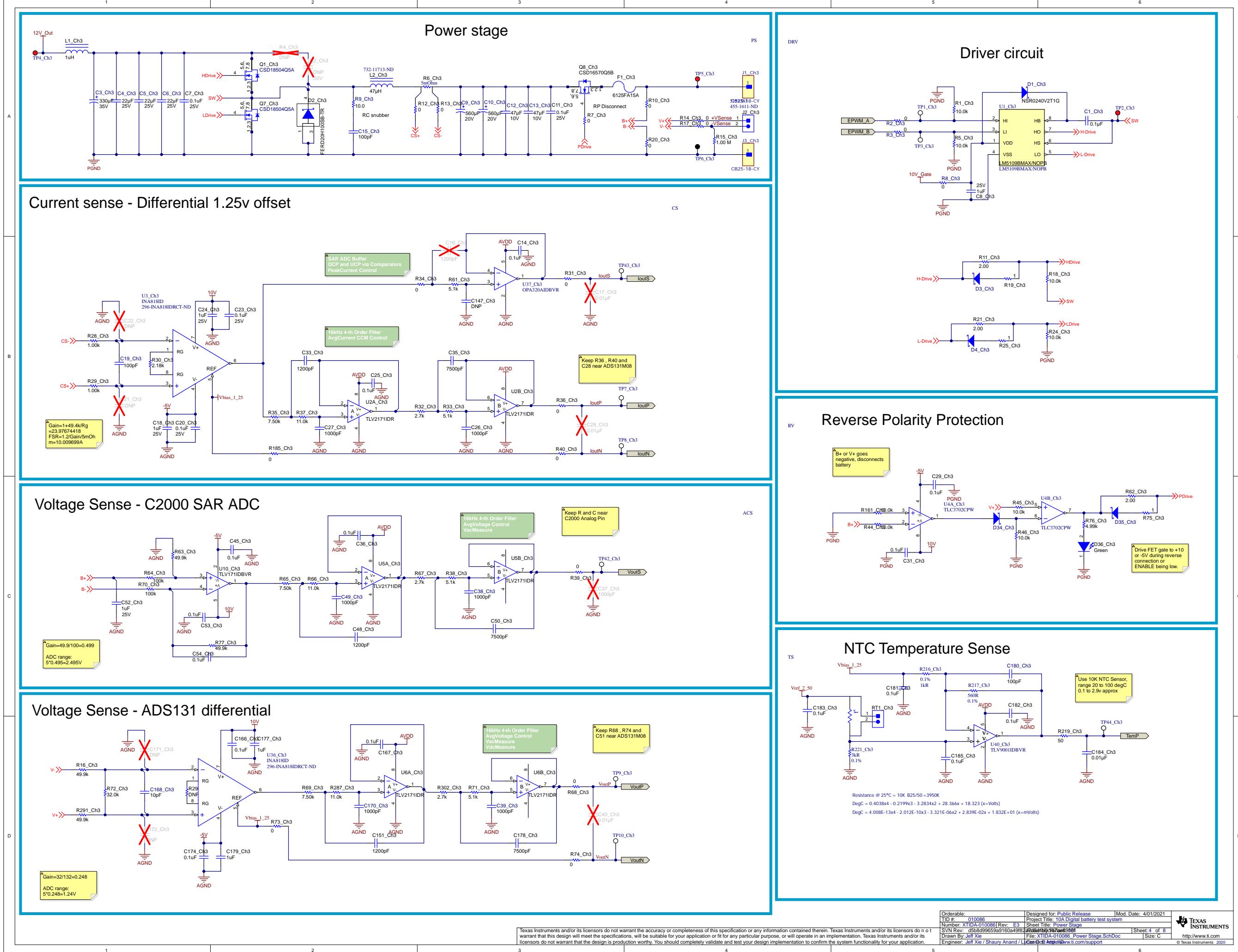
Orderable:	Designed for: Public Release	Mod. Date: 27/12/2019
TID #:	010086	Project Title: 10A Digital battery test system
Number:	XTIDA-010086	Rev: E3 Sheet Title: TOP LEVEL MAIN SHEET
SVN Rev:	d5b8d99659a9160a49f82a35d1b0d4aae6901	Sheet: 2 of 8
Drawn By:	Jeff Xie	File: XTIDA-010086_TOP LEVEL MAIN SHEET.SchDoc Size: B
Engineer:	Jeff Xie / Shaury Anand / LCastor	@AnalogTI.com Support http://www.ti.com

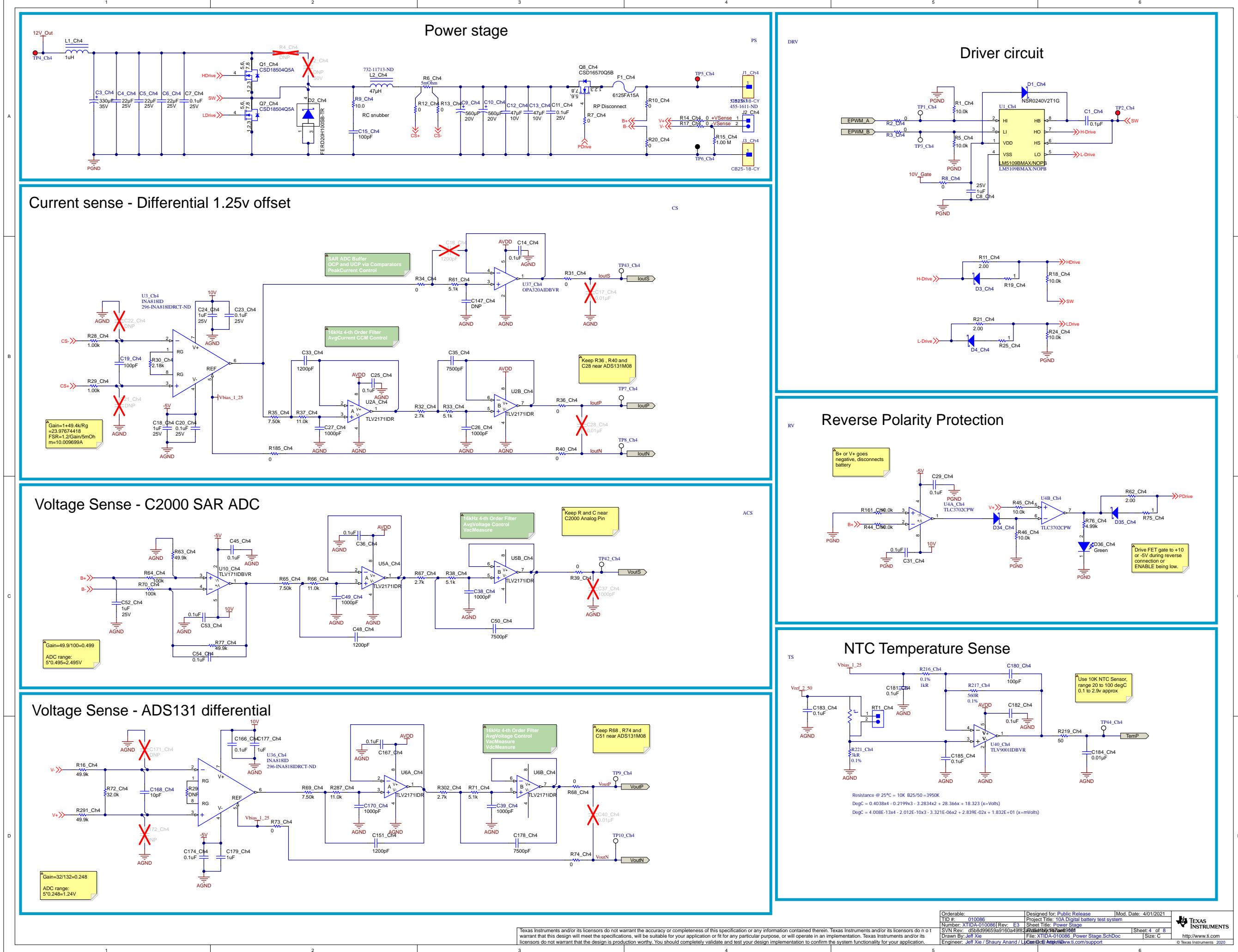


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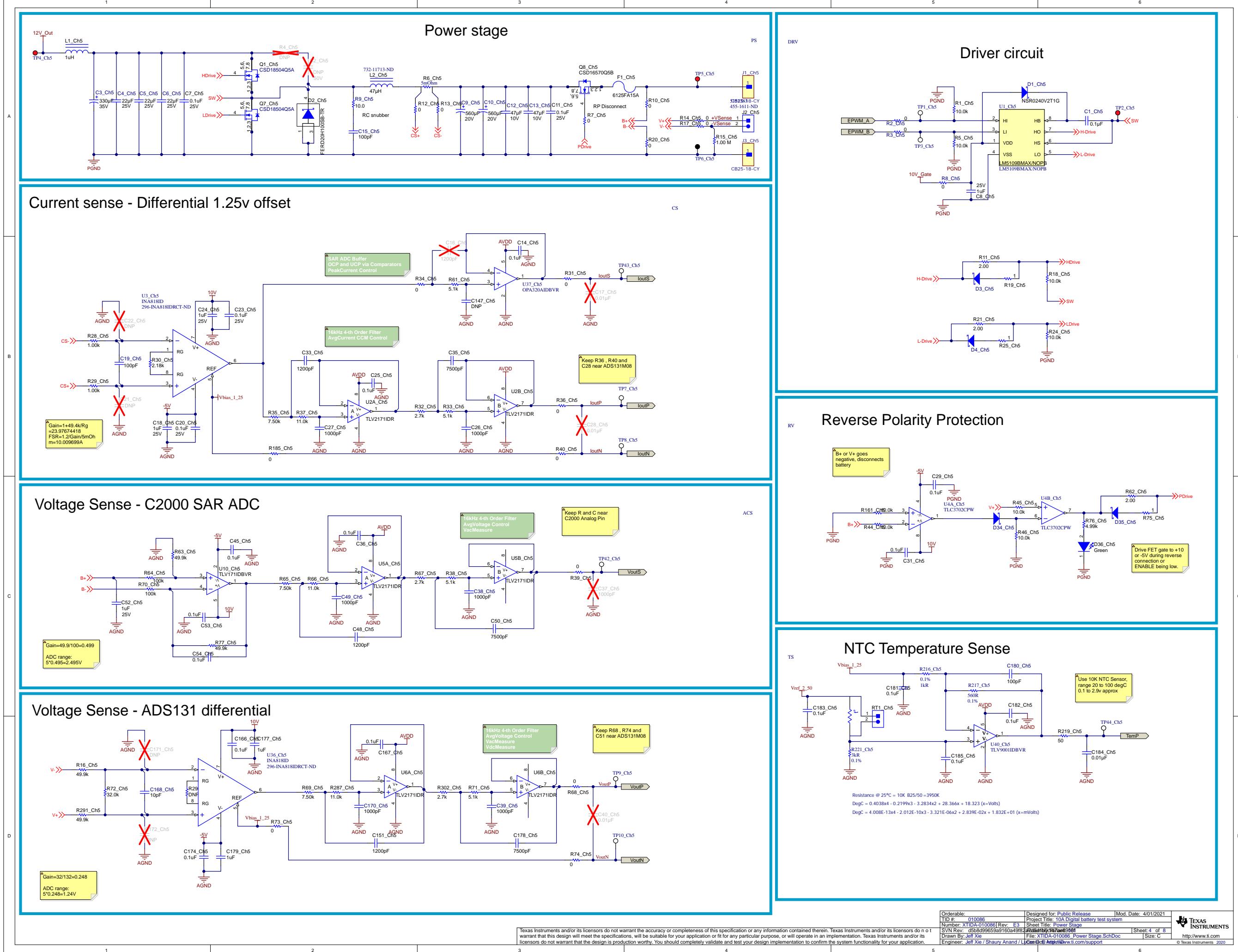


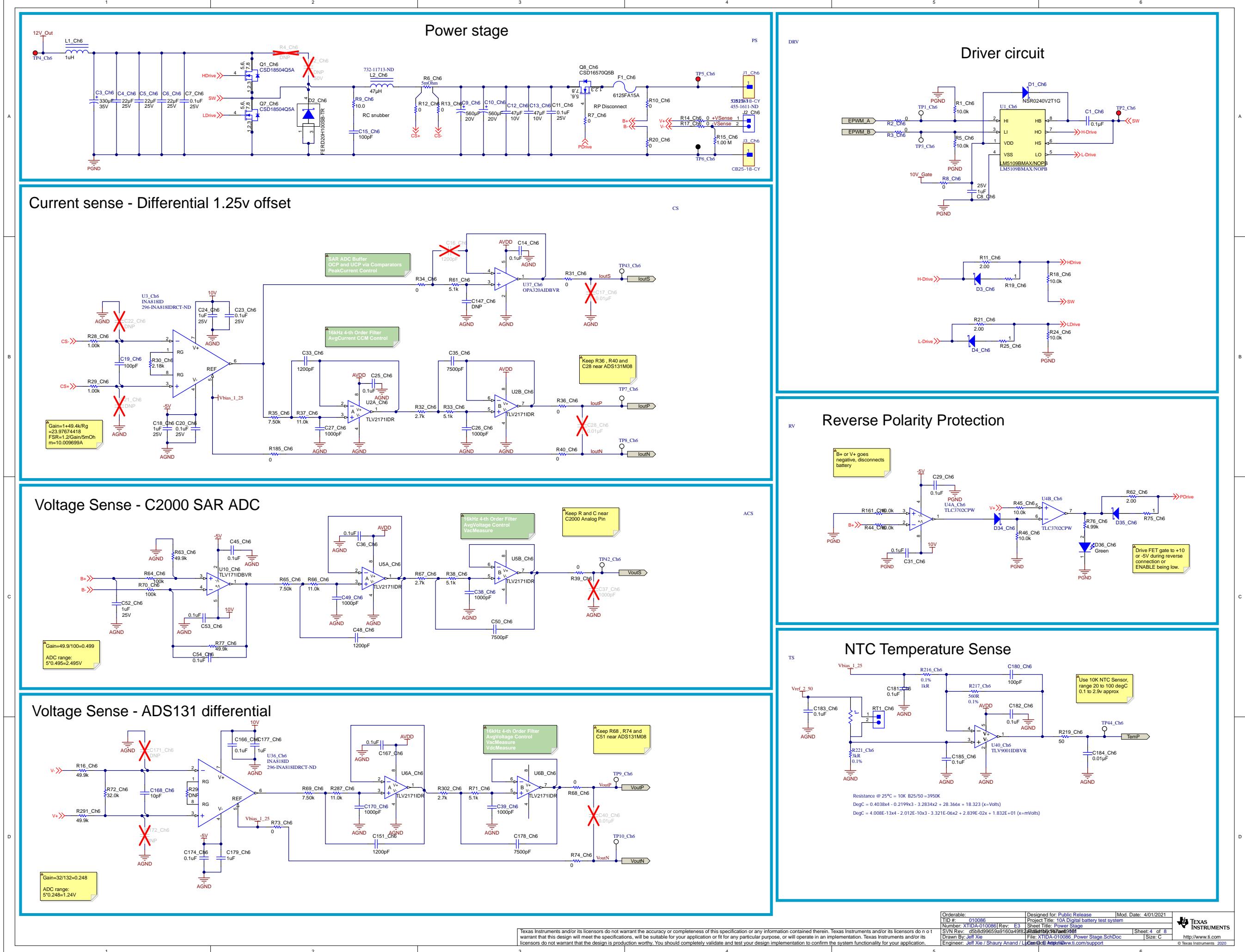


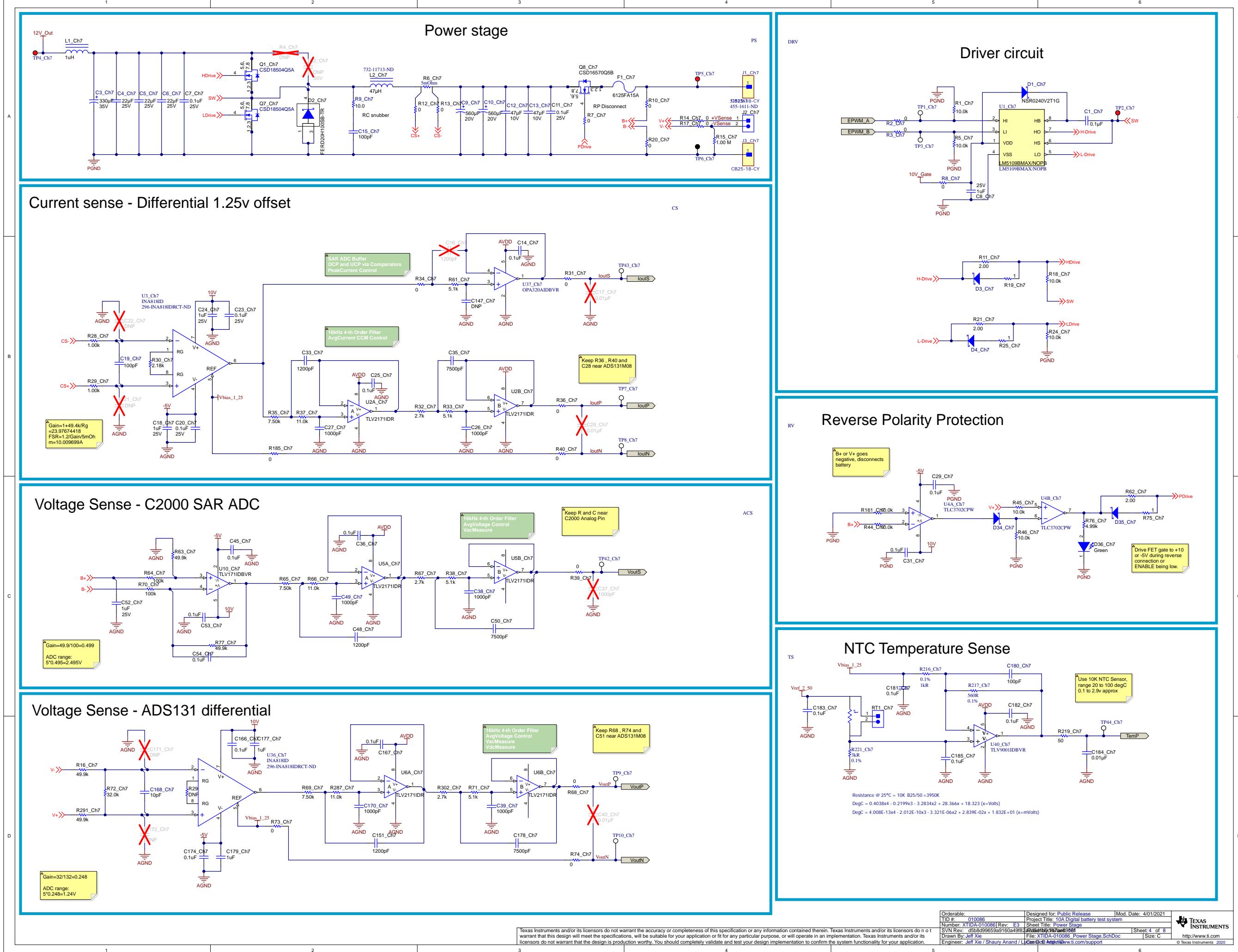


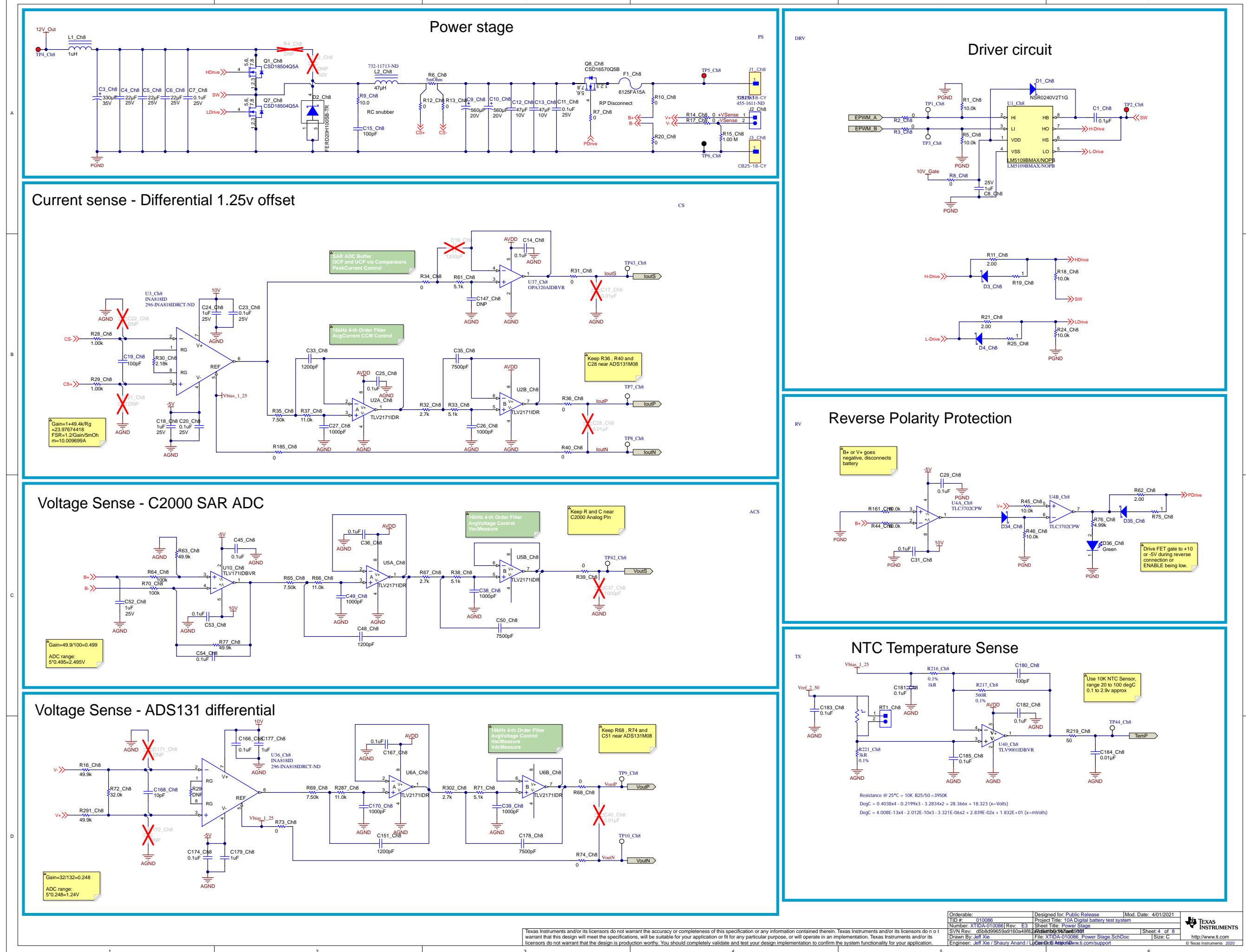
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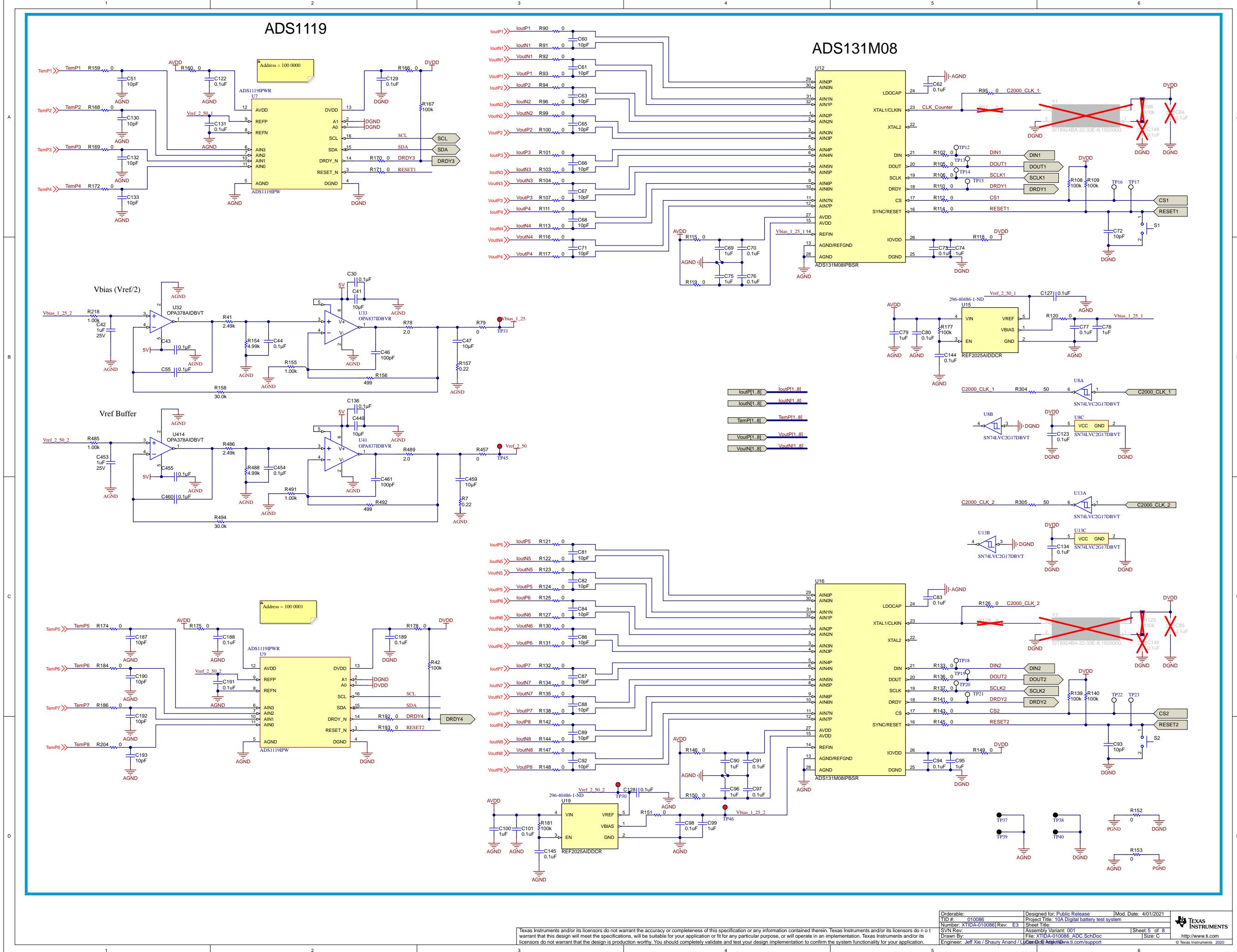
Orderable: 010086	Designed for: Public Release	Mod. Date: 4/01/2021
TID #: 010086	Project Title: 10A Digital Power Stage	
Notes: XREF 010086 Rev. E3	Street Title: Power Stage	
SVN Rev: 0564996599916024982	File Name: XTD0-010086_PowerStage.SchDoc	Sheet 4 of 8
Drawn By: Jeff Xie	Engineer: Jeff Xie / Shaurya Anand / L/C	Size: C
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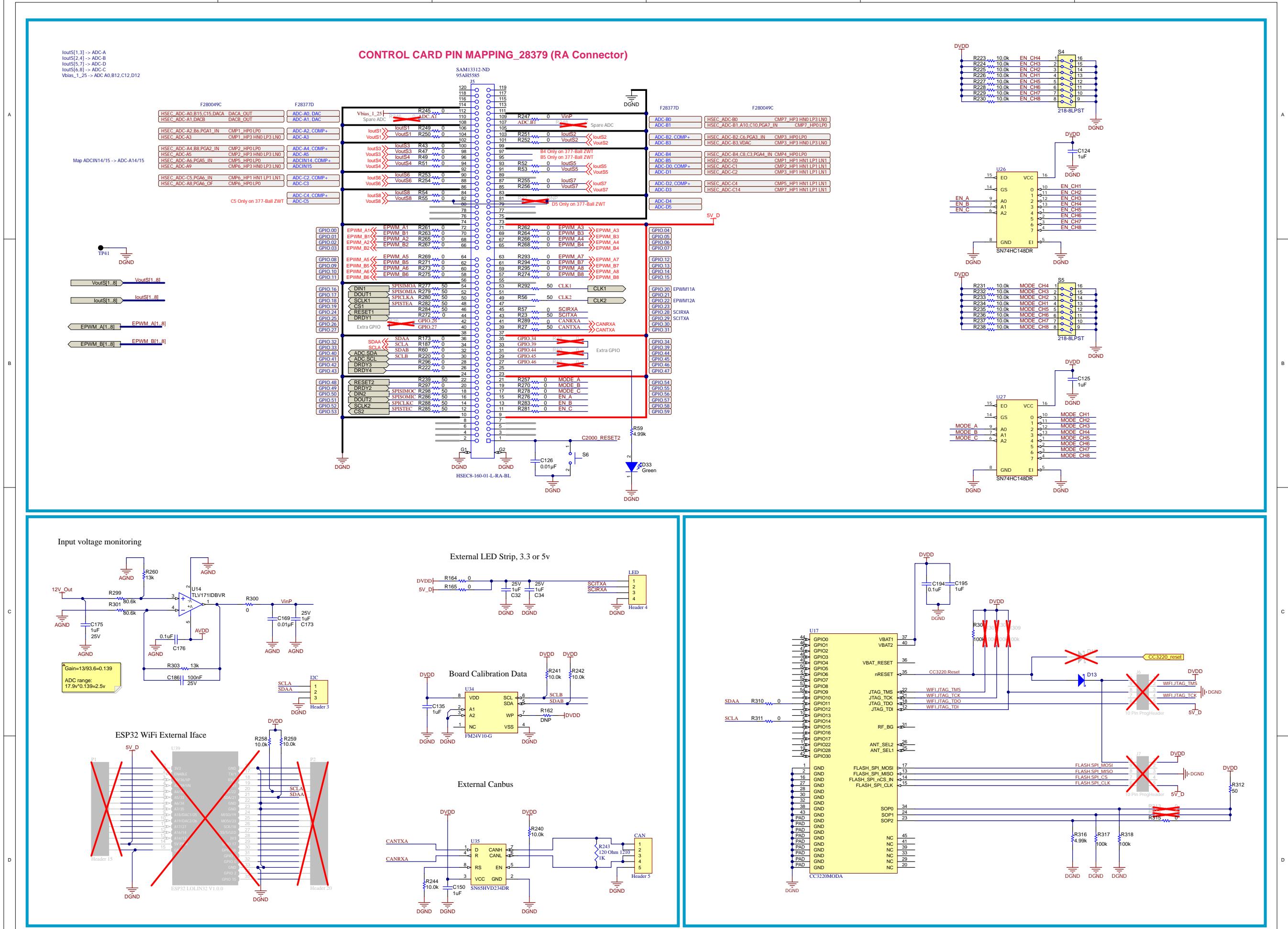








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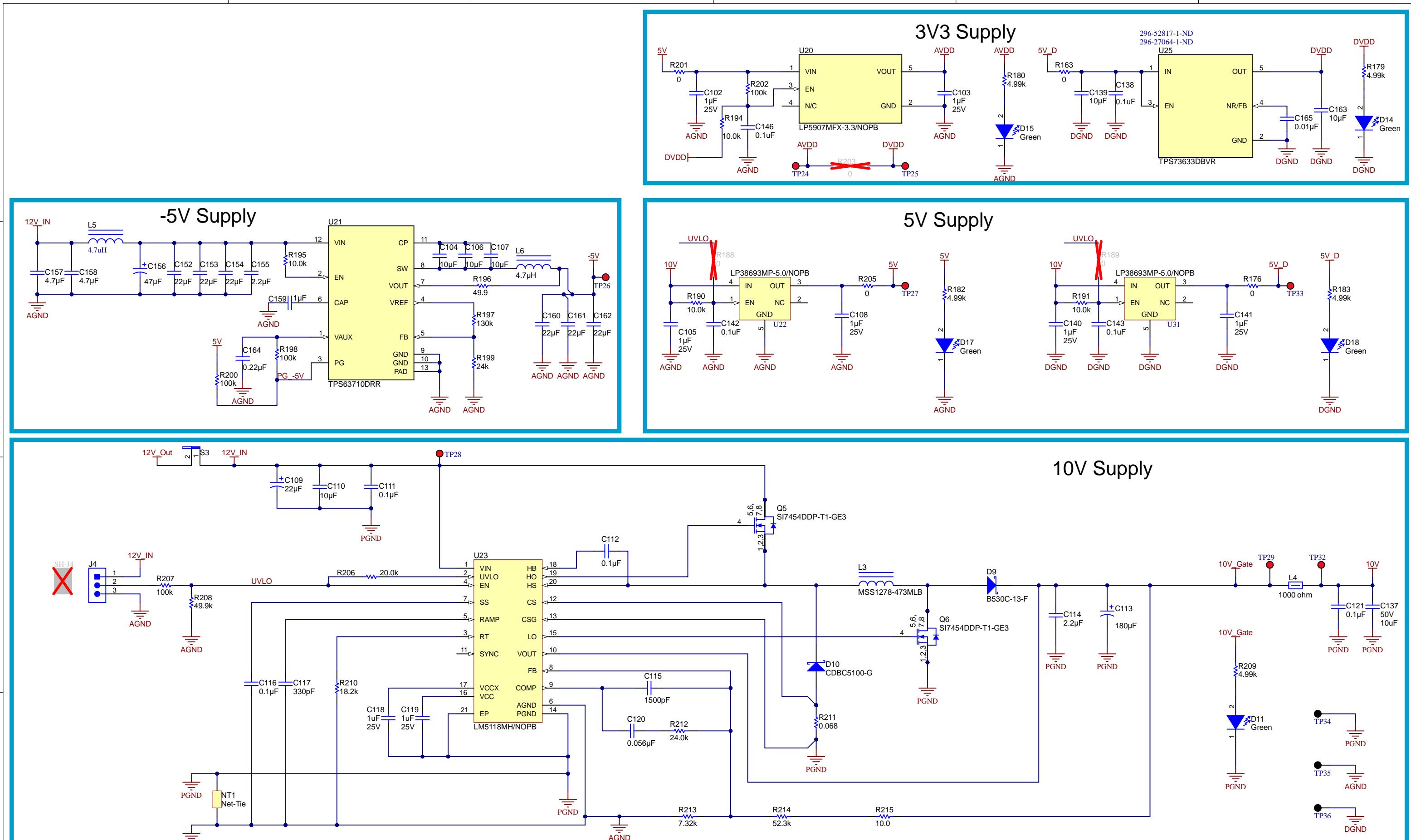
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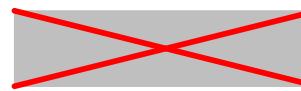
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Orderable: TID #: 010086 Number: XTIDA-010086 Rev: E3	Designed for: Public Release Project Title: 10A Digital battery test system Sheet Title: Control card Assembly Variant: 001 SVN Rev: Drawn By: Jeff Xie File: XTIDA-010086_Bias Power Supply.SchDoc Engineer: Jeff Xie / Shaury Anand / LCastor@Analog.TI.com Mod. Date: 4/01/2021 Sheet: 7 of 8 Size: B
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H1 NY PMS 440 0025 PH
 H2 NY PMS 440 0025 PH
 H3 NY PMS 440 0025 PH
 H4 NY PMS 440 0025 PH
 H5 NY PMS 440 0025 PH
 H6 NY PMS 440 0025 PH
 H13 NY PMS 440 0025 PH
 H14 NY PMS 440 0025 PH
 H17 NY PMS 440 0025 PH
 H19 NY PMS 440 0025 PH

H7 2205
 H8 2205
 H9 2205
 H10 2205
 H11 2205
 H12 2205
 H15 2205
 H16 2205
 H18 2205
 H20 2205

~~FID1 FID2 FID3 FID4 FID5 FID6~~



PCB
LOGO
FCC disclaimer

PCB
LOGO
WEEE logo



PCB
LOGO
Pb-Free Symbol

PCB
LOGO
Logo7

LBL1
 TH14-423-10
 Size: 0.65" x 0.20"
 ZZ1
 This Assembly Note is for PCB labels only

Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	Ti TIDA RLS Version
003	BadBiki Version

ZZ2
 These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
 These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
 These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Orderable:	Designed for: Public Release	Mod. Date: 4/01/2021
TID #:	010086	Project Title: 10A Digital battery test system
Number:	XTIDA-010086	Rev: E3 Sheet Title:
SVN Rev:	Assembly Variant: 001	Sheet: 8 of 8
Drawn By:	File: XTIDA-010086_Hardware.SchDoc	Size: B
Engineer:	Jeff Xie / Shaury Anand / LCastor@Analog.TI.com	http://www.ti.com/support