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دستورالعمل	مد آدرس دهی اپرند اول(مد	مد آدرس دهی اپرند دوم(در	
	آدرس دهی در صورت نداشتن	صورت وجود)	
	اپرند)		
CBR R1,K	Register Mode ¹	Immediate Mode	
AND RO,R10	Register Mode	Register Mode	
MUL R2,R6	Register Mode	Register Mode	
RJMP 0x400	Program Memory		
	Relative Addressing		
	Mode		
ICALL	Program Memory		
	Indirect Addressing		
	Mode		
EIJMP	Program Memory		
	Indirect Addressing		
CALL 0×2000	Mode		
CALL 0x2000	Program Memory Direct		
CD D4 D2	Addressing Mode	Dogistor Modo	
CP R4,R2	Register Mode	Register Mode	
BRBS 2,0x400	Register Mode	Program Memory Relative Addressing	
		Mode	
BRHS 0x300	Program Memory		
BINIS 0x300	Relative Addressing		
	Mode		
MOV R10,R11	Register Mode	Register Mode	
LD RO,X+	Register Mode	Data Memory Indirect	
		Addressing Mode with	
		Post-Increment ²	
LDS R0,0x100	Register Mode	Data Memory Direct	
		Addressing Mode	
STD R20,Y+0x30	Register Mode	Data Memory Indirect	
		Addressing Mode with	
		Displacement	
ST –X,R10	Data Memory Indirect	Register Mode	
	Addressing Mode with		
	Pre-Decrement		
ELPM R4,Z+	Register Mode	Program Memory	
		Indirect Addressing	
		Mode with Post-	
IN D4 5500	Danista (NA) di	Increment	
IN R1,EEDR	Register Mode	I/O ³	

Direct Register Mode
Indirect Register Addressing Mode. Here the value of X register refers to an address in Data Memory
Direct I/O