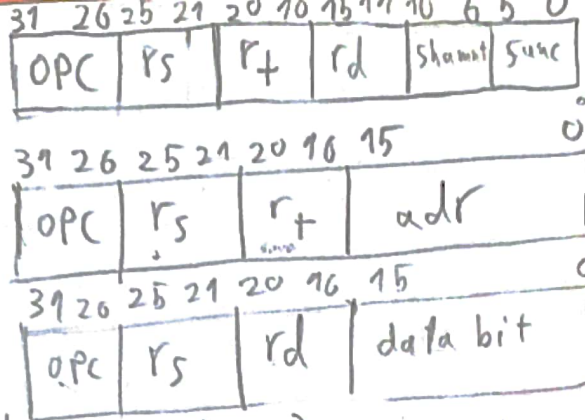


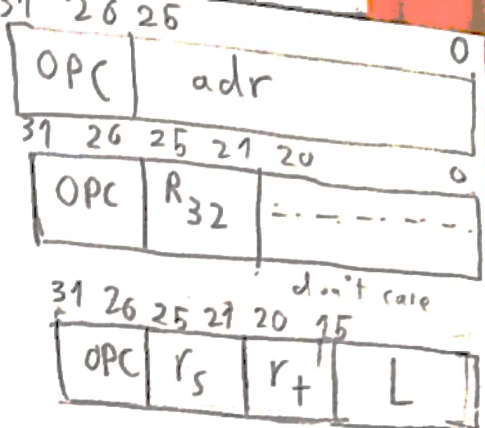
Ref: addi, sli: \rightarrow
ope 2 0000 4f, 000400



$J, J_0 :$

Jr:
000444

beg



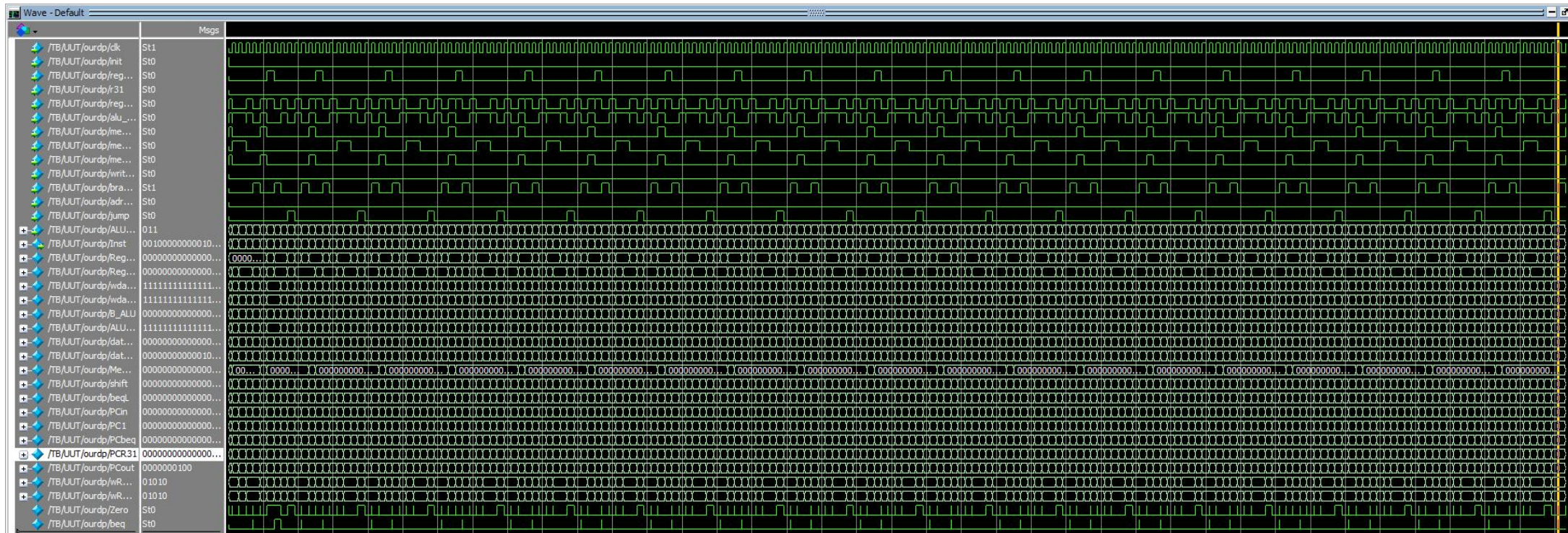
1619454
 1619454
 1619454
 1619454
 1619454
 1619454

	Reg Dst	R31	Reg Write	ALU Src	★ ALU OP	Mem Read	Mem Write	Mem to Reg	Write Pct+9	Branch	Addr R31	Jump
RT	1	0	1	0	00	0	0	0	0	0	0	✓
addi	0	0	1	1	01	0	0	0	0	0	0	0
sw	X	X	0	1	01	0	1	X	X	0	0	0
lw	0	0	1	1	01	1	0	<u>1</u>	0	0	0	0
J.	X	X	0	X	X	0	0	X	X	X	X	1
Jal	X	1	1	X	X	0	0	X	1	X	X	1
Jr.	X	X	0	X	X	0	0	X	X	X	1	0
beq	X	X	0	0	10	0	0	X	X	1	0	0
slti	0	0	1	1	11	0	0	1	0	0	0	0

ALUop	ALU
00	RT
01	add
10	Sub
11	slt

$R_1 \leftarrow \text{max_num}[0]$
 $R_2 \leftarrow \text{index} = 0$
 for $(i = 0; i < 10; i++)$
 $R_{29} \leftarrow \text{num}[i]$
 if $(\text{max} < \text{num}[i])$
 $\text{max} = \text{num}[i]$
 $\text{index} = i$

Loop: $\text{liw } R_1, 1000(R_0)$ $\left\{ \begin{array}{l} \text{sw } R_1, 2000(R_0) \\ \text{sw } R_0, 2004(R_0) \end{array} \right.$
 $\text{slti } R_{10}, R_{29}, 20$
 $\text{beq } R_{10}, R_0, \text{End_Loop}$
 $\text{liw } R_4, 1000(R_{29})$
 $\text{slti } R_{11}, R_1, R_4$
 $\text{beq } R_{11}, R_0, \text{Newi}$
 $\text{sw } R_4, 2000(R_0)$ $\left\{ \begin{array}{l} \text{addi } R_1, R_4, 0 \\ \text{sw } R_{29}, 2004(R_0) \end{array} \right.$
 $\text{sw } R_{29}, 2004(R_0)$
 Newi: $\text{addi } R_{29}, R_{29}, 1$
 J Loop
 End_Loop;



[illegible]