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Ex. No. 3

## Exercise 3 - String Manipulations 3A - Moving a string of bytes

#### Aim:

To move a string from source to location using string manipulation instructions.

## Procedure for executing MASM:

- 1. Mount the local folder in the DOS-BOX using a temp disk name:
   `mount <disk-name> <folder-location>`
- 2. Change directory into the mounted disk: `<disk-name>: `
- 3. Assemble the instructions: `masm <file-name>.asm`
- 5. Debug the executable file to read the memory map and execute the program: `debug <file-name>.exe`. After entering debug mode,
  - a. `d <segment:offset> ` dump(read) memory map from the given location
  - b. `e <segment:offset> ` edit memory values from the given location. Use 'White space' to continue editing and 'new line' to exit editing.
  - c. `u ` unassemble code (with or without <segment:offset>)
  - d. `g ` execute the program
  - e. `?` display command list
  - f. `q` quit the debugger

- 1. Initialise data and extra segment using their respective registers.
- 2. Load the base address of data segment(ds) and extra segment(es) using an intermediate accumulator register(ax) as direct memory transfer is not allowed in 8086.
- 3. Load the words, count of bytes in the string to cx.
- 4. Load the source(si) and destination index(di) register with the offset values of src\_string and dest\_string from their respective segments, i.e., base address of the two strings respectively.
- 5. Clear the direction flag, so that SI and DI can auto-increment.
- 6. Copy byte from DS:SI to ES:DI. Check if CX != 0, then increment SI and DI and decrement CX. Repeat this till CX = 0.
- 7. Terminate the program.



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```
Program
                                         Comment
                                         Comment after ';'
                                         Map CS to code segment, DS to
assume cs:code, ds:data, es:extra
                                         data segment and ES to extra
                                         segment
                                         Initialise data segment and
data segment
                                         extra segment
    words dw 0005H
                                         db = define a byte, dw = define
    src_string db 41H, 70H, 70H, 6CH,
                                         a byte
                                         Initialise words(word),
data ends
                                         src_string(string),
extra segment
                                         dest string(string)
    dest_string db 00H, 00H, 00H, 00H,
 00H
extra ends
                                         Initialise code segment
                                         Move the starting address of
                                         data segment in ax, then move ax
code segment
                                         to ds; starting address of extra
start: mov ax, data
                                         segment in ax, then move ax to
        mov ds, ax
        mov ax, extra
                                         Since in 8086, only code segment
        mov es, ax
                                         register is loaded automatically, the
                                         remaining segment register can be
                                         assigned using general purpose
                                         registers.
                                         Load cx register with words
        mov cx, words
                                         Load si and di index registers
        mov si, offset src_string
                                         with the address of the src and
        mov di, offset dest_string
                                         dest strings.
        cld
                                         Clear the direction flag.
                                         Copy byte from DS:SI to ES:DI.
        rep movsb
                                         Check if CX != 0, then increment
                                         SI and DI and decrement CX.
                                         Repeat this till CX = 0.
                                         Set ah = 4cH
        mov ah, 4cH
                                         Call interrupt routine 21H for
        int 21H
                                         DOS, which terminates if ah =
code ends
                                         4cH
end start
```



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## Unassembled code:

D:\>debug	movstr.exe		
–u			
0760:0000	B86A07	MOV	AX,076A
0760:0003	8ED8	MOV	DS,AX
0760:0005	B86B07	MOV	AX,076B
0760:0008	8ECO	MOV	ES,AX
076C:000A	8B0E0000	MOV	CX,[0000]
076C:000E	BE0200	MOV	SI,0002
0760:0011	BF0000	MOV	DI,0000
0760:0014	FC	CLD	
0760:0015	F3	REPZ	
0760:0016	A4	MOUSB	
0760:0017	B44C	MOV	AH,4C
0760:0019	CD21	INT	21

# Snapshot of sample input and output:

#### Before execution:

-d 076a:000	90															
076A:0000	05	$\infty$	41	70	70	6C	65	00-00	$\infty$	$\infty$	$\infty$	$\infty$	<b>00</b>	$\infty$	<b>00</b>	Apple
076A:0010	<b>00</b>	<b>00</b>	$\infty$	00	$\infty$	00	00	00-00	00	$\infty$	$\infty$	00	<b>00</b>	$\infty$	<b>00</b>	
076A:0020	B8	6A	07	8E	D8	B8	6B	07-8E	CO	8B	ΘΕ	$\infty$	$\infty$	BE	02	. j k
076A:0030	00	$\mathbf{BF}$	$\infty$	$\infty$	FC	FЗ	A4	B4-4C	CD	21	FΕ	77	09	89	46	L.!.wF
076A:0040	FE	8A	46	F9	88	46	F8	FE-46	F9	EB	C9	8A	5E	F8	B7	FFF^
076A:0050	$\infty$	8A	87	48	2F	DΘ	D8	73–17	E8	B6	$\infty$	8A	5E	F8	B7	H∕s^
076A:0060								73-07								H∕s.SP.s.
076A:0070		B6	2C	ЗÁ	46	F8	74	7E-C7	46	FA	$\infty$	$\infty$	8A	46	F8	$\ldots$ ; $F \cdot t^{\sim} \cdot F \cdot \ldots \cdot F$ .
-d 076b:000																
076B:0000								00-00								
076B:0010								07-8E								. j k
076B:0020								B4-4C								L
076B:0030								FE-46								FFF^
076B:0040								73–17								H∕s^
076B:0050								73-07								H∕s.SP.s.
076B:0060								7E-C7								$\ldots$ ; $F \cdot t^{\sim} \cdot F \cdot \ldots \cdot F$ .
076B:0070	<b>B4</b>	00	<b>B1</b>	05	D3	E0	03	06-B4	20	89	46	FC	8A	1E	B6	



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#### After execution:

```
Program terminated normally
-d 076b:0000
976B:0000 41 70 70 6C 65 00 00 00-00 00 00 00 00 00 00 00
                                                                Apple.....
076B:0010 B8 6A 07 8E D8 B8 6B 07-8E C0 8B 0E 00 00 BE 02
                                                                . j. . . . k. . . . . . . . . .
076B:0020 00 BF 00 00 FC F3 A4 B4-4C CD 21 FE 77 09 89 46
                                                                .......L. . . . . . . . . . F
076B:0030  FE 8A 46 F9 88 46 F8 FE-46 F9 EB C9 8A 5E F8 B7
                                                                ..F..F..F....
076B:0040   00 8A 87 48 2F D0 D8 73-17 E8 B6 00 8A 5E F8 B7
                                                                ...H/..s....
076B:0050    00 8A 87 48 2F D0 D8 73-07 53 B0 01 50 E8 73 01
                                                                ...H/..s.S..P.s.
976B:0060 A0 B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8
                                                                \dots; F.t~.F....F.
076B:0070 B4 00 B1 05 D3 E0 03 06-B4 2C 89 46 FC 8A 1E B6
```

#### Result:

Program to move a string from source to destination using string instruction is assembled, executed and verified.



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Ex. No. 3

## 3B - Comparing two strings of bytes

#### Aim:

To compare two strings of bytes.

- 1. Initialise data and extra segment using their respective registers.
- 2. Load the base address of data segment(ds) and extra segment(es) using an intermediate accumulator register(ax) as direct memory transfer is not allowed in 8086.
- 3. Load the bytes, count of bytes [length of string + 1: to differentiate between the mismatch at last index case and perfect match case] in the string to cx.
- 4. Load the source(si) and destination index(di) register with the offset values of src\_string and dest\_string from their respective segments, i.e., base address of the two strings respectively.
- 5. Clear the direction flag, so that SI and DI can auto-increment.
- 6. Compare byte from DS:SI and ES:DI. Check if CX != 0, then compare bytes by subtracting, if zero flag is set, then increment SI and DI and decrement CX. Repeat this till CX = 0 or ZF = 0.
- 7. Load status with the value of CX, this will contain the index of first mismatch counted from back, if not, it will contain 0000H.
- 8. Terminate the program.



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Ex. No. 3

```
Program
                                     Comment
                                     Comment after ';'
                                     Map CS to code segment, DS to data
assume cs:code, ds:data, es:extra
                                     segment and ES to extra segment
                                     Initialise data segment and extra
data segment
                                     segment
    bytes dw 0004H
                                     db = define a byte, dw = define a byte
     src_string db 62H, 65H, 67H
                                     Initialise bytes(word), status(word),
    org 0020H
                                     src string(string),
    status dw 0000H
                                     dest string(string)
data ends
extra segment
    dest_string db 62H, 65H, 74H
extra ends
                                     Initialise code segment
code segment
                                     Move the starting address of data
start: mov ax, data
                                     segment in ax, then move ax to ds;
        mov ds, ax
                                     starting address of extra segment in
        mov ax, extra
                                     ax, then move ax to es.
        mov es, ax
                                     Since in 8086, only code segment register is
                                     loaded automatically, the remaining segment
                                     register can be assigned using general
                                     purpose registers.
                                     Load cx register with bytes
        mov cx, bytes
        mov si, offset src_string
                                     Load si and di index registers with
                                     the address of the src and dest
        mov di, offset dest_string
                                     strings.
                                     Clear the direction flag.
        cld
                                     Compare byte from DS:SI and ES:DI.
        repe cmpsb
                                     Check if CX != 0, then compare bytes
                                     by subtracting, if zero flag is set,
                                     then increment SI and DI and decrement
                                     CX. Repeat this till CX = 0 or ZF = 0.
                                     Move cx to status. Contains index of
        mov status, cx
                                     first mismatch or 0000H
                                     Set ah = 4cH
        mov ah, 4cH
                                     Call interrupt routine 21H for DOS,
        int 21H
                                     which terminates if ah = 4cH
code ends
end start
```



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## Unassembled code:

D:\>debug	cmpstr.exe		
–u			
076E:0000	B86A07	MOV	AX,076A
076E:0003	8ED8	MOV	DS,AX
076E:0005	B86D07	MOV	AX,076D
076E:0008	8ECO	MOV	ES,AX
076E:000A	8B0E0000	MOV	CX,[0000]
076E:000E	BE0200	MOV	SI,000Z
076E:0011	BF0000	MOV	DI,0000
076E:0014	FC	CLD	
076E:0015	F3	REPZ	
076E:0016	A6	CMPSB	
076E:0017	890E2000	MOV	[0020],CX
076E:001B	B44C	MOV	AH,4C
076E:001D	CD21	INT	21

## Snapshot of sample input and output:

Case i: Unequal Strings
Before Execution:

-d 076a:0000 076a:0000 076a:0000 04 00 62 65 67 00 00 00-00 00 00 00 00 00 00 00 00 00	DETOTE LA	ecu	CTO														
076A:0010 00 00 00 00 00 00 00 00 00 00 00 00	-d 076a:00	000															
076A:0020 00 00 00 00 00 00 00 00 00 00 00 00	076A:0000	04	$\infty$	62	65	67	$\infty$	$\infty$	00-00	$\infty$	$\infty$	00	<b>00</b>	$\infty$	$\infty$	<b>00</b>	beg
076A:0030 62 65 74 00 00 00 00 00-00 00 00 00 00 00 00 00	076A:0010	$\infty$	$\infty$	$\infty$	$\infty$	$\infty$	$\infty$	$\infty$	00-00	$\infty$	$\infty$	00	00	$\infty$	$\infty$	<b>00</b>	
076A:0040 B8 6A 07 8E D8 B8 6D 07-8E C0 8B 0E 00 00 BE 02    .jm	076A:0020	$\infty$	$\infty$	$\infty$	$\infty$	$\infty$	$\infty$	$\infty$	00-00	$\infty$	$\infty$	00	00	$\infty$	$\infty$	<b>00</b>	
076A:0050 00 BF 00 00 FC F3 A6 89-0E 20 00 B4 4C CD 21 B7	076A:0030	62	65	74	$\infty$	$\infty$	$\infty$	$\infty$	00-00	$\infty$	$\infty$	00	$\infty$	$\infty$	$\infty$	<b>00</b>	bet
076A:0060 00 8A 87 48 2F D0 D8 73-07 53 B0 01 50 E8 73 01H/s.SP.s. 076A:0070 A0 B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8;F.t~.Fd 076d:0000 076D:0000 62 65 74 00 00 00 00 00-00 00 00 00 00 00 00 00	076A:0040	<b>B8</b>	6A	07	8E	D8	<b>B8</b>	6D	07-8E	CO	8B	ΘE	$\infty$	$\infty$	$\mathbf{BE}$	02	.jm
076A:0070 A0 B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8; F.t~.Fd 076d:0000   076D:0000 62 65 74 00 00 00 00 00-00 00 00 00 00 00 00 00	076A:0050	$\infty$	$\mathbf{BF}$	$\infty$	$\infty$	FC	FЗ	A6	89-0E	20	$\infty$	<b>B4</b>	<b>4</b> C	CD	21	B7	L
-d 076d:0000  076D:0000 62 65 74 00 00 00 00 00-00 00 00 00 00 00 00 00	076A:0060	<b>00</b>	8A	87	48	2F	$D\Theta$	D8	73-07	53	BΘ	01	50	E8	73	01	H∕s.SP.s.
076D:0000 62 65 74 00 00 00 00 00-00 00 00 00 00 00 00 00	076A:0070	ΑO	<b>B6</b>	2C	ЗÁ	46	F8	74	7E-C7	46	FA	00	00	8A	46	F8	$\dots$ ; F.t~.FF.
076D:0010       B8 6A 07 8E D8 B8 6D 07-8E C0 8B 0E 00 00 BE 02       jm         076D:0020       00 BF 00 00 FC F3 A6 89-0E 20 00 B4 4C CD 21 B7          076D:0030       00 8A 87 48 2F D0 D8 73-07 53 B0 01 50 E8 73 01          076D:0040       A0 B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8	-d 076d:00	000															
076D:0020 00 BF 00 00 FC F3 A6 89-0E 20 00 B4 4C CD 21 B7	076D:0000	62	65	74	$\infty$	$\infty$	$\infty$	$\infty$	00-00	$\infty$	$\infty$	00	00	$\infty$	$\infty$	<b>00</b>	bet
076D:0030 00 8A 87 48 2F D0 D8 73-07 53 B0 01 50 E8 73 01H/s.SP.s. 076D:0040 A0 B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8;F.t~.FF. 076D:0050 B4 00 B1 05 D3 E0 03 06-B4 2C 89 46 FC 8A 1E B6,F 076D:0060 2C B7 00 D1 E3 FF B7 FA-15 9A 00 00 A0 04 89 DE ,	076D:0010	<b>B8</b>	6A	07	8E	<b>D8</b>	B8	6D	07-8E	CO	8B	ΘE	$\infty$	$\infty$	BE	02	.jm
076D:0040 A0 B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8; F.t~.FF. 076D:0050 B4 00 B1 05 D3 E0 03 06-B4 2C 89 46 FC 8A 1E B6, F 076D:0060 2C B7 00 D1 E3 FF B7 FA-15 9A 00 00 A0 04 89 DE ,	076D:0020	$\infty$	$\mathbf{BF}$	$\infty$	$\infty$	FC	FЗ	A6	89-0E	20	$\infty$	<b>B4</b>	<b>4</b> C	CD	21	B7	L
076D:0050 B4 00 B1 05 D3 E0 03 06-B4 2C 89 46 FC 8A 1E B6	076D:0030	$\infty$	8A	87	48	2F	$D\Theta$	D8	73-07	53	BΘ	01	50	E8	73	01	H∕s.SP.s.
076D:0060 2C B7 00 D1 E3 FF B7 FA-15 9A 00 00 A0 04 89 DE ,	076D:0040	ΑO	<b>B6</b>	ZC	ЗÁ	46	F8	74	7E-C7	46	FA	90	00	8A	46	F8	$\dots$ ; F.t~.FF.
	076D:0050	<b>B4</b>	00	<b>B1</b>	05	<b>D3</b>	EΘ	03	06 - B4	2C	89	46	FC	8A	<b>1E</b>	<b>B6</b>	, . F
076D:0070 06 C4 7E FA B9 00 01 5A-1E 8E DA FC F2 A5 1F 8A~Z	076D:0060	20	B7	00	D1	<b>E3</b>	FF	B7	FA-15	9A	00	00	AO	04	89	DE	,
	076D:0070	06	C4	7E	FA	B9	00	01	5A-1E	8E	DA	FC	FZ	A5	1F	8A	~Z



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Ex. No. 3

After Execution: 076a:0020 = 01H -> Strings are **different** at index 1 from back, i.e., 3 from the front

```
-g
Program terminated normally
-d 076a:0000
076A:0000
       04 00 62 65 67 00 00 00-00 00 00 00 00 00 00 00
                                             ..beg.....
       076A:0010
076A:0020
       076A:0030  62 65 74 00 00 00 00 00-00 00 00 00 00 00 00 00
                                            bet..........
.j....m.......
076A:0050
       00 BF 00 00 FC F3 A6 89-0E 20 00 B4 4C CD 21 B7
                                            ....L.!.
       00 8A 87 48 2F DO D8 73-07 53 BO 01 50 E8 73 01
976A:0060
                                             ...H/..s.S..P.s.
       AO B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8
                                            \dots; F.t~.F....F.
076A:0070
```

Case ii: Equal Strings Before Execution:

```
-e 076d:0000
076D:0000 62.
               65.
                     74.67
d 076a:0000
076A:0000   04 00 62 65 67 00 00 00-00 00 00 00 00 00 00 00
                                                 ..beg......
076A:0030  62 65 67 00 00 00 00 00-00 00 00 00 00 00 00 00
                                                 beg.....
076A:0040
        B8 6A 07 8E D8 B8 6D 07-8E CO 8B 0E 00 00 BE 02
                                                 . j . . . . m. . . . . . . . .
076A:0050
        00 BF 00 00
                  FC
                    F3 A6 89-0E
                              20 00 B4 4C
                                        CD 21 B7
                                                 ...... ..L. †.
076A:0060 00 8A 87 48 2F D0 D8 73-07 53 B0 01 50 E8 73 01
                                                 ...H∕..s.S..P.s.
\dots; F.t~.F....F.
-d 076d:0000
        62 65 67 00 00 00 00 00-00 00 00 00 00 00 00 00
076D:0000
                                                 beg.....
076D:0010
        B8 6A 07 8E D8 B8 6D 07-8E C0 8B 0E 00 00 BE 02
                                                 .j....m.......
076D:0020  00 BF 00 00 FC F3 A6 89-0E 20 00 B4 4C CD 21 B7
                                                 ....... ..L. !..
076D:0030 00 8A 87 48 2F D0 D8 73-07 53 B0 01 50 E8 73 01
                                                 ...H/..s.S..P.s.
                                                 \dots; F.t~.F....F.
        AO B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8
076D:0040
        B4 00 B1 05 D3 E0 03 06-B4 2C 89 46 FC 8A 1E B6
076D:0050
                  E3 FF B7
                         FA-15 9A 00 00 A0 04 89 DE
076D:0060
        2C B7 00 D1
..~...Z.....
```



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After Execution: 076a:0020 = 00H -> Strings are same

```
Program terminated normally
-d 076a:0000
076A:0000 04 00 62 65 67 00 00 00-00 00 00 00 00 00 00 00
                                                 ..beg.....
076A:0010
        076A:0030 62 65 67 00 00 00 00 00-00 00 00 00 00 00 00 00
                                                 beg......
076a:0040 B8 6a 07 8E D8 B8 6D 07-8E C0 8B 0E 00 00 BE 02
                                                 .j....m......
076A:0050 00 BF 00 00 FC F3 A6 89-0E 20 00 B4 4C CD 21 B7
076A:0060 00 8A 87 48 2F D0 D8 73-07 53 B0 01 50 E8 73 01
                                                 ...H∕..s.S..P.s.
076A:0070 A0 B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8
                                                 \dots; F.t~.F....F.
```

#### Result:

Program to compare two strings of bytes is assembled, executed and verified.



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Ex. No. 3

## 3C - Searching a byte in a string

#### Aim:

To search for a byte in the given string

- 1. Initialise data and extra segment using their respective registers.
- 2. Load the base address of data segment(ds) and extra segment(es) using an intermediate accumulator register(ax) as direct memory transfer is not allowed in 8086.
- 3. Load the bytes, count of bytes [length of string + 1: to differentiate between the key occurrence at last index case and no occurrence case] in the string to cx.
- 4. Load the destination index(di) register with the offset values of search\_string, i.e., base address of the string.
- 5. Load al with key to be searched in search\_string.
- 6. Clear the direction flag, so that SI and DI can auto-increment.
- 7. Compare the byte from AL with ES:DI. Check if CX != 0, then compare bytes by subtracting, if zero flag is not set, then increment SI and DI and decrement CX. Repeat this till CX = 0 or ZF != 0.
- 8. Load status with the value of CX, this will contain the index of first occurrence counted from back, if no occurrence, it will contain 00H.
- 9. Terminate the program.



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Ex. No. 3

```
Program
                                      Comment
                                      Comment after ';'
assume cs:code, ds:data, es:extra
                                      Map CS to code segment, DS to data
                                      segment and ES to extra segment
                                      Initialise data segment and extra
data segment
    bytes dw 0004H
                                      db = define a byte, dw = define a byte
    key db 01H
                                      Initialise bytes(word), status(word),
    org 0020H
                                      src string(string), dest string(string)
    status dw 0000H
data ends
extra segment
    search_string db 01H, 02H, 03H
extra ends
                                      Initialise code segment
code segment
                                      Move the starting address of data
start: mov ax, data
                                      segment in ax, then move ax to ds;
        mov ds, ax
                                      starting address of extra segment in
        mov ax, extra
                                      ax, then move ax to es.
        mov es, ax
                                      Since in 8086, only code segment register is
                                      loaded automatically, the remaining segment
                                      register can be assigned using general purpose
                                      registers.
                                      Load cx register with bytes
        mov cx, bytes
                                      Load di index register with the address
        mov di, offset search_string
                                      of the search string.
                                      Load al with the byte to search, key
        mov al, key
                                      Clear the direction flag.
        cld
                                      Compare the byte from AL with ES:DI.
        repne scasb
                                      Check if CX != 0, then compare bytes by
                                      subtracting, if ZF = 0, then increment
                                      SI and DI and decrement CX. Repeat this
                                      till CX = 0 or ZF != 0.
                                      Move cx to status. Contains index of
        mov status, cx
                                      first mismatch or 0000H
        mov ah, 4cH
                                      Set ah = 4cH
        int 21H
                                      Call interrupt routine 21H for DOS,
code ends
                                      which terminates if ah = 4cH
end start
```



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Ex. No. 3

#### Unassembled code:

D: <b>\&gt;</b> debug	SCANSTR.EXE		
–u			
076E:0000	B86A07	MOV	AX,076A
076E:0003	8ED8	MOV	DS,AX
076E:0005	B86D07	MOV	AX,076D
076E:0008	8EC0	MOV	ES,AX
076E:000A	8B0E0000	MOV	CX,[0000]
076E:000E	BF0000	MOV	DI,0000
076E:0011	A00200	MOV	AL,[0002]
076E:0014	FC	CLD	
076E:0015	FZ	REPNZ	
076E:0016	AE	SCASB	
076E:0017	890E2000	MOV	[0020],CX
076E:001B	B44C	MOV	AH,4C
076E:001D	CD21	INT	21

## Snapshot of sample input and output:

Case i: String containing key

Before execution: -d 076a:0000 076A:0000 04 00 01 00 00 00 00 00-00 00 00 00 00 00 00 00 076A:0010 076A:0030 01 02 03 00 00 00 00 00-00 00 00 00 00 00 00 00 B8 6A 07 8E D8 B8 6D 07-8E CO 8B 0E 00 00 BF 076A:0040 00. j . . . . m . . . . . . . . . 00 A0 02 00 FC 076A:0050 F2 ΑE 89-0E 20 00 B4 **4**C CD 21 **B7** 076A:0060 00 8A 87 48 2F DO D8 73-07 53 BO 01 50 E8 73 01 ...H/..s.S..P.s. 076A:0070 A0 B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8  $\dots$ ; F.t~.F....F. -d 076d:0000 976D:0000 01 02 03 00 00 00 00 00-00 00 00 00 00 00 00 00 076D:0010 B8 6A 07 8E D8 B8 6D 07-8E CO 8B 0E 00 00 BF 00.j....m.... 076D:0020 00 A0 02 00 FC F2 AE 89-0E 20 00 B4 4C CD 21 B7 076D:0030 00 8A 87 48 2F D0 D8 73-07 53 B0 01 50 E8 73 01 ...H/..s.S..P.s.  $\dots$ ; F.t~.F....F. 076D:0040 AO B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8 B4 00 B1 05 D3 E0 03 06-B4 2C 89 46 FC 076D:0050 8A 1E B6 2C B7 00 D1 E3 FF FA-15 9A 00 00 A0 04 89 DE 076D:0060 В7 076D:0070 06 C4 7E FA B9 00 01 5A-1E 8E DA FC F2 A5 1F 8A . . ~ . . . . Z. . . . . . .



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Ex. No. 3

After execution: 076a:0020 = 03H -> Key **found** at index 3 from back, i.e., 1 from the front

```
g
Program terminated normally
-d 076a:0000
976A:0000
        04 00 01 00 00 00 00 00-00 00 00 00 00 00 00 00
976A:0010
        076A:0020
         076A:0030
        01 02 03 00 00 00 00 00-00 00 00 00 00 00 00 00
                  D8 B8 6D 07-8E CO 8B 0E 00 00 BF 00
076A:0040
         B8 6A 07 8E
                                                   .j...m...
                  FC
                     F2 AE 89-0E
                               20 00 B4
                                       4C CD 21 B7
976A:0050
         00 A0 02 00
         00 8A 87 48 2F DO D8 73-07 53 BO 01 50 E8 73 01
076A:0060
                                                    .H/..s.S..P.s.
076A:0070
        AO B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8
```

Case ii: String not containing key Before execution:

```
-e 076a:0000
076A:0000 04.
                 ΘΘ.
                         01.ff
d 076a:0000
976A:0000
          04 00 FF 00 00 00 00 00-00 00 00 00 00 00 00 00
076A:0010
          076A:0020
          076A:0030
          01 02 03 00 00 00 00 00-00 00 00 00 00 00 00 00
                                                          .j....m.......
976A:0040
          B8 6A 07 8E
                     D8 B8 6D 07-8E
                                   co
                                      8B OE OO
                                               00 BF
                                                    00
076A:0050
          00 A0 02 00 FC F2 AE 89-0E
                                   20 00 B4 4C CD 21 B7
                                                          ........ ..L.!.
          00 8A 87 48 2F DO D8 73-07 53 BO 01 50 E8 73 01
076A:0060
                                                          ...H/..s.S..P.s.
                                                          ..,:F.t~.F....F.
076A:0070
          AO B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8
-d 076d:0000
976D:0000
          01 02 03 00 00 00 00 00-00 00 00 00 00 00 00 00
976D:0010
          B8 6A 07 8E D8 B8 6D 07-8E C0 8B 0E 00 00 BF
                                                     \mathbf{00}
                                                          .j....m........
076D:0020
          00 A0 02 00 FC F2 AE 89-0E 20 00 B4 4C CD 21 B7
                                                          ....... ..L.!.
                                                          ...H∕..s.S..P.s.
                                      BO 01 50 E8 73 01
076D:0030
          00 8A 87 48
                     2F DO D8
                              73-07 53
                                                          \ldots; F. t^{\sim}.F. \ldotsF.
                     46 F8 74
                              7E-C7 46
076D:0040
          AO B6 2C 3A
                                      FA 00 00 8A 46 F8
976D:0050
          B4 00 B1 05
                     D3
                        E0 03
                              06 - B4
                                   2C
                                      89 46 FC
                                               8A 1E
                                                     B6
076D:0060
          2C B7 00 D1
                     E3 FF B7 FA-15 9A 00 00 A0 04 89 DE
076D:0070
          06 C4 7E FA B9 00 01 5A-1E 8E DA FC F2 A5 1F 8A
                                                            ....Z.....
```



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After execution: 076a:0020 = 00H -> Key not found

#### Result:

Program to search a byte in the given string is assembled, executed and verified.



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Ex. No. 3

## 3D - moving a string without using string instructions

#### Aim:

To move a string from source to location without using string manipulation instructions.

- 1. Initialise data and extra segment using their respective registers.
- 2. Load the base address of data segment(ds) and extra segment(es) using an intermediate accumulator register(ax) as direct memory transfer is not allowed in 8086.
- 3. Load the bytes, count of bytes in the string to cx.
- 4. Load the source(si) and destination index(di) register with the offset values of src and dest from their respective segments, i.e., base address of the two strings respectively.
- 5. Label 'here' for this stub. Load al with the value at si using [si]. Move the value from al to es:di using es:[di].
- 6. Increment SI and DI.
- 7. Loop to 'here' label till CX = 0.
- 8. Terminate the program.



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Ex. No. 3

```
Comment
Program
                                      Comment after ';'
assume cs: code, ds: data, es: extra
                                      Map CS to code segment, DS to data
                                       segment and ES to extra segment
                                       Initialise data segment and extra
data segment
                                       segment
    count dw 0003H
                                      db = define a byte, dw = define a
    src db 62H, 65H, 67H
                                       byte
data ends
                                      Initialise count(word),
extra segment
                                      src(string), dest(string)
    dest db 00H, 00H, 00H
extra ends
                                      Initialise code segment
code segment
                                      Move the starting address of data
start: mov ax, data
                                       segment in ax, then move ax to ds;
        mov ds, ax
                                       starting address of extra segment
        mov ax, extra
                                       in ax, then move ax to es.
        mov es, ax
                                      Since in 8086, only code segment register
                                       is loaded automatically, the remaining
                                       segment register can be assigned using
                                       general purpose registers.
        mov cx, count
                                       Load cx register with count
        mov si, offset src
                                       Load si and di index registers
        mov di, offset dest
                                      with the address of the src and
                                      dest strings.
here:
        mov al, [si]
                                      Move [si] to al
        mov es:[di], al
                                      Move al to es:[di]
                                      Increment si
        inc si
                                      Increment di
        inc di
                                      Decrement cx. If cx != 0, jump to
        loop here
                                       'here'.
                                      Set ah = 4cH
        mov ah, 4cH
                                      Call interrupt routine 21H for
        int 21H
                                      DOS, which terminates if ah = 4cH
code ends
end start
```



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Ex. No. 3

#### Unassembled code:

•	movbes.exe		
−u 076C : 0000		MOV	AX,076A
076C:0003	8ED8	MOV	DS,AX
076C:0005	B86B07	MOV	AX,076B
076C:0008	8ECO	MOV	ES,AX
076C:000A	8B0E0000	MOV	CX,[0000]
076C:000E	BE0200	MOV	SI,0002
0760:0011	BF0000	MOV	DI,0000
076C:0014	8A04	MOV	AL,[SI]
076C:0016	26	ES:	
0760:0017	8805	MOV	[DI],AL
0760:0019	46	INC	SI
076C:001A	47	INC	DI
076C:001B	E2F7	LOOP	0014
076C:001D	B44C	MOV	AH,4C
076C:001F	CD21	INT	21

## Snapshot of sample input and output:

#### Before execution:

```
-d 076a:000
076A:0000
          03 00 62 65 67 00 00 00-00 00 00 00 00 00 00 00
                                                           ..beg......
076A:0010
          076A:0020
          B8 6A 07 8E D8 B8 6B 07-8E C0 8B 0E 00 00 BE 02
                                                           . j. . . . k. . . . . . . .
                                                           ......&...FG....L.
076A:0030
          00 BF 00 00 8A 04 26 88-05 46 47 E2 F7 B4 4C CD
076A:0040
          21 8A 46 F9 88
                        46 F8 FE-46 F9 EB C9 8A 5E F8 B7
                                                           † .F . .F . .F . . . .
076A:0050
          00 8A 87 48 2F DO D8 73-17 E8 B6 00 8A 5E F8 B7
                                                           ...H∕..s....
076A:0060
          00 8A 87 48 2F
                        DO D8 73-07 53 BO 01 50 E8 73 01
                                                           ...H/..s.S..P.s.
076A:0070 A0 B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A
                                                           ..,:F.t~.F....F.
-d 076b:0000
076B:0000
          076B:0010
          B8 6A 07 8E D8 B8 6B 07-8E CO 8B 0E 00 00 BE 02
                                                           . j. . . . k . . . . . . . . .
                                                           .....&...FG....L.
076B:0020
          00 BF
               \mathbf{00}
                   \infty
                     8A 04
                            26 88-05
                                    46 47 E2
                                             F7 B4 4C CD
                                                           †.F..F..F...^..
076B:0030
          21 8A 46 F9 88 46 F8 FE-46 F9 EB C9 8A 5E F8 B7
076B:0040
          00 8A 87 48 2F DO D8 73-17 E8 B6 00 8A 5E F8 B7
                                                           ...H∕..s....
076B:0050
          00 8A 87 48 2F
                        DO D8 73-07 53 BO 01 50 E8 73 01
          AO B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8
076B:0060
          B4 00 B1 05 D3 E0 03 06-B4 2C 89 46 FC 8A 1E B6
976B:0070
```



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Ex. No. 3

#### After execution:

```
Program terminated normally
-d 076b:0000
076B:0000 62 65 67 00 00 00 00 00-00 00 00 00 00 00 00 00
076B:0010 B8 6A 07 8E D8 B8 6B 07-8E C0 8B 0E 00 00 BE 02
                                                                  . j. . . . k. . . . . . . . .
076B:0020 00 BF 00 00 8A 04 26 88-05 46 47 E2 F7 B4 4C CD
                                                                  .....&..FG...L.
                                                                 †.F..F..F...^..
076B:0030   21  8A  46  F9  88  46  F8  FE-46  F9  EB  C9  8A  5E  F8  B7
076B:0040 00 8A 87 48 2F D0 D8 73-17 E8 B6 00 8A 5E F8 B7
                                                                  ...H⁄..s....^.
076B:0050 00 8A 87 48 2F D0 D8 73-07 53 B0 01 50 E8 73 01
                                                                  ...H/..s.S..P.s.
076B:0060   A0 B6 2C 3A 46 F8 74 7E-C7 46 FA 00 00 8A 46 F8
076B:0070 B4 00 B1 05 D3 E0 03 06-B4 2C 89 46 FC 8A 1E B6
                                                                  . . . . . . . . . , .F . . . .
```

#### Result:

Program to move a string from source to destination without using string instruction is assembled, executed and verified.

