Amritha C A

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Enthusiastic and high energy driven software professional, seeking new areas of experience in technical fields and arenas to prove myself as a worthy programmer. Exceptional problem solver with an aptitude for troubleshooting and the ability to quickly master new skills, technology or role.

SYNOPSIS

- Electronics engineer from IPU, experienced in C++ , OOPS and embedded systems; currently associated with Siemens as a Senior Firmware Engineer.
- Applications engineer with an eye for detail whilst achieving deadlines consistently.
- Demonstrated ability in project planning, conceptualization and execution in a result oriented manner.
- Experienced in OOPS, OOD and OOA concepts.
- Experienced in object-oriented programming strategies and methodologies.
- Experienced in design patterns and STL
- Key innovator with algorithmic thinking capabilities.
- Key contributor to organizational objectives, mentoring new recruits and also worked as a competent member of the team under minimal supervision.
- Effective communicator with excellent organizational, analytical and time management skills.
- Worked with closely-knit international cross-functional teams to assist the successful completion of the project.

CORE COMPETENCIES

- Object oriented programming
- Python Scripting
- Debugging
- Google mocks and google test framework.
- Experienced in network debugging and client-server model

- Application-level coding
- Algorithmic coding
- Agile model development
- QACPP Misra compliance tool
- Architecture designing and understanding

IT SKILLS

- Operating System   : Windows95 and above, Linux: Ubuntu
- RTOS   : embOS, **ThreadX**
- Languages   : **C/C++, Python**,
- Technology   : BLE, 4G/ LTE, HDLC,
- Communication Protocol: I2C, Ethernet
- Tools   : **Visual Studio**, Wireshark, Putty, **Eclipse**,J-link , doxygen, QACPP,
- SVN   : TFS, Tortoise Git,
- Processors   : RX71M, RX65N, ATMEL sama5d4x
- IDE   : VS2012, IAR for ARM

EXPERIENCE

SIEMENS

July'21 – Till date : "Hearing aid equipment development project"
- o Siemens client project to develop a high end smart hearing aid.
- o Worked to enable GBTA – General Bluetooth Application to include the latest and optimized code development.
- o Worked on design and implementation of the BT-5.2 stack applications.
- o Worked on the design and implementation of microphone equalizers on the Hearing aid - phone APP interface .
- o Played a key role in understanding and implementing the project with minimum memory constraints.
- o Well experienced in debugging and compiler and linker concepts.
- o Experienced in Google tests and mocks framework.

Tools Used: C++, OOPS, Design Patterns, Python, Jlink, VS Code, GIT, Jenkins, Azure, I2C, UART, SPI, BLE
Major Responsibilities:
- o To design and develop embedded firmware and enhancement in existing functionality. Involvement in Software Development Life-Cycle Activities (requirement gathering, elaboration, solicit clarification, understanding WBS, documentation). Unit Testing, Perform Code Reviews
- o To coordinate with software development team to aid application software development.
- o To coordinate with hardware and validation team during project execution on the issues related with schedule and quality.
- o To coordinate with Project Management Team to ensure projects schedule compliance.
- o Identify the quality KPI's and ensure the compliance at each stage.

Landis + Gyr

Mar'17 – July'21: "Landis+ Gyr Platform Development project"
- o Played a key role in the implementation of the firmware upgrade system for the platform project over the DLMS/ HDLC network using optical port.
- o Helped design, implement, and test a mechanism to transfer huge data over DLMS Worked on the design and implementation of bootloader for the Picasso platform project.
- o Assisted in the designing of display driver code for dot-matrix display. Worked majorly on the application side.
- o Assisted with design patterns in the display application code for better optimization to cover all requirements. Worked on the design and implementation of icons on the display.
- o Firmware activation code improvisation and inclusion of the UBI layer on flash.
- o Played a key role in understanding and implementing the project with minimum flash constraints. Well experienced in IAR debugging and compiler and linker concepts.

Tools Used: C++, OOPS, Design Patterns, Python , Jlink, Eclipse,Gerrit, Jenkins,TFS, ThreadX, Tortoise git, XML, Cosem, I2C, DMT, UART, SPI

Aug'14 – Mar'17 " S-560 : A gateway project for the German market(BSI)"
Key Deliverables:
o   Worked on tariffication and tariffication infrastructure development for the project. Also played a major part in the python infrastructure development for the same
o   Worked on modem application level coding(C++) including AT commands.
o   Updated the network handling to include connection timeouts.

Tools Used: C++, Python, Visual Studio, TFS, SVN, Modem, IAR, gcc, I2C, REST API, XML, TCP/IP, Data Structures

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TRAININGS/WORKSHOPS

- **Visited Landis + GYR Headquarters in Zug, Switzerland for training and knowledge transfer for Platform Picasso project for two weeks during June'2018**
- Attended training on Switching Technology, Introduction to the Network Technology and Basics of Networking .

COURSES

 Oct'12 – May'17 Completed a diploma course:
 Engineers Employability Enhancement Program on VLSI +HDL (Hardware Description Languages)

ACADEMIA

2014   B.Tech(Electronics and Communication) from IP University with 82 % aggregate
2010   SSC from Mount Carmel School, Anand Niketan with 88.4% aggregate
2008   HSC from Kerala Education Society School, R K Puram with 92% aggregate

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AWARDS AND RECOGNITION

- **Received Technical excellence award in 2018**
- **Received Star award in 2016**
- **Received Gem award in 2015**