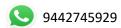
ANANTH S

Senior System Engineer





ananthsaravanan1996@gmail.com



About me

Senior Embedded developer and Tester, with 5+ Years of experience in embedded software development life cycle.

Worked in process like AGILE, ASPICE V (SWE2 -SWE5) model.

Domain Knowledge in Tuners and Radio bands like AM, FM, DAB, DRM.

Management tools like JIRA, RTC, Eclipse.

Expertise in software development for Radio middleware, application and HAL.

Expertise in Software testing like Unit testing, Integration Testing, Functional Testing, Sanity testing.

Work Experience

Graduation

Passed Year

5+ Years

Master of Technology in Sensor System Technology,

VIT, Vellore.

Apr 2017- Apr 2019

Bachelor of Engineering in Mechatronics Engineering,

K S Rangasamy College of Technology,

Namakkal.

Apr 2013- Apr 2017

Skills and Tools

- C
- C++
- Visual Studio
- GHS
- JIRA
- Eclipse
- RTC
- Tuner Diagnostics
- Redwood Generator
- CAN, SPI, I2C, UART
- Canoe
- Canalyser

- Ventus 2.0
- MS Office
- MS Excel
- Audacity
- SDR Play
- Salae
- Beagle
- Oscilloscope
- R&S signal generator
- Renesas E1,
 E2
- Lauterbach Debugger

- Enterprise Architect
- SVN
- Jenkins
- UDL
- ODST
- Para soft
- Unit Testing
- Functional Testing
- Sanity Testing
- Regression Testing
- Stress Testing

Career



Internship Trainee Jasmin Infotech Pvt Itd Chennai



Work Experience

Project Details #1

- Porting of NXP tuner for DAB Band

The main objective of the project is to port the driver functionalities used in NXP Saturn and pns30d tuner to NXP Mercury and radion s tuners and have to derive the use cases based on the software and system requirements Mainly worked on HAL layer and application layer for communication between tuner and application framework.

Roles	Design, Develop, Integrate, Defect analysis and fix
Responsibilities	Lead the design and development of Tuner Component in IVI Infotainment.
	Develop and implement software applications.
	Lead code reviews and ensure high-quality code standard through static analysis tools
	Develop and execute testing and debugging plans
	Identify and resolve bugs and fixing it.
	Ensure compliance with industry standards and regulations
Skills	C, C++, static analysis (Compiler, Coverity warnings), Testing (Unit testing, debugging, Functional testing, regression testing, Stress testing), Integration activities (GIT).
Hardware Skills	Oscilloscope, Emulator JTAG, Renesas E1, E2, Signal generators (Redwood, R&S, Multi FM, Lumantec Ventus 2.0, IZT, HIFI FMT), Beagle, Salae

Project Details #2

- Radio Control Middleware development

I am part of creating Radio control middleware stack which is integrated in real-time NXP tuners controlled through Microprocessors. SPI and I2C based protocol for communication between Application layer and abstraction Layer. the Scope of this project is Handling the Radio controls like (Seek, Tune, Scan) in Application layer for Automotive infotainment system in European cars.

Roles	Design, Develop, Defect analysis and fix
Responsibilities	Lead the design of Tuner applications in IVI Infotainment.
	Develop and implement software applications.
	Lead code reviews and ensure high-quality code standard through static analysis tools.
	Identify and resolve bugs and fixing it.
	Ensure compliance with industry standards and regulations.
Skills	C, static analysis (Compiler, Coverity warnings), Testing (Unit testing, debugging), Integration activities (GIT). Linux
Hardware Skills	Signal generators (Multi FM, Lumantec Ventus 2.0, IZT), Beagle, Salae

Project Details #3

- DRM SDR Library framework development

I had worked on Library framework development for global radio transmitter and receiver (DRM) and also developed

source code for it. Worked on static analysis tool for source code violations.

Roles	Static analysis and unit testing
Responsibilities	Ensure high-quality code standard through static analysis tools.
	Identify and resolve bugs and fixing it.
	Ensure compliance with industry standards and regulations.
Skills	C, static analysis (Compiler, Coverity warnings), Testing (Unit testing, debugging), Integration activities (GIT). Linux
Hardware Skills	Signal generators (SDR Play, IZT)

Project Details #4

- Automotive Radio communication development

Worked for an OEM for Automotive Infotainment Radio middleware development, unit testing and functional testing and integration activities.

Roles	Development, Static analysis, unit testing and Functional Testing
Responsibilities	Ensure high-quality code standard through static analysis tools.
	Identify and resolve bugs and fixing it.
	Ensure all the functional test cases were passed on test case sheet.
Skills	C, Static analysis (Compiler, Coverity warnings), Testing (Unit testing, debugging), Integration activities (GIT).
Hardware Skills	Signal generators (SDR Play, IZT)

Project Details #5

- Smart Health monitoring for cardiac patients

I had done a feasible smart watch with high accuracy in monitoring the live heart condition of heart patients through Deep learning techniques and published my paper in International Journal.

Personal Details

Birthday: January 08, 1996 Nationality: Indian Language: English/Tamil/Telugu Gender: P7966225 Resident: Chennai, India