

Abhishek Jadimath S (B.E., M.Tech.)

<https://www.linkedin.com/in/abhishek-jadimath-3a3b3399/>

Email: abhijadimath@gmail.com

Ph.: +919632435156

An Automotive Electronics (M.Tech) graduate from SJCE, Mysuru with 5 years and 7 months of experience in Powertrain development and testing in automotive industry with hands on experience on model based development along with manual and automation testing. Seeking a better personal and professional life.

Technical Summary:

1. Senior Engineer (April 2021 – Present)

Stellantis India, Chennai

- Hands on experience on MATLAB/Simulink, Vector CANoe/CANalyzer, ETAS INCA, dSPACE ControlDesk and PROVEtech.
- Exposed to the full life cycle of Software Development & Testing.
- Experience in manual and automation testing using automotive tools.
- Verification on HIL systems using Provetechn and dSPACE tools.
- Conducting training to new team members on the domain knowledge and testing activities.

2. Engineer (July 2018- March 2021)

Great Wall India R & D Pvt. Ltd, Bengaluru.

- Hands on experience on MATLAB/Simulink, Vector CANoe/CANalyzer, ETAS INCA, SILVER, MTest, dSPACE ControlDesk.
- Exposed to the full life cycle of Software Development & Testing.
- Experience in manual and automation testing using automotive tools.
- Automation using Python and CAPL.
- Test Methodologies, including writing/reviewing test cases, execution, defect tracking, test documentation.
- Knowledge and experience in Unit testing, Interface Testing and System Testing (HIL setup).
- Working experience in CAN, UDS and KWP2000 protocol.
- Hands on experience over ECU flashing and Debug tools.
- Conducting training to new team members on the domain knowledge and testing activities.

3. Research Intern (Feb 2017- Nov 2017)

Automotive Control Systems Lab, SJCE, Mysuru.

- Study, installation and maintenance of dSPACE tools for Automotive control systems.
- Authored and presented paper on Control Design for Wiper Motor using Resistive Based Rain Sensor and dSPACE tools at CISCON 2017, MIT, Manipal. The paper was published in STM Journal, Volume 4 Number 3.
- Co-Authored a paper on Design and Development of Controller for Bosch 4.2 Oxygen sensor using dSPACE tools at IEEE international conference in GSSSIET, Mysuru. The paper was published in IEEE Xplore.

Project responsibilities:

1. STELLANTIS:

➤ Conventional Programs

- Flashing and debugging of ECU SW with PROF.
- writing/reviewing test cases based on the requirements.
- Execution on HIL system.
- Defect tracking and test documentation.
- SRT (Software Road Readiness Test).

2. Great Wall India R & D Pvt. Ltd

➤ HCU - 7DCT, 48vBSG (Mild Hybrid)

- Model design based on OILs and SRS documents with MIL and bench testing.
- ECU hardware setup and Harness connection debug and fixing.
- Flashing and debugging of ECU SW with PROF and debugger (Trace32).
- FOTA CAPL Scripting, C++ and Python interface for Test Automation.
- CAN interface testing, OBD and UDS services Functional Testing, EOL Testing.

➤ HCU – P2 and P2P4 (PHEV)

- CAN interface testing and Component test using bench setup.
- ECU hardware setup and Harness connection debug and fixing.
- Flashing and debugging of ECU SW with PROF and debugger (UDE).
- writing/reviewing test cases, execution, defect tracking, test documentation for HIL testing.

➤ HEV – DHT (Full Hybrid)

- Model design analysis based on OILs and SRS documents.
- Writing/reviewing test cases, execution, defect tracking, test documentation for MIL testing.

Skills & Tools:

C++, C, MATLAB, Simulink, CANalyzer, CANape, CANoe, ETAS INCA, TRACE32, UDE, CAN, UDS, dSPACE Control Desk, Python.

Education:

- Sri Jayachamarajendra College of Engineering 2015-2017
Major: **Autotmoive Electronics**
Degree: Masters
CGPA: 9.19/10
- Visvesvarya Technological University, India 2010-2014
Major: **Electronics and Communication Engineering**
Degree: Bachelor ECE
Agg.: 61% (3.00 GPA)

References:

I Dr. Sudarshan Patil Kulkarni
II Sanjay Shenoy

Mentor & Professor (SJCE)
Module Lead (GWM)

pk.sudarshan@gmail.com
+919663654497