

Anil kumar

Ph.no: +91 93927 66820

Email ID: anilkumar.pallagani4568@gmail.com

Career Objective:

To contribute to the organizational goals and advancement of the company while enhancing my personality and potential in IT sector.

PROFESSIONAL SUMMARY

- Total **5.4 years** of relevant experience in Verification and Validation in Embedded Software Safety Critical Systems in **Automotive** and **Locomotive**.
- **1.4 years** of relevant experience in **Automotive**.
- Having hands on experience on **CAN, UDS, Canoe & CANalyzer**.
- Having Hands on experience in **HIL** Testing.
- Expertise in **Requirement Based Testing**.
- Good Knowledge on **Advanced Driver Assistance System** Features.
- Experience in various software engineering activities like Requirement Analysis, Test case, Test case development, Validation and defect analysis.
- Good Experience on **CAPL** Scripting Programming.
- Preparing Daily Status report and Involved in the team interactions for problem solving and sprint meetings.
- Good understanding of Test case Design Techniques.
- Expertise in MC/DC (Modified Condition Decision Coverage).
- Hands on experience in performing **Verification & Validation**.
- Ability to prioritize tasks, handles multiple tasks, and meets deadlines.
- Good experience with STLC in compliance with **ASPICE** and **EN 50128**
- Good understanding in **CAN protocol**.
- Possess basic knowledge on **CBTC** systems.

ORGANIZATIONAL EXPERIENCE

- Employee of Alten india technology and currently working as Verification & validation Engineer since December 2022 to March 2024.
- Previously worked as Test Engineer in TVM signalling and transportation systems pvt. Ltd Bangalore, from October 2018 to November 2022

TECHNICAL SKILLS

- **Programming** : Basic Python and CAPL scripting
- **Domain** : Railway and Automotive.
- **Tools** : vTeststudio, CANoe, Doors, clearquest, Lascomm AEC, Trace32, EMIL(MBD), JIRA, Vectorcast.

EDUCATIONAL QUALIFICATION

- **B.Tech** in Electronics and Communication Engineering with an aggregate of 7.22 cgpa from Lakireddy balireddy college of engineering, Mylavaram in the year 2018.
- **Diploma** in Electronics and Communication Engineering with an aggregate of 73.4 percent from AANM & VVRSR Polytechnic, Gudlavalleru in the year 2015.

Project 1: SYSTEM TESTING VISION PROJECT - CAMERA

Role : Verification and Validation Engineer
Client : VALEO
Tools : vTeststudio, CANoe, Doors, JIRA, Trace32.

Description:

Autonomous Driving is role is emerging and increase the safety of passengers/customers. Here we test diagnostic testing and functional testing. (And here the microcontroller will detect signals and give to moibileyeq and the mobileequ will change the signals into images and the camnera will able to see the images) and also will check proper transmission and reception of signals between the camera ECUs and other nodes not.

Roles & Responsibilities:

Requirement documents are shared on the beginning day of the Sprint cycle

- Analyze and discuss the requirements with the team members to understand what needs to be tested and how it is to be tested.
- Based on above analysis, development / updating of test cases are carried out in the automation test suite
- Test data for Functional and Regression testing is created.
- Estimate test efforts, prioritize testing activities and communicate status to Lead on a regular basis.
- Execute all the test cases and analyze the results (Passed/Failed). Based on the analysis, update test cases with necessary information and re-execute.
- If the failure is consistent, the issue is reported by defining severity and priority to onsite team and corresponding action is taken according to the Business requirements.
- Record test results (Passed / Failed) of executed test cases.
- Generate the report based on execution criteria. If any test case is failed, failure comments are mentioned against it in the summary report

Role: Team Member.

Project 2: TAILGATE ECU

Client	: VALEO
Role	: Verification and Validation Engineer
Tools	: vTeststudio, CANoe, Doors, JIRA,Trace32

Description:

the project mainly involves in tailgate ecu working or not even though we created faults in the tailgate ecu still the ecu should able to work and also we will check the diagnostics of the ECU and we will check the nodes of other signals.

Roles & Responsibilities:

- Collecting requirements from the requirement tool door
- Understanding and analyzing the requirement documents of the tailgate
- Prepare the test plan and update the test cases according to requirements.
- Reviewing of the test plans developed by the test engineers.
- Create panels for the signals using pannel designer → if you are known how to design pannel using inuput and output box in pannel please put this step or leave this step
- Testing of the feature functionalities of tail gate is opening and closing using can signals

- Prepare the test reports by updating with the overall results in terms of number of passed and failed cases with appropriate comments and observations.
- Involved in Regression testing of different features.

Role: Team Member.

Project 3: U500 fluence

Client	: ALSTOM
Role	: Verification and Validation Engineer
Tools	: Doors, EMIL, JIRA,

Description:

the project mainly involves U500 sub systems and the artifact models working of train functions. Performed verification of models against system requirements and their functionality using MBSE techniques and system testing techniques.

Responsibilities:

- Verification of train control system requirements against EMIL tool.
- Performed system testing for train models against requirements.
- Performing requirement verification in model based applications.
- Launching IA for impacted files
- Review of artifacts before delivering to the customer
- Verification of atomic, functional and IO blocks of EMIL tool
- Test cases, test procedures and test execution
- Creation of test environment setup
- Configuring Test cases and Test procedures into SVN
- Review of test cases and test procedure

Project 4: EI signalling system

Client : Kyosan japan
Tools : Doors, Microsoft office, Multimeter.
Role : Signalling testing engineer

Responsibilities:

- Conducting Factory Acceptance Test (FAT) covering functional test, control table test, cross table test and aspect control chart test for the Signal Interlocking Designs with specified documentations for preservation.
- Performed Electronic interlocking end to end Product testing and preparation of test reports.
- Evaluation and adjustments of power supplies as per circuits and verification using multimeter.
- Conducting Site Acceptance Test (SAT) covering Panel Simulation and functional Test, Bell-Test, Wire and Null Count check, Negative Test for the Signal Interlocking Design and Installation with specified documentations for preservation.
- Performing Fault Mode Test Evaluation and Analysis (FMEA) for the reliable performance of the SSI System and providing Action List during.
- Responsible for providing functional solution and Monitoring of the design of various sub-systems (Automatic Train Control Sub-system, Automatic Train Supervision Sub-system and Central Interlocking Sub-system, Maintenance).
- Verification and Validation of TCMS subsystem - Worked in System Specifications, Verification and Validation, Testing, commissioning and Troubleshooting of Metro projects.
- Hardware in loop (HIL) testing for different input and output vital and non vital cards.

Project 5: Embedded QA-RTD(Replay tracking device)

Client : Sandcube analytics pvt ltd
Tools : JIRA, Doors, Microsoft office
Role : Engineer- QA

Responsibilities:

- Collaborated with the Development and Product Management team to understand customer requirements.
- Responsible for designing and executing test plans, test cases and test scripts to uncover, identify and document software problems and their causes. Assisted a team to maintain quality of the system.
- Involved in different testing such as functional testing, unit testing, software, and hardware integration testing, regression testing and retesting.
- Involved in the software code review and software quality review.
- Responsible for technical reviews, walkthrough, and software inspection.
- Responsible to build and maintain traceability matrix to map the test cases against the requirements by using IBM DOORS next generation requirement management tool.

Personal Information:

Name : Anil kumar pallagani
Nationality : Indian
Gender : Male
Languages : English, Hindi and Telugu
Permanent Details : Gannavaram (T), Krishna (D), Andhra Pradesh-521101

I hereby declare that all the information provided above is true to the best of my knowledge.

Place: Bangalore

Anil kumar